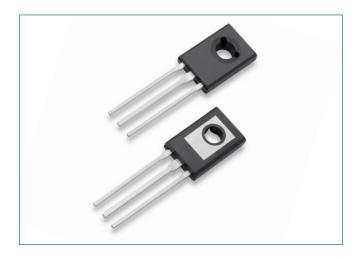


## C106 Series





#### **Description**

Glassivated PNPN devices designed for high volume consumer applications such as temperature, light, and speed control; process and remote control, and warning systems where reliability of operation is important.

#### **Features**

- Glassivated Surface for Reliability and Uniformity
- Power Rated at Economical Prices
- Practical Level Triggering and Holding Characteristics
- Flat, Rugged, Thermopad Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Sensitive Gate Triggering
- These are Pb-Free Devices

## **Pin Out**



## **Functional Diagram**



## Additional Information







Samples

# **Thyristors** Surface Mount > 200 - 600V > C106 Series

#### Maximum Ratings (T<sub>1</sub> = 25°C unless otherwise noted)

Rating		Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Sine Wave, 50–60 Hz, RGK = 1 K, TC = -40° to 110°C)	C106B C106D, C106D1* C106M	V <sub>DRM</sub> ,	200 400 600	V
On-State RMS Current (180° Conduction Angles, TC = 80°C)		I <sub>T (RMS)</sub>	4.0	А
Average On–State Current (180° Conduction Angles, T <sub>c</sub> = 80°C)		I <sub>T(AV)</sub>	2.55	А
Peak Non-Repetitive Surge Current (1/2 Cycle, Sine Wave, 60 Hz, $T_J = +25^{\circ}$ C)	I <sub>TSM</sub>	20	А	
Circuit Fusing Considerations (t = 8.3 ms)	l²t	1.65	A2s	
Forward Peak Gate Current (Pulse Width 1.0 sec, TC = 80°C)	I <sub>GM</sub>	0.2	А	
Forward Peak Gate Power (Pulse Width ≤ 1.0 µsec, T <sub>C</sub> = 80°C)	P <sub>GM</sub>	0.5	W	
Forward Average Gate Power (Pulse Width $\leq$ 1.0 $\mu$ sec, $T_{C}$ = 80°C)		P <sub>G(AV)</sub>	0.1	W
Operating Junction Temperature Range		T <sub>J</sub>	-40 to +110	°C
Storage Temperature Range		T <sub>stg</sub>	-40 to +150	°C
Mounting Torque (Note 2)	_	6.0	in. lb.	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the

#### **Thermal Characteristics**

Rating		Symbol	Value	Unit
Thermal Resistance,	Junction-to-Case (AC) Junction-to-Ambient	R <sub>ejc</sub> R <sub>eja</sub>	3.0 75	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		T <sub>L</sub>	260	°C

## **Electrical Characteristics** - **OFF** $(T_J = 25^{\circ}C \text{ unless otherwise noted})$

Characteristic		Symbol	Min	Тур	Max	Unit
Peak Repetitive Forward or Reverse Blocking Current	T <sub>J</sub> = 25°C		_	_	10	μΑ
$(V_{AK} = Rated V_{DRM} \text{ or } V_{RRM}, R_{GK} = 1 \Omega k)$	T <sub>J</sub> = 110°C	DRM' RRM	-	-	100	μA

#### **Electrical Characteristics - ON** (T<sub>J</sub> = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
Peak Forward On-State Voltage (Note 3) (I <sub>TM</sub> = 4 A)		V <sub>TM</sub>	-	-	2.2	V
Gate Trigger Current (Continuous dc)	$T_J = 25^{\circ}C$		_	15	200	
$(V_D = 12 \text{ V}, R_L = 100 \Omega, \text{All Quadrants})$	T <sub>J</sub> = -40°C	GT	-	35	500	μΑ
Peak Reverse Gate Voltage ( $I_{GR} = 10 \mu A$ )		V <sub>GRM</sub>	_	-	6.0	V
Gate Trigger Voltage (Continuous dc)	$T_J = 25^{\circ}C$	.,,	0.4	0.60	0.8	V
$(V_D = 12 \text{ Vdc}, R_L = 100 \Omega, T_C = 25^{\circ}\text{C})$	T <sub>J</sub> = -40°C	V <sub>GT</sub>	0.5	0.75	1.0	
Gate Non-Trigger Voltage (Continuous dc) (Note 4) (V <sub>AK</sub> = 12 V, R <sub>L</sub> = 100 (VAK = 12 V, RL = 100 , TJ = 110°C), T <sub>J</sub> = 110°C)		$V_{\sf GD}$	0.2	_	_	V
Latching Current $T_J = 25$			_	0.20	5.0	A
$(V_{AK} = 12 \text{ V}, I_{G} = 20 \text{ mA}, R_{GK} = 1 \text{ k}\Omega)$	T <sub>J</sub> = -40°C	'L	_	0.35	7.0	mA
Holding Current	T <sub>J</sub> = 25°C		_	0.19	3.0	
$(V_D = 12 \text{ Vdc})$ (Initiating Current = 20 mA, $R_{GK} = 1 \text{ k}\Omega$ )	T <sub>J</sub> = -40°C	I <sub>H</sub>	_	0.33	6.0	mA
	$T_J = +110^{\circ}C$		_	0.07	2.0	

Recommended Operating Conditions may affect device reliability.

1. V<sub>DRM</sub> and V<sub>BRM</sub> for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

<sup>2.</sup> Torque rating applies with use of torque washer (Shakeproof WD19523 or equivalent). Mounting Torque in excess of 6 in. lb. does not appreciably lower case-to-sink thermal resistance. Main terminal 2 and heat-sink contact pad are common.

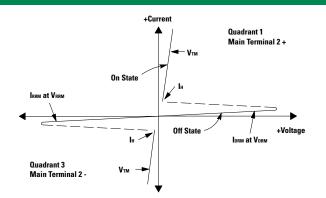
## **Dynamic Characteristics**

Characteristic	Symbol	Min	Тур	Max	Unit
Critical Rate-of-Rise of Off State Voltage ( $V_{AK}$ = Rated $V_{DRM}$ , Exponential Waveform, $R_{GK}$ = $1k\Omega$ , $T_{J}$ = 110°C)	dv/dt	-	8.0	_	V/µs

<sup>3.</sup> Pulse Test: Pulse Width  $\leq$  2.0 ms, Duty Cycle  $\leq$  2%.

## **Voltage Current Characteristic of SCR**

Symbol	Parameter	
V <sub>DRM</sub>	Peak Repetitive Forward Off State Voltage	
I <sub>DRM</sub>	Peak Forward Blocking Current	
V <sub>RRM</sub>	Peak Repetitive Reverse Off State Voltage	
I <sub>RRM</sub>	Peak Reverse Blocking Current	
V <sub>TM</sub>	Maximum On State Voltage	
I <sub>H</sub>	Holding Current	



**<sup>4.</sup>**  $R_{\text{GK}}$  is not included in measurement.



**Figure 1. Average Current Derating** 

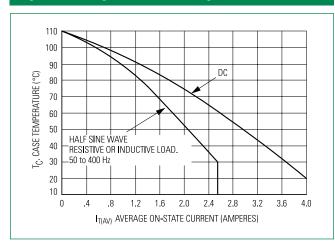


Figure 2. Maximum On-State Power Dissipation

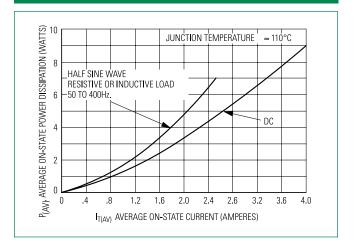


Figure 3. Typical Gate Trigger Current vs. Junction Temp

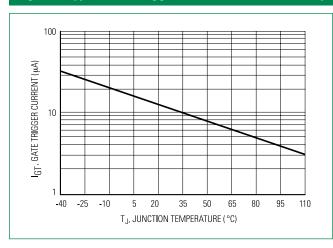


Figure 4. Typical Holding Current vs. Junction Temp

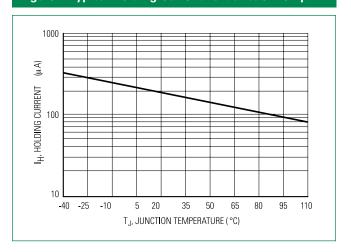


Figure 5. Typical Gate Trigger Voltage vs. Junction Temp

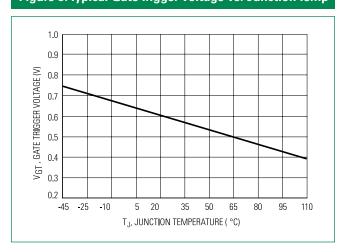
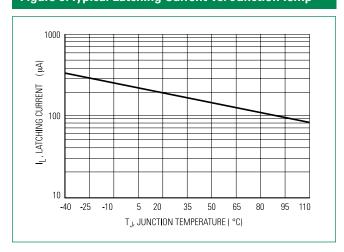
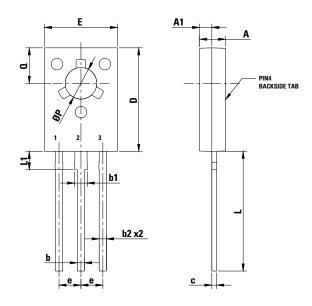


Figure 5. Typical Latching Current vs. Junction Temp

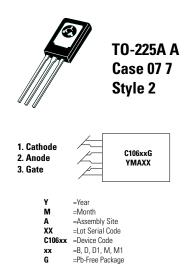




#### **Dimensions**



## **Part Marking System**



Dim	Inches		Millin	neters
Dim	Min	Max	Min	Max
Α	0.102	0.110	2.60	2.80
A1	0.047	0.055	1.20	1.40
b	0.028	0.034	0.70	0.86
b2	0.028	0.034	0.70	0.86
С	0.019	0.022	0.49	0.57
D	0.417	0.449	10.60	11.40
E	0.291	0.323	7.40	8.20
е	0.090 TYP		2.29 TYP	
L	0.551	0.630	14.00	16.00
L1	0.091	0.106	2.30	2.70
Р	0.118	0.134	3.00	3.40
Q	0.142	0.157	3.60	4.00
b1	0.047	0.055	1.2	1.4

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- **3.** 077-01 THRU -08 OBSOLETE, NEW STANDARD 077-09.

Pin Assignment			
1	Cathode		
2	Anode		
3	Gate		

#### **Ordering Information**

Device	Package	Shipping
C106BG		
C106DG		
C106D1G*	TO225AA (Pb-Free)	2500 Units/Box
C106MG		
C106M1G*		
C106MTG		60 Units/Tube 1920 Units/Box

<sup>\*</sup>D1 signifies European equivalent for D suffix and M1 signifies European equivalent for M suffix.

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# Littelfuse:

C106D1G C106BG C106M1G C106MG C106DG