

# RGT50NS65D

## 650V 25A Field Stop Trench IGBT

V <sub>CES</sub>	650V
I <sub>C(100°C)</sub>	25A
V <sub>CE(sat) (Typ.)</sub>	1.65V
$P_D$	194W

#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Low Switching Loss
- 3) Short Circuit Withstand Time 5µs
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating; RoHS Compliant

### Applications

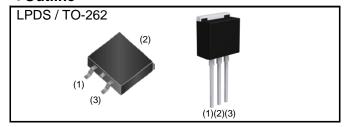
General Inverter

**UPS** 

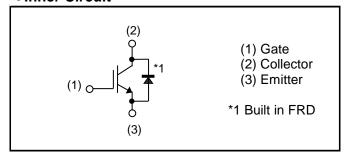
**Power Conditioner** 

Welder

#### Outline



#### ●Inner Circuit



Packaging Specifications

	Packaging	Taping / Tube
	Reel Size (mm)	330 / -
Typo	Tape Width (mm)	24 / -
Type	Basic Ordering Unit (pcs)	1,000 / 1,000
	Packing Code	TL / C9
	Marking	RGT50NS65D

## ● Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		$V_{CES}$	650	V
Gate - Emitter Voltage		$V_{GES}$	±30	V
Callactor Current	T <sub>C</sub> = 25°C	I <sub>C</sub>	48	А
Collector Current	T <sub>C</sub> = 100°C	I <sub>C</sub>	25	А
Pulsed Collector Current		I <sub>CP</sub> *1 75		А
	T <sub>C</sub> = 25°C	I <sub>F</sub>	35	А
Diode Forward Current	T <sub>C</sub> = 100°C	I <sub>F</sub>	20	А
Diode Pulsed Forward Current		I <sub>FP</sub> *1	75	А
	T <sub>C</sub> = 25°C	P <sub>D</sub>	194	W
Power Dissipation	T <sub>C</sub> = 100°C	P <sub>D</sub>	97	W
Operating Junction Temperature		T <sub>j</sub>	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C

<sup>\*1</sup> Pulse width limited by T<sub>jmax.</sub>

### ●Thermal Resistance

Parameter	Symbol	Values			Linit
Farameter		Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.77	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	2.12	°C/W

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
r ai ai nietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	$I_C = 10 \mu A, V_{GE} = 0 V$	650	1	1	V
Collector Cut - off Current	I <sub>CES</sub>	$V_{CE} = 650V, V_{GE} = 0V$	ı	1	10	μΑ
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, \ V_{CE} = 0V$	ı	•	±200	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 17.5 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_C = 25A, V_{GE} = 15V$ $T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	-	1.65 2.15	2.1 -	V

# ●IGBT Electrical Characteristics (at T<sub>j</sub> = 25°C unless otherwise specified)

Darameter	Symbol	Conditions		Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V	-	1400	-	
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$	-	56	-	pF
Reverse Transfer Capacitance	$C_{res}$	f = 1MHz	-	22	-	
Total Gate Charge	Q <sub>g</sub>	V <sub>CE</sub> = 300V	-	49	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 25A	-	15	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	19	-	
Turn - on Delay Time	t <sub>d(on)</sub>	$I_C = 25A, V_{CC} = 400V$	-	27	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_G = 10\Omega$	-	32	-	ns
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 25°C	-	88	-	
Fall Time	t <sub>f</sub>	Inductive Load	-	65	-	
Turn - on Delay Time	t <sub>d(on)</sub>	$I_C = 25A, V_{CC} = 400V$	-	28	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_{G} = 10\Omega$	-	37	-	20
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 175°C	-	100	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	110	-	
		$I_C = 75A, V_{CC} = 520V$				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650 V, V_{GE} = 15 V$	FU	LL SQUA	RE	-
		$R_G = 50\Omega, T_j = 175^{\circ}C$				
		$V_{CC} \le 360V$				
Short Circuit Withstand Time	t <sub>sc</sub>	V <sub>GE</sub> = 15V	5	-	-	μs
		T <sub>j</sub> = 25°C				

# **•FRD Electrical Characteristics** (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			l lmi4
			Min.	Тур.	Max.	Unit
		I <sub>F</sub> = 20A				
Diode Forward Voltage	$V_{F}$	$T_j = 25^{\circ}C$	-	1.45	1.9	V
		T <sub>j</sub> = 175°C	-	1.25	-	
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A	-	58	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>	$V_{CC} = 400V$ $di_F/dt = 200A/\mu s$	-	6.3	-	А
Diode Reverse Recovery Charge	$Q_{rr}$	T <sub>j</sub> = 25°C	-	0.20	-	μC
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A	-	256	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>	$V_{CC} = 400V$ $di_F/dt = 200A/\mu s$	-	10.4	-	Α
Diode Reverse Recovery Charge	$Q_{rr}$	T <sub>j</sub> = 175°C	-	1.35	-	μC

#### • Electrical Characteristic Curves

Fig.1 Power Dissipation vs. Case Temperature

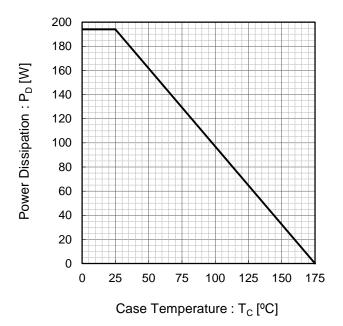


Fig.2 Collector Current vs. Case Temperature

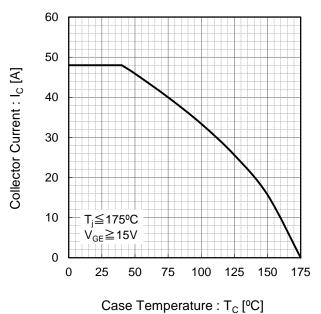


Fig.3 Forward Bias Safe Operating Area

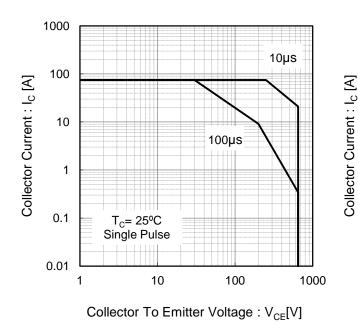
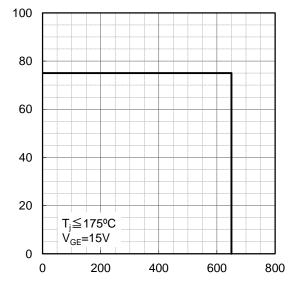


Fig.4 Reverse Bias Safe Operating Area



Collector To Emitter Voltage :  $V_{CE}[V]$ 

#### **•**Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

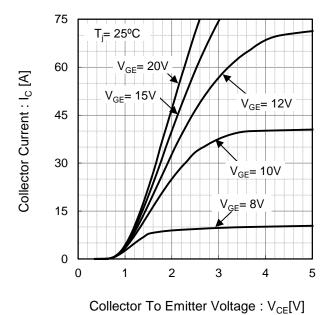
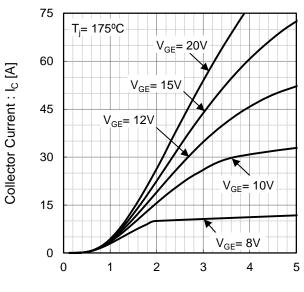


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage: V<sub>CE</sub>[V]

Fig.7 Typical Transfer Characteristics

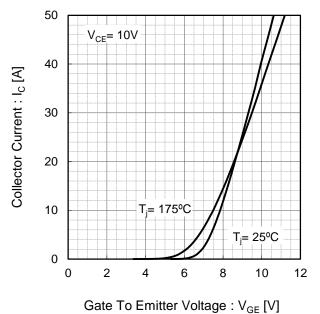
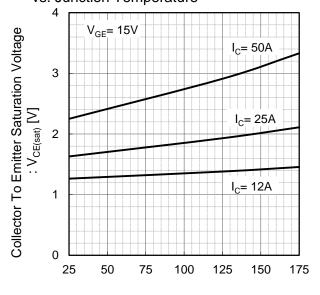


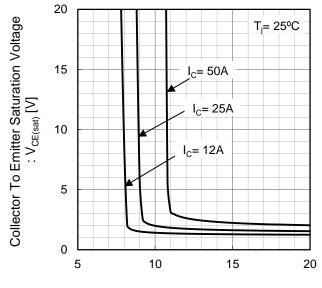
Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



Junction Temperature : T<sub>i</sub> [°C]

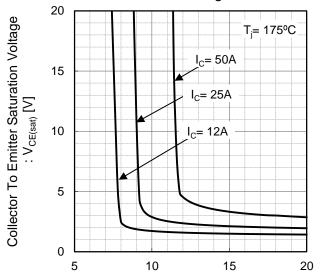
#### **•**Electrical Characteristic Curves

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate To Emitter Voltage: V<sub>GE</sub> [V]

Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate To Emitter Voltage: V<sub>GE</sub> [V]

Fig.11 Typical Switching Time vs. Collector Current

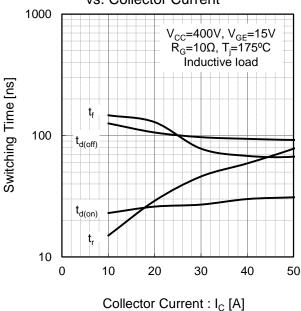
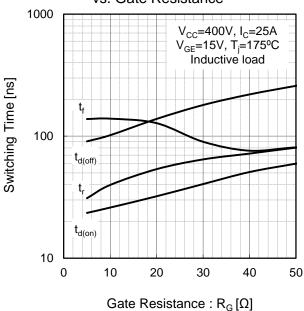


Fig.12 Typical Switching Time vs. Gate Resistance



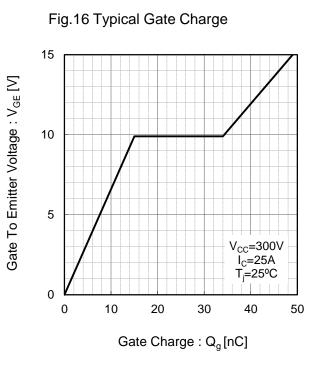
#### • Electrical Characteristic Curves

Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 0.1  $\mathsf{E}_{\mathsf{on}}$  $V_{CC}$ =400V,  $V_{GE}$ =15V R<sub>G</sub>=10 $\Omega$ , T<sub>j</sub>=175°C Inductive load 0.01 0 10 30 40 20 50 Collector Current : I<sub>C</sub> [A]

vs. Gate Resistance 10 Switching Energy Losses [mJ]  $\mathsf{E}_{\mathsf{off}}$ 1  $E_{on}$ 0.1 V<sub>CC</sub>=400V, I<sub>C</sub>=25A V<sub>GE</sub>=15V, T<sub>j</sub>=175°C Inductive load 0.01 0 10 20 30 40 50 Gate Resistance :  $R_G[\Omega]$ 

Fig.14 Typical Switching Energy Losses

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz V<sub>GE</sub>=0V T<sub>i</sub>=25°C 1 0.01 0.1 10 100 Collector To Emitter Voltage : V<sub>CE</sub>[V]



#### • Electrical Characteristic Curves

Fig.17 Typical Diode Forward Current vs. Forward Voltage 75 60 Forward Current : I<sub>F</sub> [A] 45 30 = 175°C 15 T<sub>i</sub>= 25°C 0 1.5 2 2.5 3 0 0.5

Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current 400  $V_{CC}$ =400V di<sub>F</sub>/dt=200A/µs Reverse Recovery Time: t<sub>rr</sub> [ns] Inductive load 300 T<sub>i</sub>= 175°C 200 100 T<sub>i</sub>= 25°C 0 0 10 20 30 50 Forward Current : I<sub>F</sub> [A]

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

Forward Voltage: V<sub>F</sub>[V]

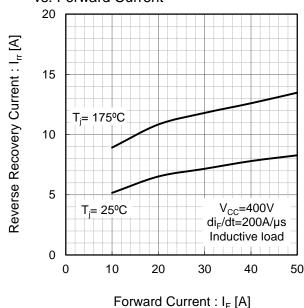
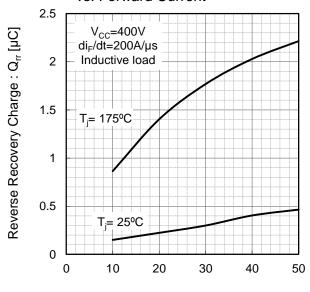


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current



Forward Current : I<sub>F</sub> [A]

### **•**Electrical Characteristic Curves

Fig.21 IGBT Transient Thermal Impedance

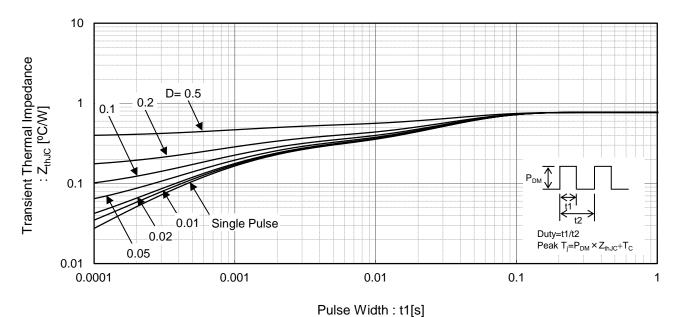
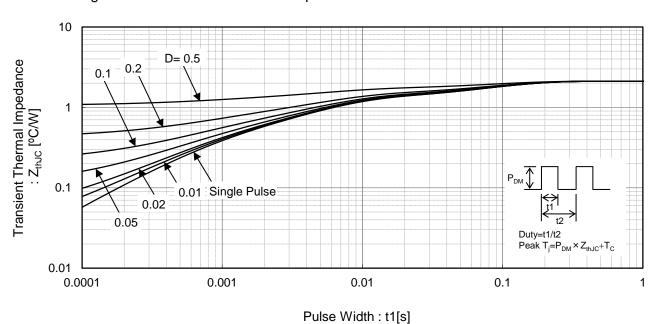


Fig.22 Diode Transient Thermal Impedance



## ●Inductive Load Switching Circuit and Waveform

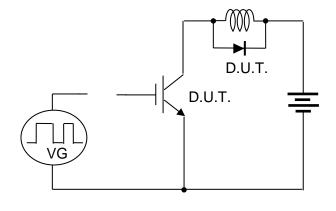


Fig.23 Inductive Load Circuit

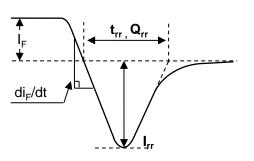


Fig.25 Diode Reverce Recovery Waveform

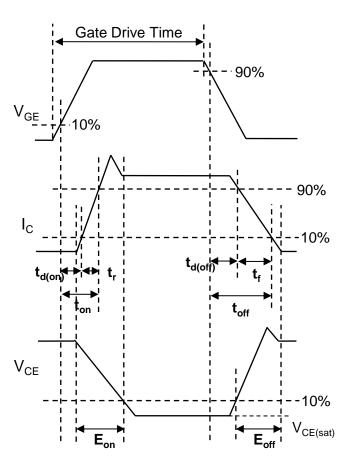


Fig.24 Inductive Load Waveform

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