

Features

- RoHS compliant*
- Protects up to four I/O ports
- Bidirectional configuration
- ESD protection
- Low capacitance: 6 pF

Applications

- Ethernet 10/100/1000 Base T
- Personal digital assistants
- Handheld electronics
- Cellular phones
- Video cards

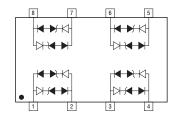
CDNBS08-SLVU2.8-8 - Low Capacitance TVS Array

General Information

The markets of portable communications, computing and video equipment are challenging the semiconductor industry to develop increasingly smaller electronic components.

Bourns offers Transient Voltage Suppressor Array combination diodes for surge and ESD protection applications in an eight lead narrow body SOIC package size format. Bourns® Chip Diodes conform to JEDEC standards, are easy to handle on standard pick and place equipment and their flat configuration minimizes roll away.

The Bourns® device will meet IEC 61000-4-2 (ESD), IEC 61000-4-4 (EFT) and IEC 61000-4-5 (Surge) requirements.



Electrical Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Parameter		Symbol	Min.	Nom.	Max.	Unit
Peak Pulse Current (t _p = 8/20 μs)		I _{PP}			30	Α
Peak Pulse Power (t _p = 8/20 μs) ¹		P _{PP}			600	W
Working Voltage		VwM			2.8	V
Breakdown Voltage @ 1 mA		V _{BR}	3.0			V
Leakage Current @ V _{WM}		I _D			1.0	μΑ
Capacitance @ 0 V, 1 MHz		С		6		pF
Snapback Voltage @ 50 mA			2.8			V
ESD Protection per IEC 61000-4-2 Contact Discharge Air Discharge		ESD	± 8 ±15			kV
EFT Protection per IEC 61000-4-4 @ 5/50 ns		EFT			60	А
Surge Protection per IEC 61000-4-5 Clamping Voltage @ 8/20 µs	@ I _P = 5 A ²	V _C			8.5	V
	@ I _P = 24 A ²	V _C			15	٧
	@ $I_{PP} = 30 A^2$	V _C			17	V

Notes:

- 1. See Peak Pulse Power vs. Pulse Time.
- 2. Each differential line pair.

Thermal Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Parameter	Symbol	Min.	Nom.	Max.	Unit
Junction Temperature Range	T_J	-55	+25	+125	°C
Storage Temperature Range	T _{STG}	-55	+25	+150	°C

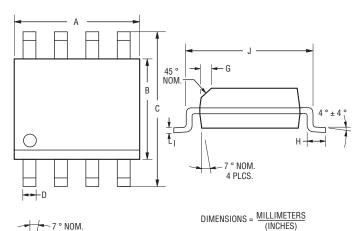


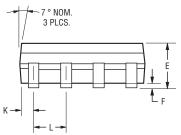
WARNING Cancer and Reproductive Harm - www.P65Warnings.ca.gov

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Product Dimensions

This is an RoHS compliant molded JEDEC narrow body SO-8 package with 100 % Sn plating on the lead frame. It weighs approximately 15 mg and has a flammability rating of UL 94V-0.

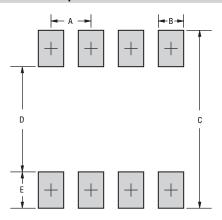




17200.	
L _F	
L-=	

Dimensions				
А	<u>4.80 - 5.00</u> (0.189 - 0.197)			
В	<u>3.81 - 4.00</u> (0.150 - 0.157)			
С	5.80 - 6.20 (0.228 ± 0.244)			
D	0.36 - 0.51 (0.014 - 0.020)			
E	1.35 - 1.75 (0.053 - 0.069)			
F	<u>0.102 - 0.203</u> (0.004 - 0.008)			
G	<u>0.25 - 0.50</u> (0.010 - 0.020)			
Н	<u>0.51 - 1.12</u> (0.020 - 0.044)			
I	<u>0.190 - 0.229</u> (0.0075 - 0.0090)			
J	<u>4.60 - 5.21</u> (0.181 - 0.205)			
К	<u>0.28 - 0.79</u> (0.011 - 0.031)			
L	<u>1.27</u> (0.050)			

Recommended Footprint



Dimensions			
А	<u>1.143 - 1.397</u> (0.045 - 0.065)		
В	<u>0.635 - 0.889</u> (0.025 - 0.035)		
С	6.223 Min.		
D	3.937 - 4.191 (0.155 - 0.165)		
E	1.016 - 1.27 (0.040 - 0.050)		

Typical Part Marking

How to Order

CDNBS08-SLVU2.8-8

CD NBS08 - SLVU 2.8 - 8 Common Code Chip Diode Package

SLVU = Low Capacitance TVS Array

NBS08 = Narrow Body SOIC8 Package

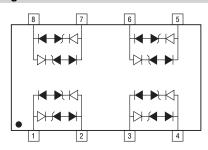
Working Peak Reverse Voltage - $2.8 = 2.8 V_{RWM}$ (Volts)

Number of Diodes

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Block Diagram

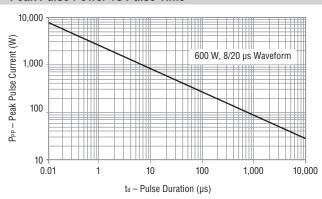


Device Pinout

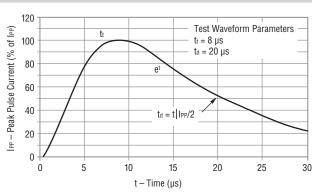
Pin	Bidirectional Common Mode	Bidirectional Differential Mode
1	Line 1	Line Pair 1
2	GND	Line Pair 1
3	GND	Line Pair 2
4	Line 4	Line Pair 2
5	Line 3	Line Pair 4
6	GND	Line Pair 4
7	GND	Line Pair 3
8	Line 2	Line Pair 3

Performance Graphs

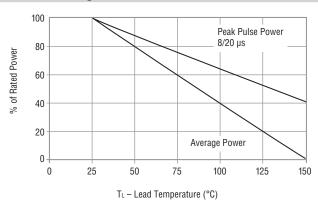
Peak Pulse Power vs Pulse Time



Pulse Waveform



Power Derating Curve



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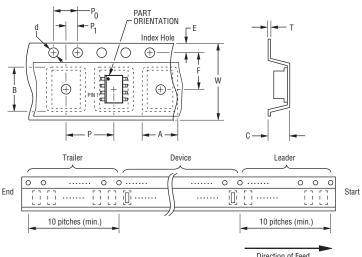
Packaging Information

Tape Width

Reel Width

Quantity per Reel

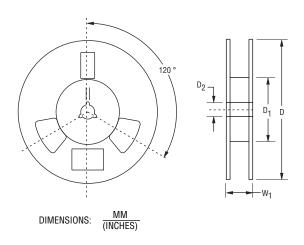
The product is packaged in tape and reel format per EIA-481 standard.



		Direction of Feed
Item	Symbol	NSOIC 8L
Carrier Width	А	$\frac{6.7 \pm 0.10}{(0.264 \pm 0.004)}$
Carrier Length	В	$\frac{5.5 \pm 0.10}{(0.217 \pm 0.004)}$
Carrier Depth	С	$\frac{2.10 \pm 0.10}{(0.083 \pm 0.004)}$
Sprocket Hole	d	$\frac{1.55 \pm 0.05}{(0.061 \pm 0.002)}$
Reel Outside Diameter	D	330 (12.992)
Reel Inner Diameter	D ₁	80.0 (3.1500) MIN.
Feed Hole Diameter	D ₂	$\frac{13.0 \pm 0.20}{(0.512 \pm 0.008)}$
Sprocket Hole Position	E	$\frac{1.75 \pm 0.10}{(0.069 \pm 0.004)}$
Punch Hole Position	F	$\frac{3.50 \pm 0.05}{(0.138 \pm 0.002)}$
Punch Hole Pitch	Р	$\frac{8.00 \pm 0.10}{(0.315 \pm 0.004)}$
Sprocket Hole Pitch	P ₀	$\frac{4.00 \pm 0.10}{(0.157 \pm 0.004)}$
Embossment Center	P ₁	$\frac{2.00 \pm 0.05}{(0.079 \pm 0.002)}$
Overall Tape Thickness	Т	$\frac{0.20 \pm 0.10}{(0.008 \pm 0.004)}$

W

W₁



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REV. 08/19

 12.00 ± 0.20

 (0.472 ± 0.008)

18.4

 $\overline{(0.724)}$

2500

MAX.

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