PRA073, PRA074, PRA100, PRA135, PRA182 (CNW)

**FEATURES** 

(to 1 ppm/°C)

**Vishay Sfernice** 

# High-Precision Thin Film Chip Resistor Arrays, Sulfur Resistant



www.vishay.com

# DESIGN SUPPORT TOOLS AVAILABLE



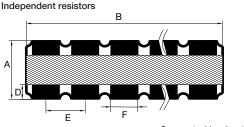
ISHAY

PRA arrays can be used in most applications requiring a matched pair (or set) of resistor elements. The networks provide 1 ppm/°C TCR tracking, a ratio tolerance as tight as 0.01 %, and outstanding stability.

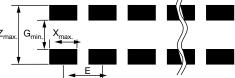
They are available in pitch:

- 0.70 mm for PRA073 (based on case 0302)
- 0.70 mm for PRA074 (based on case 0402)
- 1.00 mm for PRA100 (based on case 0603)
- 1.35 mm for PRA135 (based on case 0805)
- 1.82 mm for PRA182 (based on case 1206)

### DIMENSIONS



Suggested land pattern (according to IPC-7351A)



С

# Very low noise < -35 dB and voltage coefficient</li> < 0.01 ppm/V

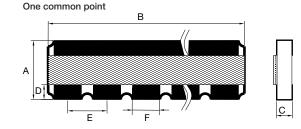
- Ratio tolerance to 0.01 % ( $R \ge 200R$ ) High-temperature (230 °C) version, see PRA HT
  - ESA-gualified version, see PRA HR
  - SMD wraparound chip resistor array
  - Thin film technology
  - Option to withstand humidity test of AEC-Q200

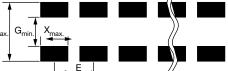
• High-stability passivated nichrome resistive layer 0.02 % on ratio, 1000 h at Pn at +70 °C • Tight TCR (10 ppm/°C) and TCR tracking

- Sulfur resistant (per ASTM B809-95 humid vapor test)
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **TYPICAL PERFORMANCE**

	ABSOLUTE	TRACKING
TCR	10 ppm/°C	2 ppm/°C
	ABSOLUTE	RATIO
TOL.	0.1 %	0.01 %





DIM.	PRA0 (0302 b		PRA07 (0402 ba		PRA100 (0603 base)		PRA135 (0805 base)			RA182 06 base)	
	mm	mil	mm	mil	mm	mil	mm	mil	mm	mil	
А	0.75 ± 0152	$29.5 \pm 6$	1.00 ± 0.152	40 ± 6	$1.52 \pm 0.152$	$60 \pm 6$	1.91 ± 0.152	75 ± 6	3.06 ± 0.152	120 ± 6	
В	B = N x E (± 0.2 mm) B = N x E (± 8 mil)										
С	0.5 ± 0.127	20 ± 5	0.5 ± 0.127	20 ± 5	0.5 ± 0.127	20 ± 5	0.5 ± 0.127	20 ± 5	0.5 ± 0.127	20 ± 5	
D	0.15 ± 0.08	5.9 ± 3	0.25 ± 0 .1	10 ± 4	0.38 ± 0.13	15 ± 5	0.38 ± 0.13	15 ± 5	0.4 ± 0.13	16 ± 5	
Е	0.7	27.5	0.7	27.5	1	40	1.35	53	1.825	72	
F	0.55 ± 0.1	21.5 ± 4	0.55 ± 0.1	$21.5 \pm 4$	0.7 ± 0.1	27.6 ± 4	1.05 ± 0.1	41.4 ± 4	1.525 ± 0.1	6 ± 4	
G <sub>min.</sub>	0.28	11	0.29	11.4	0.49	19.3	0.88	34.5	1.99	78.3	
X <sub>max.</sub>	0.51	20	0.51	20	0.66	26	1.01	39.8	1.49	58.7	
Z <sub>max.</sub>	1.8	70.9	2.05	80.7	2.57	101.2	2.96	116.5	4.11	161.8	
Mada											

#### Note

N represents number of resistors

Revision: 22-Jul-2019

Document Number: 53033

RoHS

COMPLIANT

HALOGEN FREE GREEN

<u>(5-2008)</u>



# PRA073, PRA074, PRA100, PRA135, PRA182 (CNW)

**Vishay Sfernice** 

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GLOBAL P	ART NUMBE	R INFORMAT	<b>TION</b> <sup>(1)</sup>					
New Global Pa	art Numbering: Pl	RA100I4-5K62BW	/BT51					
PR	A 1	0 0 1	4 -	5 K	6	2 B \	N B T	99
GLOBAL MODEL	CONFIG.	NUMBERS OF RESISTORS	VALUE <sup>(2)</sup> ABS	6. TOL.	RATIO TOL.	. TERMINAT		ING OPTION
PRA074 PRA100 PRA135 PRA182	I: independent C: common	2 to 8	R or K D =		<b>B</b> = 0.1 % <b>W</b> = 0.05 % <b>P</b> = 0.02 % <b>L</b> = 0.01 %	N: SnAg o	rier ver rier rier B: lead N and G	n see blank ation if no ging" option
CNW	1368	·	т	]				
GLOBAL MODEL	REFERENCE	"Codification of	l ormation see packaging" table	]				
Historical Par	rt Number examp	le: PRA100   4 5	5K62 0.1 % 0.05	5 % TR R	0051			
PRA100	I	4	5K62	0.1	%	0.05 %	TR	R0099
HISTORICAL MODEL	CONFIG.	NUMBERS OF RESISTORS	OHMIC VALUE	ABS.	TOL. F	RATIO TOL.	PACKAGING	OPTION

Notes

<sup>(1)</sup> Part number can only have 18 digits. Depending on information needed a compromise has to be found. Consult Vishay

<sup>(2)</sup> When the last digit(s) of the ohmic value is (are) 0, it (they) must be omitted

E.g.:PRA100I4-2K20BWN  $\rightarrow$  must be ordered under PRA100I4-2K2BWN PRA100I4-2K00BWN → must be ordered under PRA100I4-2KBWN

#### STANDARD ELECTRICAL SPECIFICATIONS POWER RESISTANCE ABSOLUTE RATIO ABSOLUTE RATIO **RATING PER** MODEL RANGE TOLERANCE TOLERANCE (2) TCR (3) TCR<sup>(4)</sup> SIZE **RESISTOR**<sup>(1)</sup> ± ppm/°C Ω ± % % ± ppm/°C w PRA073 10 to 50K 0.030 0.1, 0.5 0.01, 0.02, 0.05, 0.1 073 10 1, 2 **PRA074** 074 10 to 100K 0.040 0.1, 0.5 0.01, 0.02, 0.05, 0.1 10 1, 2 **PRA100** 100 10 to 250K 0.100 0.1, 0.5 0.01, 0.02, 0.05, 0.1 10 1, 2 **PRA135** 135 10 to 500K 0.125 0.1, 0.5 0.01, 0.02, 0.05, 0.1 10 1, 2 PRA182 182 10 to 2M 0.200 0.1, 0.5 0.01, 0.02, 0.05, 0.1 10 1, 2

#### Notes

(1) At +70 °C

### **CLIMATIC SPECIFICATIONS**

Operating temperature range <sup>(1)</sup>	-55 °C to +155 °C
Note	

Note

<sup>(1)</sup> For °C, temperature up to 230 PRA see HT (www.vishav.com/doc?53057) or consult factory

PERFORMANCE	S. HUMID SULFUR VAPOR
Test conditions	50 °C ± 2 °C, 85 % ± 4 % RH, exposure time 500 h
Test results	Resistance drift < (0.05 % $R$ + 0.05 $\Omega$ ), no corrosion products observed

PERFORMANCE	S	
TEST		SPECIFICATIONS
Noise		≤ -35 dB
Voltage coefficient		≤ 0.01 ppm/V
	PRA073	20 V
	PRA074	40 V
Limiting voltage	PRA100	50 V
	PRA135	100 V
	PRA182	150 V

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MECHANI	CAL SPECIFICATIONS
Substrate	Alumina
Technology	Thin film
Film	Nickel chromium with mineral passivation
	B type: SnPb over nickel barrier
Terminations	N type: SnAg over nickel barrier
	G type: Gold over nickel barrier

#### SPECIAL FEATURES

Resistance values can be different on a given network ( $R \max ./R$  min. as high as 300). Tooling charges might be required depending on the ohmic values in the same network. Please, consult Vishay Sfernice for ohmic values, tolerances and also temperature coefficient (e.g.  $\pm 1 \text{ ppm/}^{\circ}\text{C}$ ) outside the standard range.

**Vishay Sfernice** 

## **AEC-Q200 OPTION: 0058**

Vishay Sfernice offers a part compliant to AEC-Q200 specification.

# PACKAGING

Several types of packaging are available: Waffle-pack and tape and reel.

		NUMBER OF PIECES PER PACKAGE				
SIZE	MOQ	WAFFLE PACK MAX. QUANTITY PER BOX	TAPE AN	D REEL <sup>(1)</sup>		
SIZE	MOQ	WAFFLE PACK MAX. QUANTITY PER BOX	MIN.	MAX.		
PRA073 x 2		400				
PRA073 x 3		100				
PRA073 x 4		140				
PRA073 x 5	100	140				
PRA073 x 6		60				
PRA073 x 7		60				
PRA073 x 8		60				
PRA074 x 2		400				
PRA074 x 3		100				
PRA074 x 4		140	100	4000		
PRA074 x 5	100	140				
PRA074 x 6		60				
PRA074 x 7		60				
PRA074 x 8		60				
PRA100 x 2		100	100	4000		
PRA100 x 3		140	100	4000		
PRA100 x 4		60	100	4000		
PRA100 x 5	100	50				
PRA100 x 6		50	100	3000		
PRA100 x 7		50				
PRA100 x 8		28	100	4000		
PRA135 x 2		140	100	4000		
PRA135 x 3		60				
PRA135 x 4		60	100	4000		
PRA135 x 5	100	50				
PRA135 x 6		28	100	4000		
PRA135 x 7		24				
PRA135 x 8		24				
PRA182 x 2		60	100	2000		
PRA182 x 3		60	100	4000		
PRA182 x 4		50	100	2000		
PRA182 x 5	100	21	100	4000		
PRA182 x 6		24				
PRA182 x 7		24				
PRA182 x 8		20				

Note

<sup>(1)</sup> Other sizes upon request

CODIFICATION OF PACKAGING			
CODE 18	PACKAGING		
WAFFLE PACK			
W	100 min., 1 mult		
PLASTIC TAPE (Standard for all si	zes.)		
Т	100 min., 1 mult		
ТА	100 min., 100 mult		
TB	250 min., 250 mult		
TC	500 min., 500 mult		
TD	1000 min., 1000 mult		
TE	2500min., 2500 mult		
TF	Full tape (quantity depending on size of chips)		

Revision: 22-Jul-2019

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# PACKAGING RULES

### Waffle Pack

Can be filled up to maximum quantity indicated in the table here above, taking into account the minimum order quantity. When quantity ordered exceeds maximum quantity of a single waffle pack, the waffle packs are stacked up on the top of each other and closed by one single cover.

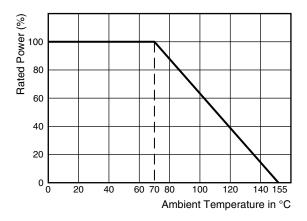
To get "not stacked up" waffle pack in case of ordered quantity > maximum number of pieces per package: Please consult Vishay Sfernice for specific ordering code.

### Tape and Reel

Can be filled up to maximum quantity indicated in the table here above, taking into account the minimum order quantity. When quantity ordered is between the MOQ and the maximum reel capacity, only one reel is provided.

When several reels are needed for ordered quantity within MOQ and maximum reel capacity: Please consult Vishay Sfernice for specific ordering code.

# **POWER RATING**



# MARKING <sup>(1)</sup>

On the primary package, printed information includes Vishay S.A. trademark series and model, schematic number of resistors, ohmic value, absolute tolerance, ratio tolerance, type of termination: B tinned over nickel barrier.

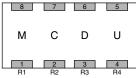
Vishay Sfernice

#### Marking on parts:

All resistors inside network have same ohmic value: If number of resistors inside network < or = 3



For instance ohmic value 13K: Coded 1302: M = 1, C = 3, D = 0, U = 2If number of resistors inside networks > 3



E.g.: 4 resistors in the network: Ohmic value 13K: Coded 1302: M = 1, C = 3, D = 0, U = 2<u>Resistors inside the network have different ohmic value, a</u>

CNW number is assigned by Vishay Sfernice

If number of resistors inside network < or = 3



E.g.: CNW1538: M = 1, C = 5, D = 3, U = 8If number of resistors inside networks > 3

8	7	6	5
М	С	D	U
1 	2 R2	3 R3	4 R4

E.g.: 4 resistors in the network:

E.g.: CNW1314: M = 1, C = 3, D = 1, U = 4

#### Note

<sup>(1)</sup> PRA073 and PRA074 are NOT marked. For CNW of size 073 and 074, only a "dot" is marked to identify R1

	CONDITIONS	DRIFT	S
TESTS	CECC REQUIREMENTS	ABSOLUTE PER (Typical Values)	RATIO
Overload	2.5 Un/2 s	0.05 % Rn + 0.05 Ω	0.01 % Rn
Climatic sequences	-55 °C to +155 °C/5 moisture cycles	0.1 % Rn + 0.05 Ω	0.01 % Rn
Thermal shock	-55 °C to +155 °C/5 cycles 30'	0.05 % Rn + 0.05 Ω	0.01 % Rn
Load life	1000 h/Pn at 70 °C	0.1 % Rn + 0.05 Ω	0.02 % Rn
Resistance to solder heat	260 °C/10 s	0.05 % Rn + 0.05 Ω	0.01 % Rn
Moisture resistance	0.01 Pn at + 40 °C 93 % RH	0.1 % Rn + 0.05 Ω	0.01 % Rn
High temperature storage	1000 h/no load at +155 °C	0.1 % Rn + 0.05 Ω	0.02 % Rn

#### Note

Rn: Nominal resistance

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