



FEATURES

- UL 60950 recognised
- Wide temperature performance at full 2 Watt load, -40°C to 85°C
- UL 94V-0 package material
- Single isolated output
- Industry standard pinout
- 3kVDC isolation (1 minute) 'Hi Pot Test'
- 5V input
- 5V output
- Fully encapsulated with toroidal magnetics
- No electrolytic or tantalum capacitors

PRODUCT OVERVIEW

The CRV2 series of industrial temperature range DC-DC converters, available in industry standard SIP packaging offers a power upgrade path from the 1W CRV1 series.

SELECTION GUIDE

Order Code	Nominal Input Voltage	Output Voltage	Output Current	Input Current at Rated Load	Load Regulation (Typ)	Load Regulation (Max)	Ripple & Noise (Typ) ¹	Ripple & Noise (Max) ¹	Efficiency (Min)	Efficiency (Typ)	Isolation Capacitance	MTTF ²	
	V	V	mA		%	%	mVp-p		%	%	pF	MIL. kHrs	Tel. kHrs
CRV2S0505SC	5	5	400	470	5.7	7.3	24	40	80	83	28	3998	

INPUT CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Voltage range	Continuous operation, 5V input types	4.5	5	5.5	V
Reflected ripple current	5V input types		7.5	15	mA p-p

ISOLATION CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Isolation test voltage	Flash tested for 1 minute	3000			VDC
Resistance	Viso= 1000VDC	10			GΩ

OUTPUT CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Rated Power	T _A =-40°C to 85°C			2.0	W
Voltage Set Point Accuracy	See tolerance envelope				
Line regulation	High V _{IN} to low V _{IN}		1.1	1.2	%/%

GENERAL CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Switching frequency	All input types		60		kHz

TEMPERATURE CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Specification	All output types, see safety approval section for UL temperature specification	-40		85	°C
Storage		-50		125	
Case Temperature above ambient				28	
Cooling	Free air convection				

ABSOLUTE MAXIMUM RATINGS

Lead temperature 1.5mm from case for 10 seconds	260°C
Wave Solder	Wave Solder profile not to exceed the profile recommended in IEC 61760-1 Section 6.1.3. Please refer to application notes for further information.
Input voltage V _{IN}	7V



For full details go to www.murata-ps.com/rohs



1. See Ripple & Noise characterisation method.
2. Calculated using MIL-HDBK-217 FN2 and Telcordia SR-332 calculation model with nominal input voltage at full load.

All specifications typical at T_A=25°C, nominal input voltage and rated output current unless otherwise specified.

TECHNICAL NOTES

ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions CRV2 series of DC-DC converters are all 100% production tested at their stated isolation voltage. This is 3kVDC for 1 minute.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

The CRV2 has been recognised by Underwriters Laboratory for functional insulation, both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. The CRV2 series has toroidal isolation transformers, with no additional insulation between primary and secondary windings of enamelled wire. While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

This consideration equally applies to agency recognised parts rated for better than functional isolation where the wire enamel insulation is always supplemented by a further insulation system of physical spacing or barriers.

SAFETY APPROVAL

The CRV2 series has been recognised by Underwriters Laboratory (UL) to UL 60950 for functional insulation in a maximum still air ambient temperature of 85°C and/or case temperature limit (case temperature measured on the face opposite the pins).

The CRV2 Series of converters are not internally fused so to meet the requirements of UL 60950 an anti-surge input line fuse should always be used with ratings as defined below. CRV2S0505SC: 2A

All fuses should be UL recognised and rated to at least the maximum allowable DC input voltage.

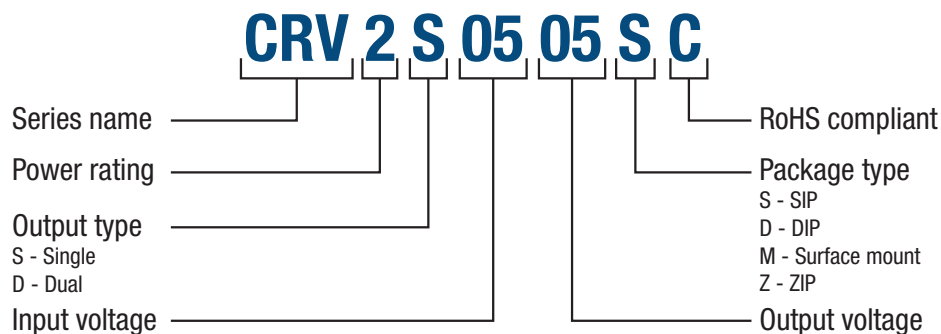
File number E151252 applies.

RoHS COMPLIANCE INFORMATION



This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. Please refer to [application notes](#) for further information. The pin termination finish on the SIP package type is Tin Plate, Hot Dipped over Matte Tin with Nickel Preplate. The DIP types are Matte Tin over Nickel Preplate. Both types in this series are backward compatible with Sn/Pb soldering systems. For further information, please visit www.murata-ps.com/rohs

PART NUMBER STRUCTURE



APPLICATION NOTES

Minimum load

The minimum load to meet datasheet specification is 10% of the full rated load across the specified input voltage range. Lower than 10% minimum loading will result in an increase in output voltage, which may rise to typically double the specified output voltage if the output load falls to less than 5%.

Unbalanced Load

The CRV2 series offers unbalanced loading capabilities with up to the full 2W available from a single output. However, when operated in this mode there may be a slight performance decrease in efficiency and load regulation.

Capacitive loading and start up

Typical start up times for this series, with a typical input voltage rise time of 2.2µs and output capacitance of 10µF, are shown in the table below. The product series will start into a capacitance of 47µF with an increased start time, however, the maximum recommended output capacitance is 10µF.

Typical Start-Up Wave Form

	Start-up time µs
CRV2S0505SC	

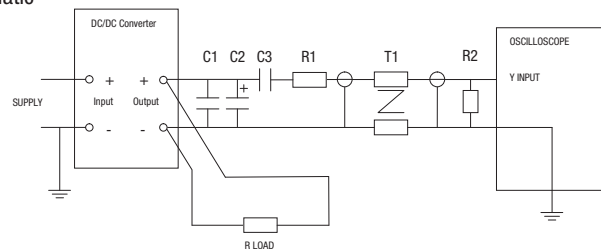
Ripple & Noise Characterisation Method

Ripple and noise measurements are performed with the following test configuration.

C1	1µF X7R multilayer ceramic capacitor, voltage rating to be a minimum of 3 times the output voltage of the DC-DC converter
C2	10µF tantalum capacitor, voltage rating to be a minimum of 1.5 times the output voltage of the DC-DC converter with an ESR of less than 100mΩ at 100 kHz
C3	100nF multilayer ceramic capacitor, general purpose
R1	450Ω resistor, carbon film, ±1% tolerance
R2	50Ω BNC termination
T1	3T of the coax cable through a ferrite toroid
RLOAD	Resistive load to the maximum power rating of the DC-DC converter. Connections should be made via twisted wires

Measured values are multiplied by 10 to obtain the specified values.

Differential Mode Noise Test Schematic



APPLICATION NOTES (Continued)

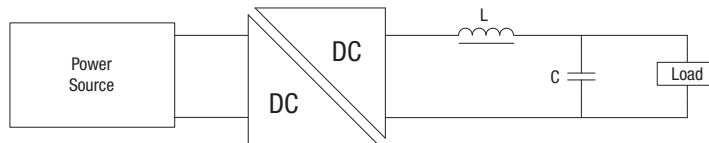
Output Ripple Reduction

By using the values of inductance and capacitance stated, the output ripple at the rated load is lowered to 5mV p-p max.

Component selection

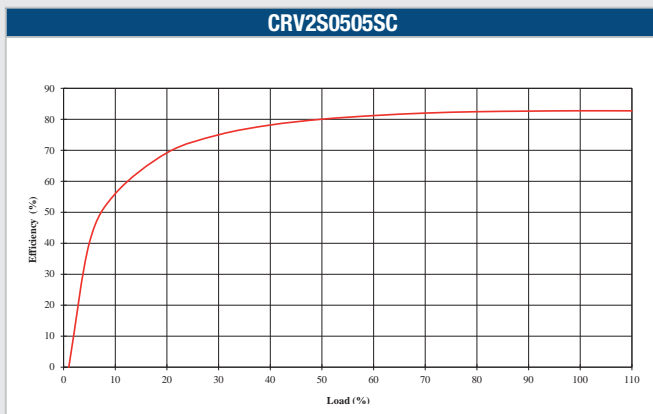
Capacitor: It is required that the ESR (Equivalent Series Resistance) should be as low as possible, ceramic types are recommended. The voltage rating should be at least twice (except for 15V output), the rated output voltage of the DC-DC converter.

Inductor: The rated current of the inductor should not be less than that of the output of the DC-DC converter. At the rated current, the DC resistance of the inductor should be such that the voltage drop across the inductor is <2% of the rated voltage of the DC-DC converter. The SRF (Self Resonant Frequency) should be >20MHz.

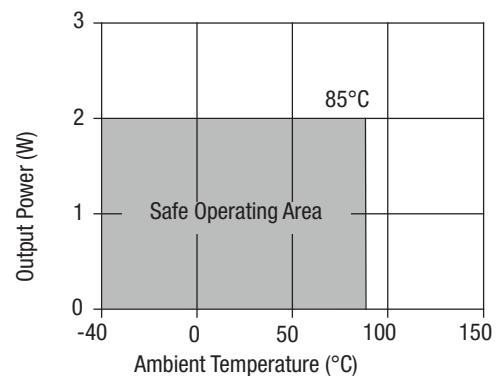


	Inductor		Capacitor
	L, μ H	SMD	Through Hole
CRV2S0505SC			C, μ F

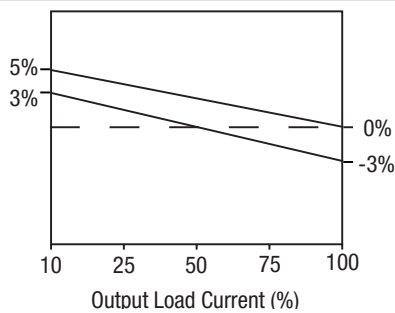
EFFICIENCY VS LOAD



TEMPERATURE DERATING GRAPH



TOLERANCE ENVELOPE

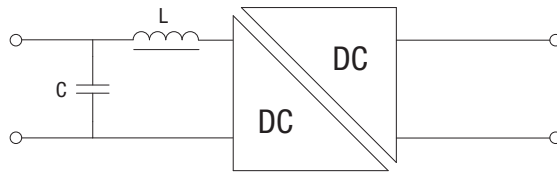


The voltage tolerance envelope shows typical load regulation characteristics for this product series. The tolerance envelope is the maximum output voltage variation due to changes in output loading.

EMC FILTERING AND SPECTRA

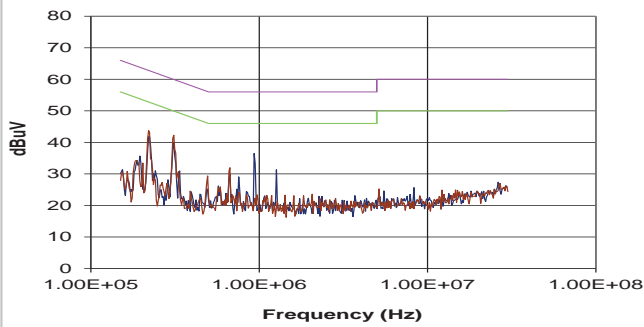
FILTERING

The following table shows the additional input capacitor and input inductor typically required to meet EN 55022 Curve B Quasi-Peak EMC limit, as shown in the following plots. The following plots show positive and negative quasi peak and CISPR22 Average Limit B (pink line) and Quasi Peak Limit B (green line) adherence limits.



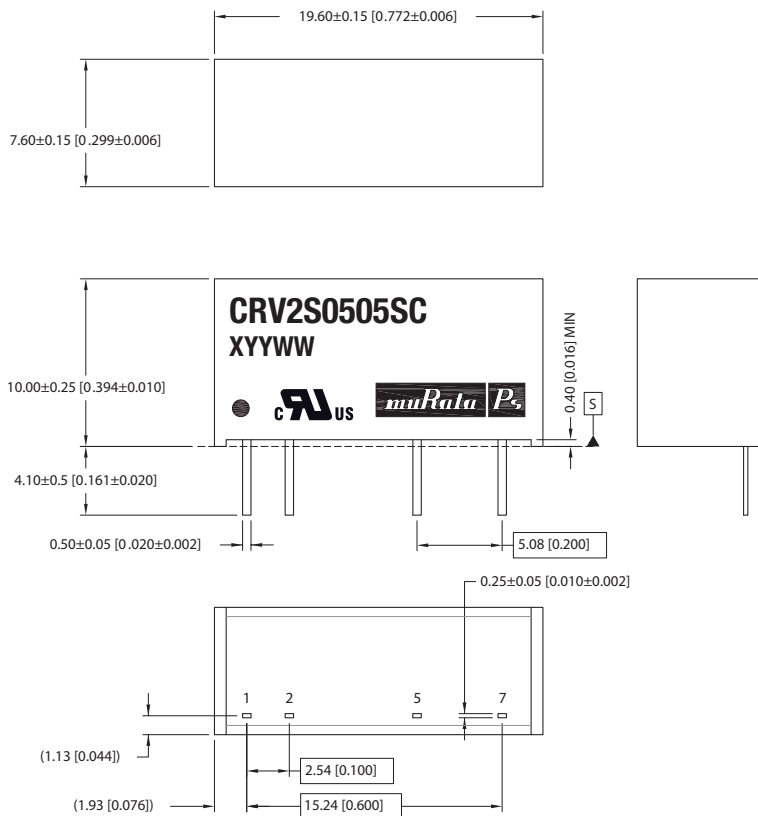
Part Number	Inductor			Capacitor
	L, μ H	SMD	Through Hole	C, μ F
CRV2S0505SC	10		13R103C	4.7

CRV2S0505SC



PACKAGE SPECIFICATIONS

MECHANICAL DIMENSIONS



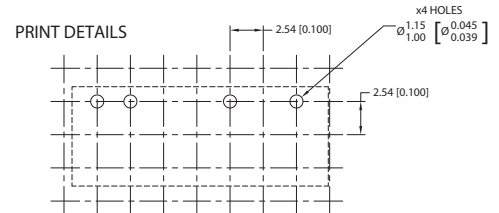
All dimensions in mm (inches) Controlling dimension is mm.
 All pins on a 2.54 (0.100) pitch and within ±0.1 (0.004) of true position from pin 1 at seating plane 'S'

Weight: 2.8g

PIN CONNECTIONS

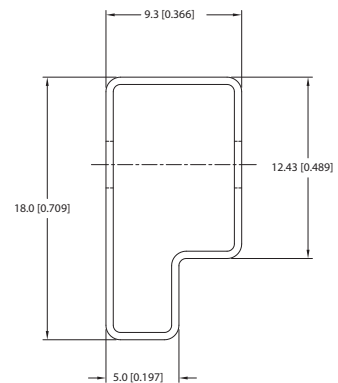
Pin	Function
1	+VIN
2	-VIN
5	-VOUT
7	+VOUT

RECOMMENDED FOOTPRINT DETAILS



Unless otherwise stated all dimensions in mm (inches) ±0.5mm.

TUBE OUTLINE DIMENSIONS



Unless otherwise specified all dimensions in mm [inches] ±0.55mm
 Tube Length : 520mm [20.472] ±2.0 [0.079].

Tube Quantity : 25

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- Traffic signal equipment
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- Data Processing equipment

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