

PMEG6030EP 3 A low VF MEGA Schottky barrier rectifier 7 November 2017

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 3 A
- Reverse voltage: V_R ≤ 60 V
- Low forward voltage •
- High power capability due to clip-bond technology
- AEC-Q101 qualified
- Small and flat lead SMD plastic package •
- Capable for reflow and wave soldering

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS) •
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Table 1. Quick I	reference data	
Symbol	Parameter	Co

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5 ; f = 20 kHz; T _{amb} \leq 50 °C; square wave	[1]	-	-	3	A
		δ = 0.5 ; f = 20 kHz; T _{sp} ≤ 135 °C; square wave		-	-	3	A
V _R	reverse voltage	T _j = 25 °C		-	-	60	V
V _F	forward voltage	I _F = 3 A; T _j = 25 °C		-	460	530	mV
I _R	reverse current	V _R = 60 V; T _j = 25 °C		-	80	200	μA

[1] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

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5. Pinning information

Table 2. F	Pinning inf	formation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]		1 - 2
2	A	anode	¹ CFP5 (SOD128)	sym001

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information						
Type number	Package					
	Name	Description	Version			
PMEG6030EP	CFP5	plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128			

7. Marking

Table 4. Marking codes	
Type number	Marking code
PMEG6030EP	AB

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	60	V
I _{F(AV)}	average forward current	δ = 0.5 $\ ;$ f = 20 kHz; T_{amb} \leq \ 50 \ ^{\circ}C; square wave	[1]	-	3	A
		δ = 0.5 $~;$ f = 20 kHz; $T_{sp} \leq ~135 \ ^{\circ}\text{C};$ square wave		-	3	A
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	50	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	625	mW
			<u>[3]</u>	-	1.05	W
			[1]	-	2.1	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

Device mounted on a ceramic PCB, AI_2O_3 , standard footprint. [1]

Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint. [2]

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance		[1] [2]	-	-	200	K/W
	from junction to ambient		[3] [2]	-	-	120	K/W
			[4] [2]	-	-	60	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[5]	-	-	12	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

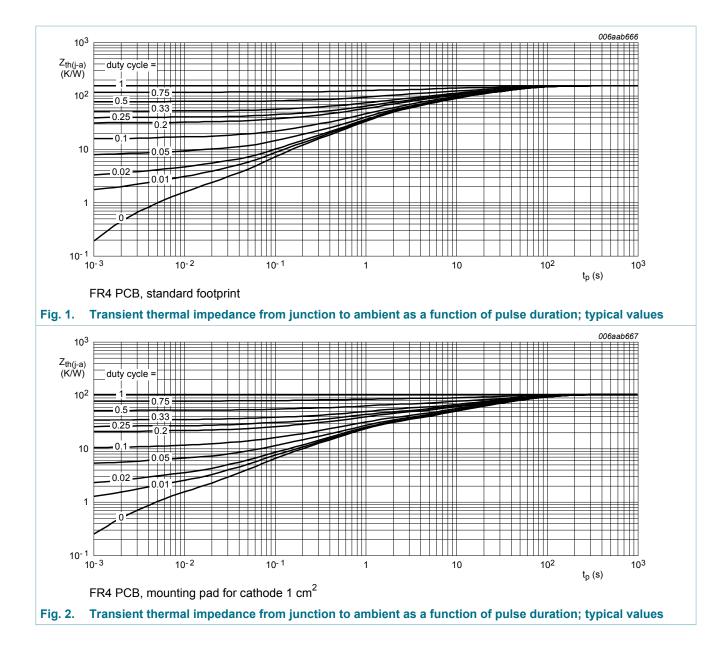
For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a [2] significant part of the total power losses.

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm². [3]

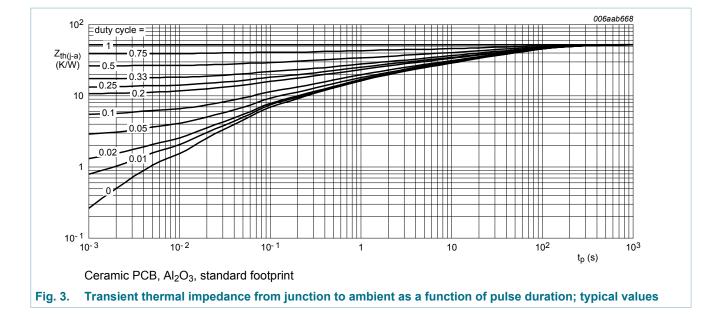
Device mounted on a ceramic PCB, AI_2O_3 , standard footprint. [4]

[5] Soldering point of cathode tab.

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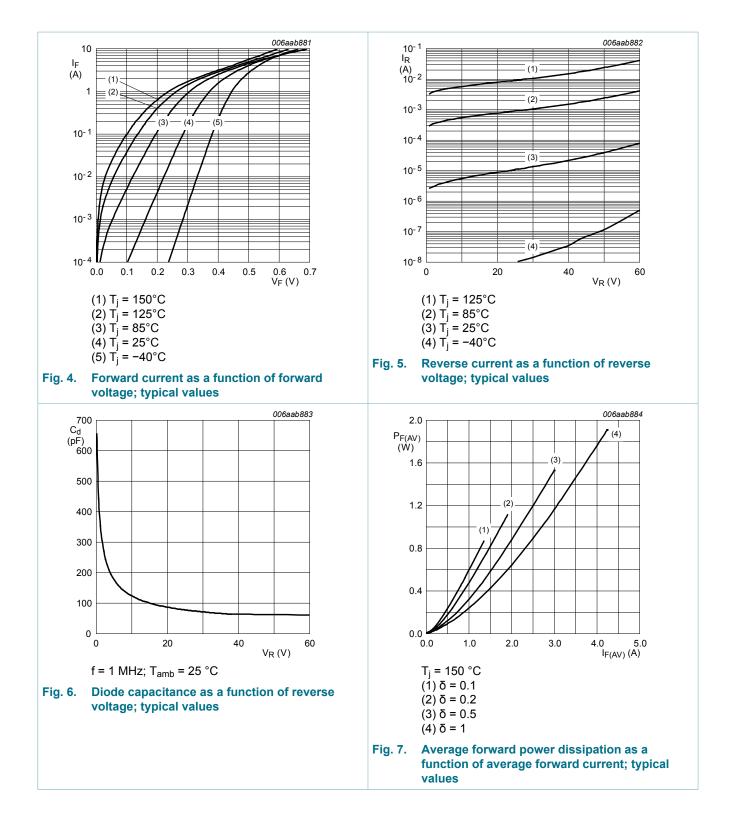
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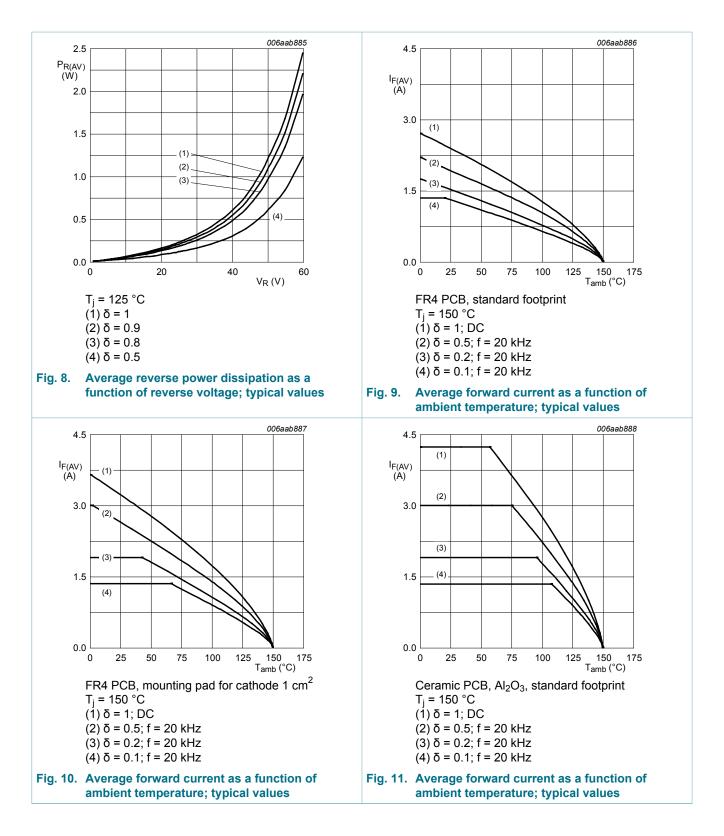
10. Characteristics

Table 7. Cha	racteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F fo	forward voltage	I _F = 0.1 A; T _j = 25 °C	-	290	330	mV
		I _F = 0.5 A; T _j = 25 °C	-	340	400	mV
		I _F = 1 A; T _j = 25 °C	-	380	440	mV
		I _F = 1.5 A; T _j = 25 °C	-	400	470	mV
		I _F = 2 A; T _j = 25 °C	-	430	500	mV
		I _F = 3 A; T _j = 25 °C	-	460	530	mV
I _R	reverse current	V _R = 5 V; T _j = 25 °C	-	4	-	μA
		V _R = 10 V; T _j = 25 °C	-	5	-	μA
		V _R = 60 V; T _j = 25 °C	-	80	200	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	360	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	120	-	pF

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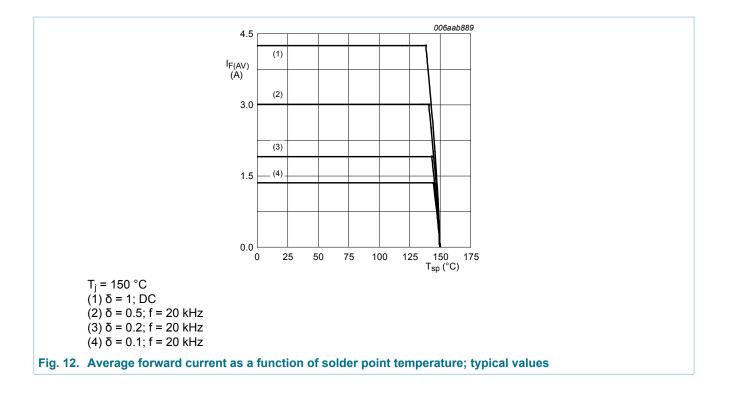


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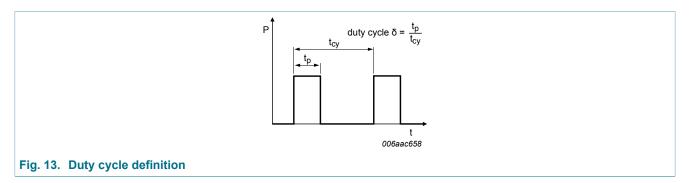
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11. Test information

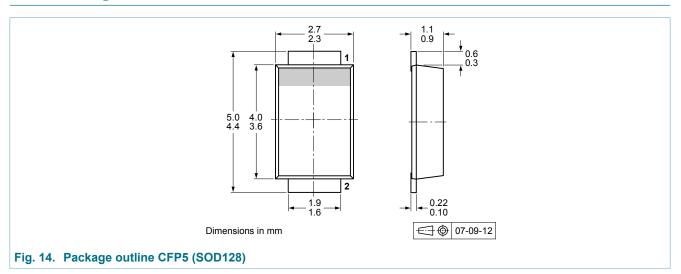


The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

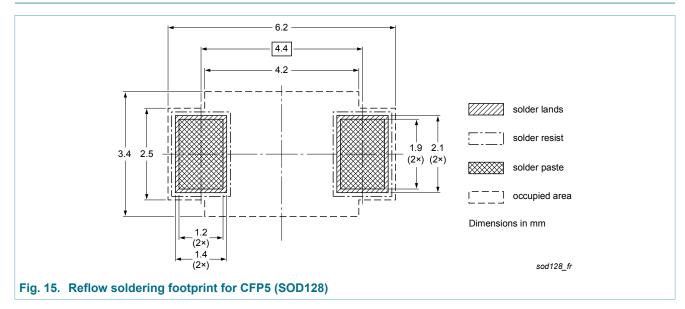
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

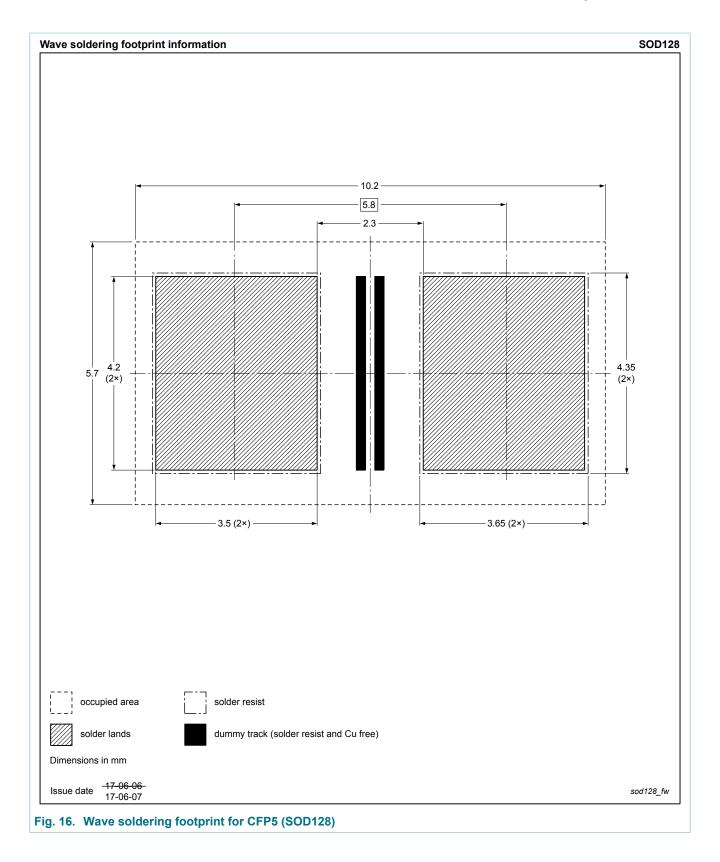
12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PMEG6030EP v.2	20171107	Product data sheet	-	PMEG6030EP_1			
Modifications:	 Features and benefits: Capable for reflow and wave soldering added Soldering: Wave soldering footprint added 						
PMEG6030EP_1	20100120	Product data sheet	-	-			

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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