LTM8063
40V, 2A Step-Down µModule Regulator

DESCRIPTION

Demonstration circuit 2494A is a 40V, 2A step-down µModule® regulator featuring the LTM®8063. The demo board is designed for 5V output from a 6.5V to 40V input. The wide input range allows a variety of input sources, such as automotive batteries and industrial supplies. The user adjustable features of the LTM8063 such as output voltage, switching frequency, soft-start, and power good can be changed on DC2494A simply by modifying the appropriate resistors and/or capacitors.

The LTM8063 can be programmed to different operation modes. The SYNC pin on the demo board is grounded by default for low ripple Burst Mode® operation. Moving JP1 to PULSE SKIPPING position changes the operation mode to pulse-skipping operation. To synchronous to an external clock, move JP1 to SYNC and apply the external clock to the SYNC turret. Once JP1 is on SPREAD SPECTRUM position, an external DC source can be applied to the SYNC pin to enable low EMI spread spectrum operation. This DC source can also be generated from Vout with appropriate voltage divider if V_{OUT} is 2.9V or higher. See the Quick Start Procedure section for more details.

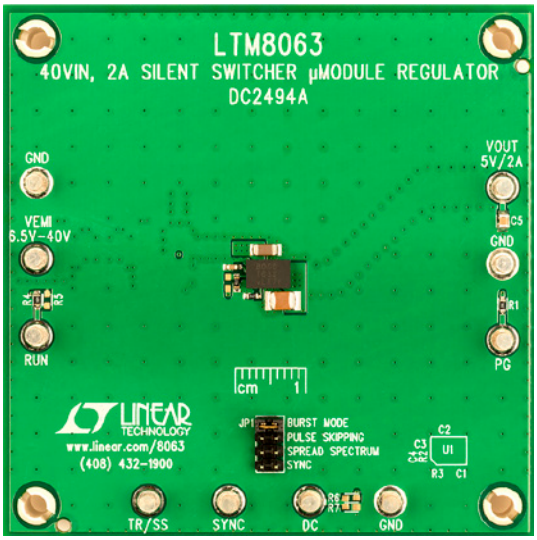
Figure 1 shows the efficiency of the circuit under different input voltages in Burst Mode operation. The rated maximum load current is 2A, while derating is necessary for certain input voltage and thermal conditions. Figure 2 shows the LTM8063 thermal performance on DC2494A demo board.

The demo board has an EMI filter installed. An inductor L1, which is shorted on the board by default, can be added in the EMI filter to further reduce the conducted emission.

The LTM8063 data sheet gives a complete description of the part, operation and application information. The data sheet must be read in conjunction with this demo manual for DC2494A.

Design files for this circuit board are available at http://www.linear.com/demo/DC2494A

BOARD PHOTO



PERFORMANCE SUMMARY

Specifications are at T_A = 25°C

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V_{IN}	Input Voltage Range		6.5		40	V
V_{OUT}	Output Voltage		4.85	5	5.15	V
I_{OUT}	Maximum Output Current		2			A
f_{SW}	Switching Frequency			1.4		MHz
EFF	Efficiency at DC	V_{IN} = 12V, I_{OUT} = 1A		91.1		%

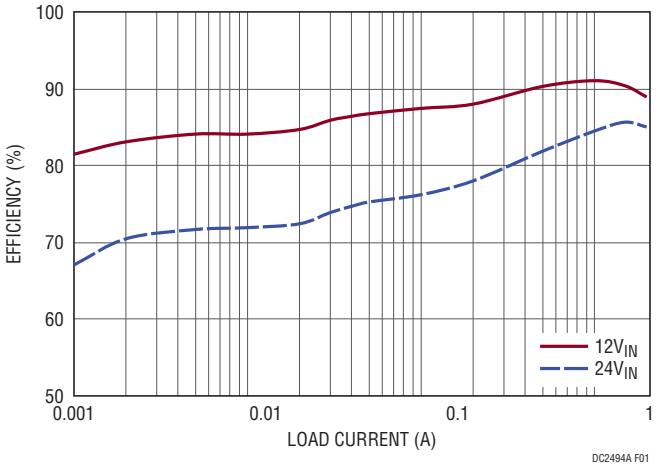


Figure 1. DC2494A Efficiency vs Load Current

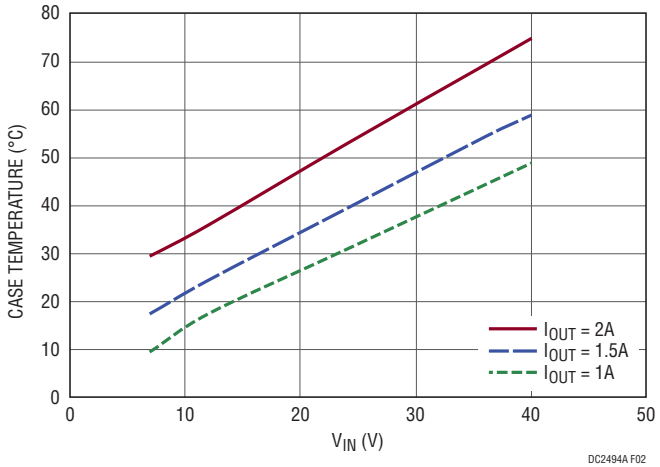


Figure 2. DC2494A Case Temperature Rise vs Input Voltage

PERFORMANCE SUMMARY

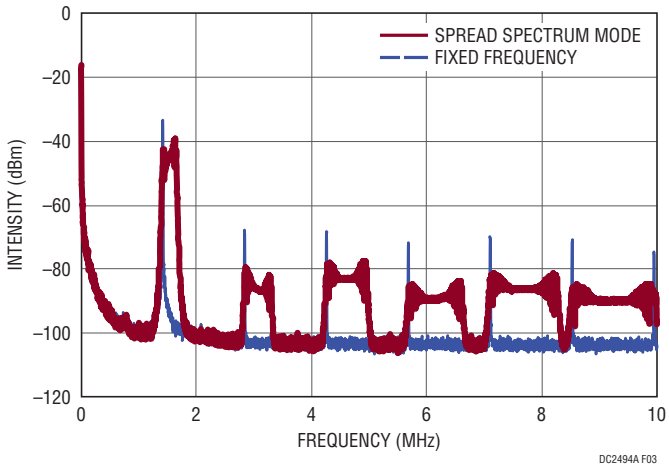


Figure 3. DC2494A Output Noise Spectrum, V_{IN} = 12V, V_{OUT} = 5V, I_{OUT} = 2A

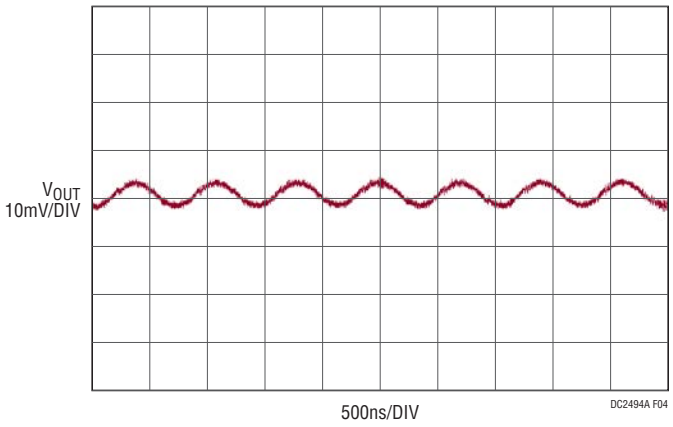


Figure 4. DC2494A Output Ripple, V_{IN} = 12V, V_{OUT} = 5V, I_{OUT} = 2A

QUICK START PROCEDURE

Demonstration circuit 2494A is easy to set up to evaluate the performance of the LTM8063. Refer to Figure 4 for proper measurement equipment setup and follow the procedure below:

NOTE: When measuring the input or output voltage ripple, care must be taken to avoid a long ground lead on the oscilloscope probe. See Figure 5 for the proper scope technique.

1. Set an input power supply that is capable of 40V/2A. Then turn off the supply.
2. With power off, connect the supply to the input terminals V_{EMI} and GND.
3. Turn on the power at the input.

   NOTE: Make sure that the input voltage never exceeds 40V.
4. Check for the proper output voltage of 5V. Turn off the power at the input.
5. Once the proper output voltage is established, connect a variable load capable of sinking 2A at 5V to the output terminals V_{OUT} and GND. Set the current for 0A.
   a. If efficiency measurements are desired, an ammeter can be put in series with the output load in order to measure the DC2494A's output current.
   b. A voltmeter can be placed across the output terminals in order to get an accurate output voltage measurement.
6. Turn on the power at the input.

   NOTE: If there is no output, temporarily disconnect the load to make sure that the load is not set too high.
7. Once the proper output voltage is established again, adjust the load and/or input within the operating range and observe the output voltage regulation, ripple voltage, efficiency and other desired parameters.
8. An external clock can be added to the SYNC terminal when SYNC function is used (JP1 on the SYNC position). Please ensure that the chosen RT sets the LTM8063 switching frequency to equal or below the lowest SYNC frequency. An external 2.9V to 4.2V DC voltage can be applied to SYNC turret to enable low noise spread spectrum function (JP1 on the SPREAD SPECTRUM position). This DC voltage can be generated from V_{OUT} with a appropriate voltage divider. See the Synchronization section in the data sheet for more information.

QUICK START PROCEDURE

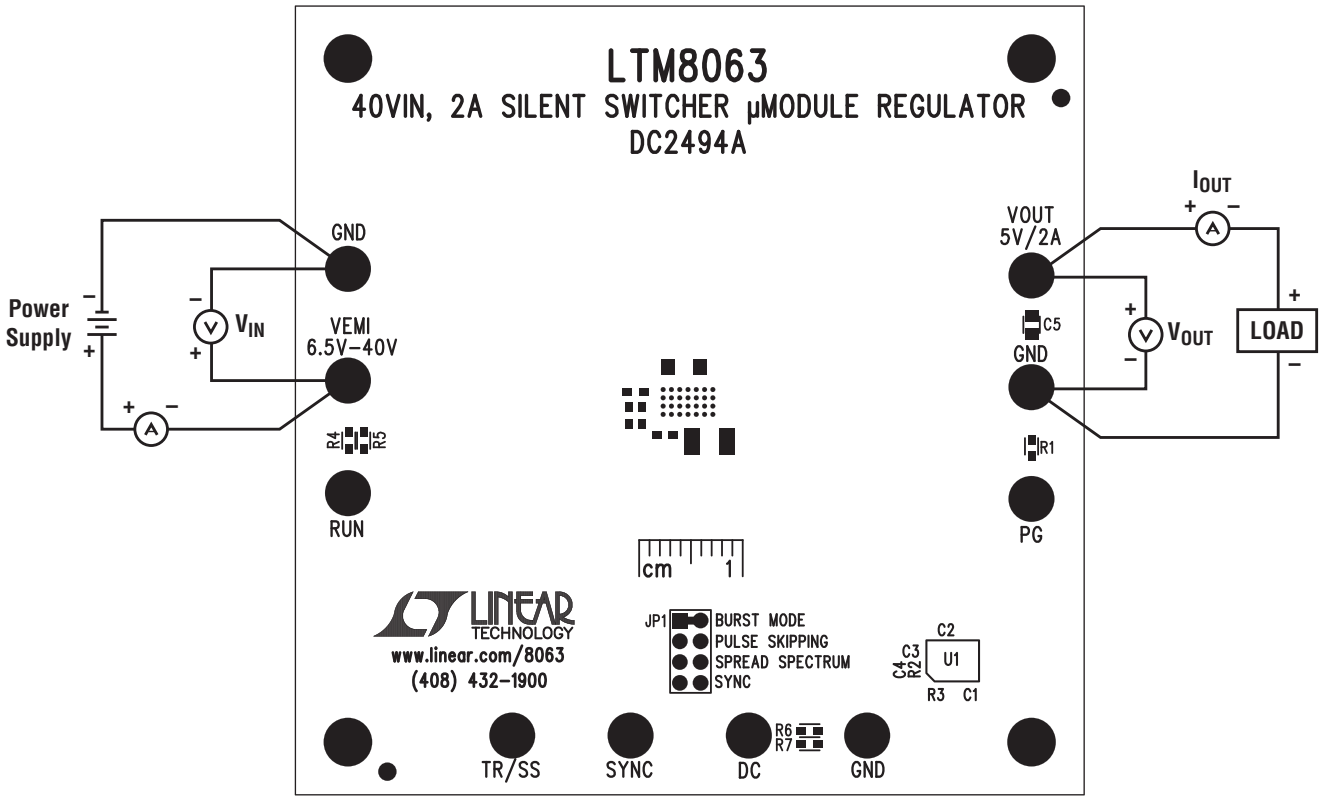


Figure 5. Proper Measurement Equipment Setup

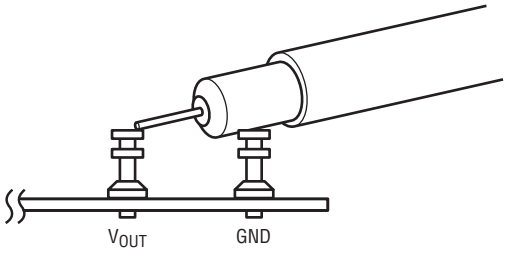
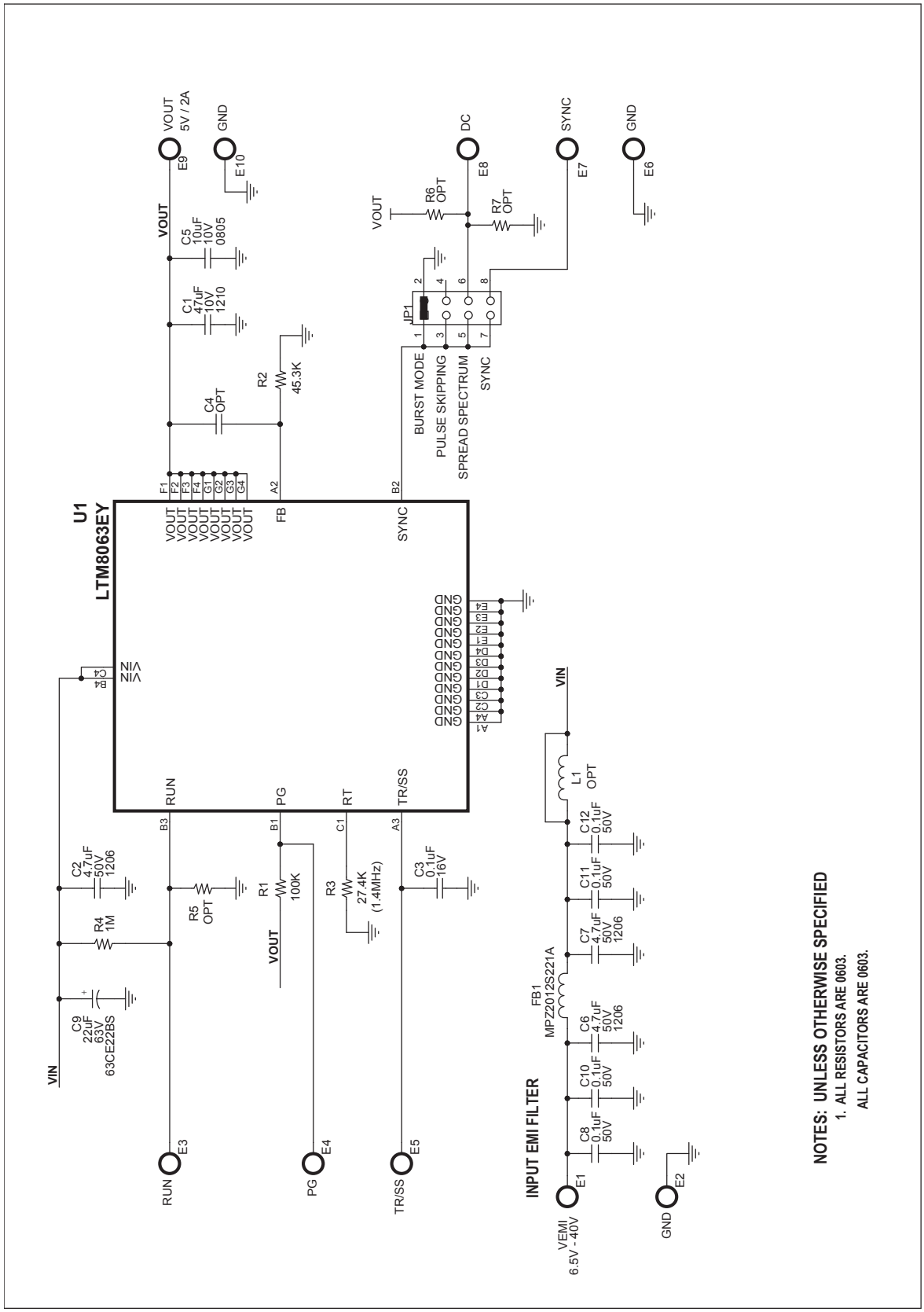


Figure 6. Measuring Output Ripple

PARTS LIST

ITEM	QTY	REFERENCE	PART DESCRIPTION	MANUFACTURER/PART NUMBER
Required Circuit Components				
1	1	C1	CAP., X7R, 47µF, 10V, 10%, 1210	MURATA, GRM32ER71A476KE15L
2	1	C2	CAP., X7R, 4.7µF, 50V, 10%, 1206	AVX, 12065C475KAT2A
3	1	C3	CAP., X7R, 0.1µF, 16V, 10%, 0603	MURATA, GRM188R71C104KA01D
4	1	R1	RES, CHIP, 100k, 1/10W, 1%, 0603	VISHAY, CRCW0603100KFKEA
5	1	R2	RES, CHIP, 45.3k, 1/10W, 1%, 0603	VISHAY, CRCW060345K3FKEA
6	1	R3	RES, CHIP, 27.4k, 1/10W, 1%, 0603	VISHAY, CRCW060327K4FKEA
7	1	R4	RES., CHIP, 1M, 1/10W, 1%, 0603	VISHAY, CRCW06031M00FKEA
8	1	U1	IC, REGULATOR, 28BGA	LINEAR TECH., LTM8063EY#PBF
Additional Demo Board Circuit Components				
1	0	C4 (OPT)	CAP., OPTION, 0603	
2	1	C5	CAP., X7R, 10µF, 10V, 10%, 0805	MURATA, GRM21BR71A106KA73L
3	2	C6, C7	CAP., X7R, 4.7µF, 50V, 10%, 1206	AVX, 12065C475KAT2A
4	4	C8, C10, C11, C12	CAP., X7R, 0.1µF, 50V, 10%, 0603	MURATA, GRM188R71H104KA93D
5	1	C9	CAP., ALUM, 22µF, 63V, 20%	SUN ELEC., 63CE22BS
6	1	FB1	FERRITE BEAD 0805	TDK, MPZ2012S221AT000
7	0	L1 (OPT.)	IND., OPTION	
8	0	R5, R6, R7 (OPT.)	RES., 0603	
Hardware: For Demo Board Only				
1	10	E1-E10	TESTPOINT, TURRET, 0.094"	MILL-MAX, 2501-2-00-80-00-00-07-0
2	1	JP1	DOUBLE ROW HEADER 2 × 4 0.079"	WURTH ELECTRONICS, 62000821121
3	1	XJP1	SHUNT, 0.079" CENTER	WURTH ELECTRONICS, 60800213421
4	4	MH1-MH4	STAND-OFF, NYLON 0.375"	KEYSTONE, 8832(SNAP ON)

SCHEMATIC DIAGRAM



ESD Caution
ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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