BD180G

Plastic Medium-Power Silicon PNP Transistor

This device is designed for use in 5.0 to 10 Watt audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

Features

- High DC Current Gain
- BD180 is complementary with BD179
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CEO}	80	Vdc
Collector-Base Voltage	V _{CBO}	80	Vdc
Emitter-Base Voltage	V _{EBO}	5.0	Vdc
Collector Current	I _C	1.0	Adc
Base Current	Ι _Β	2.0	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	30 240	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

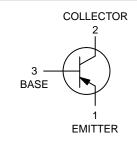
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4.16	°C/W



ON Semiconductor®

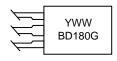
http://onsemi.com

3.0 AMPERES POWER TRANSISTORS PNP SILICON 80 VOLTS, 30 WATTS





MARKING DIAGRAM



Y = Year

WW = Work Week

BD180 = Device Code

G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
BD180G	TO-225 (Pb-Free)	500 Units/Box

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Sustaining Voltage (Note 1) (I _C = 0.1 Adc, I _B = 0)	V _{(BR)CEO}	80	_	Vdc
Collector Cutoff Current (V _{CB} = 80 Vdc, I _E = 0)	I _{CBO}	-	1.0	mAdc
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)	I _{EBO}	-	1.0	mAdc
DC Current Gain (I _C = 0.15 A, V _{CE} = 2.0 V) (I _C = 1.0 A, V _{CE} = 2.0 V)	h _{FE}	40 15	250 -	-
Collector–Emitter Saturation Voltage (Note 1) (I _C = 1.0 Adc, I _B = 0.1 Adc)	V _{CE(sat)}	-	0.8	Vdc
Base-Emitter On Voltage (Note 1) (I _C = 1.0 Adc, V _{CE} = 2.0 Vdc)	V _{BE(on)}	-	1.3	Vdc
Current–Gain – Bandwidth Product ($I_C = 250 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$, $f = 1.0 \text{ MHz}$)	f _T	3.0	-	MHz

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

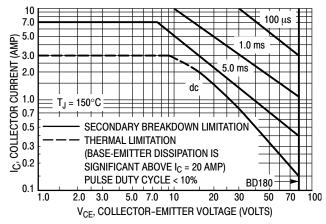


Figure 1. Active Region Safe Operating Area

The Safe Operating Area Curves indicate $I_C - V_{CE}$ limits below which the device will not enter secondary breakdown. Collector load lines for specific circuits must fall within the applicable Safe Area to avoid causing a catastrophic failure. To insure operation below the maximum T_J , power–temperature derating must be observed for both steady state and pulse power conditions.

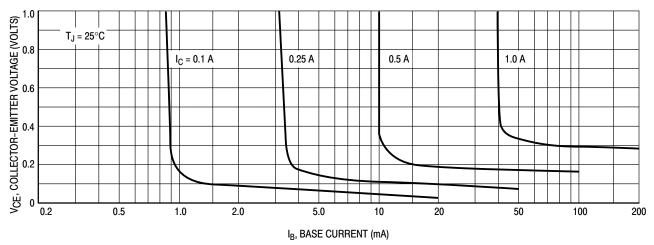
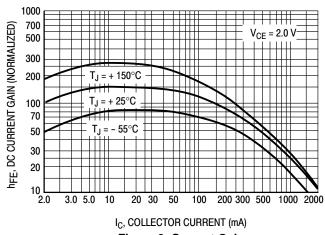


Figure 2. Collector Saturation Region

BD180G



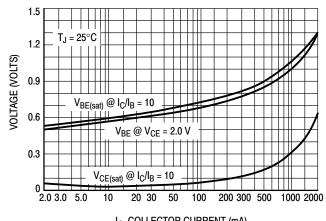


Figure 3. Current Gain

I_C, COLLECTOR CURRENT (mA) **Figure 4. "On" Voltages**

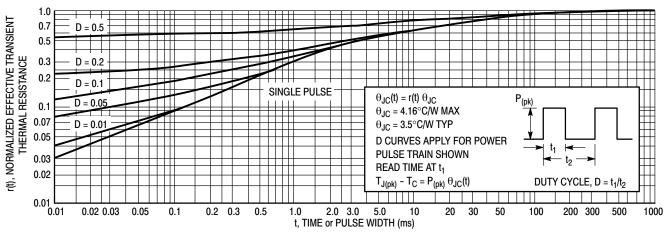
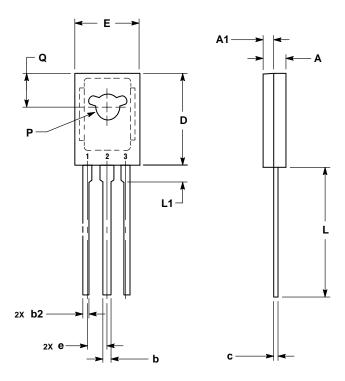


Figure 5. Thermal Response

BD180G

PACKAGE DIMENSIONS

TO-225 CASE 77-09 **ISSUE AB**



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS. 3. NUMBER AND SHAPE OF LUGS OPTIONAL.

MILLIMETERS		
MIN	MAX	
2.40	3.00	
1.00	1.50	
0.60	0.90	
0.51	0.88	
0.39	0.63	
10.60	11.10	
7.40	7.80	
2.04	2.54	
14.50	16.63	
1.27	2.54	
2.90	3.30	
3.80	4.20	
	MIN 2.40 1.00 0.60 0.51 0.39 10.60 7.40 2.04 14.50 1.27 2.90	

STYLE 1:

PIN 1 FMITTER

COLLECTOR BASE

ON Semiconductor and (III) are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, ON semiconductor and war registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC wors the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent—Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implications the product could receive a situation where surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada **Fax**: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81–3–5817–1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

ON Semiconductor: