

TXN/TYN 058 (G) ---> TXN/TYN 1008 (G)

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FEATURES

- HIGH SURGE CAPABILITY
- HIGH ON-STATE CURRENT
- HIGH STABILITY AND RELIABILITY
- TXN Serie : INSULATED VOLTAGE = 2500V(RMS) (UL RECOGNIZED : E81734)

ABSOLUTE RATINGS (limiting values)

The TYN/TXN 058 ---> TYN/TXN 1008 Family of

DESCRIPTION

Silicon Controlled Rectifiers uses a high performance glass passivated chips technology.

This general purpose Family of Silicon Controlled Rectifiers is designed for power supplies up to 400Hz on resistive or inductive load.

Symbol	Parameter	Value	Unit		
IT(RMS)	RMS on-state current (180° conduction angle)	TXN TYN	Tc=100°C Tc=105°C	8	A
IT(AV)	Average on-state current (180° conduction angle,single phase circuit)	TXN TYN	Tc=100°C Tc=105°C	5	A
ITSM	Non repetitive surge peak on-state current	tp=8.3 ms	84	A	
	(Tj initial = 25°C)		tp=10 ms	80	
l ² t	l ² t value tp=10 ms			32	A ² s
di/dt	Critical rate of rise of on-state current Gate supply : IG = 100 mA dig/dt = 1 A/us			50	A/µs
⊺stg Tj	Storage and operating junction temperature range			- 40 to + 150 - 40 to + 125	℃ ℃
ТІ	Maximum lead temperature for soldering during 10 s at 4.5 mm from case			260	°C

Symbol	Parameter	TYN/TXN							Unit
		058	108	208	408	608	808	1008	
VDRM VRRM	Repetitive peak off-state voltage Tj = 125 °C	50	100	200	400	600	800	1000	V

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THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
Rth (j-a)	Junction to ambient		60	°C/W
Rth (j-c) DC	Junction to case for DC	TXN	3.5	•C/W
		TYN	2.5	

GATE CHARACTERISTICS (maximum values)

 $P_{G}(AV) = 1W$ $P_{GM} = 10W$ (tp = 20 µs) $I_{FGM} = 4A$ (tp = 20 µs) $V_{RGM} = 5 V$.

ELECTRICAL CHARACTERISTICS

Symbol	Test Conditions			Value		Unit
				BLANK	G]
IGT	V _D =12V (DC) R _L =33Ω	Tj=25℃	MAX	15	25	mA
VGT	V _D =12V (DC) R _L =33Ω	Tj=25°C	MAX	1.5		V
VGD	V _D =V _{DRM} R _L =3.3kΩ	Tj= 110°C	MIN	0.2		V
tgt	VD=VDRM IG = 40mA dlg/dt = 0.5A/µs	Tj=25°C	TYP	2		μs
١L	IG= 1.2 IGT	Tj=25°C	TYP	50		mA
iΗ	IT= 100mA gate open	Tj=25°C	MAX	30	45	mA
∨тм	ITM= 16A tp= 380µs	Tj=25°C	MAX	1.8		v
IDRM	VDRM Rated	Tj=25°C	МАХ	0.01		mA
IRRM	VRRM Rated	Tj= 110°C				
dV/dt	Linear slope up to VD=67%VDRM gate open	Tj= 110°C	MIN	200	500	V/µs
tq	V _D =67%V _{DRM} H _{TM=} 16A V _R = 25V dH _{TM} /dt=30 A/μs dV _D /dt= 50V/μs	Tj= 110°C	ТҮР	70		μs



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Package	IT(RMS)	VDRM / VRRM	Sensitivity Specification		
	Α	V	BLANK	G	
TXN	8	50	Х	Х	
(Insulated)		100	Х	X	
		200	Х	Х	
		400	Х	Х	
		600	Х	Х	
		800	Х	Х	
		1000	Х	Х	
TYN		50	Х	Х	
(Uninsulated)		100	Х	Х	
		200	Х	Х	
		400	Х	Х	
		600	X	Х	
		800	Х	Х	
		1000	X	Х	

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Fig.1 : Maximum average power dissipation versus average on-state current (TXN).

Fig.2 : Correlation between maximum average power dissipation and maximum allowable temperatures (T_{amb} and T_{Case}) for different thermal resistances heatsink + contact (TXN).

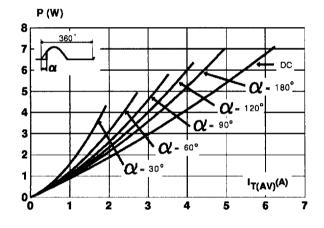


Fig.3 : Maximum average power dissipation versus average on-state current (TYN).

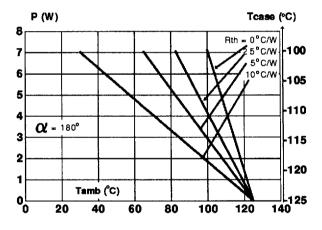


Fig.4 : Correlation between maximum average power dissipation and maximum allowable temperatures (T_{amb} and T_{case}) for different thermal resistances heatsink + contact (TYN).

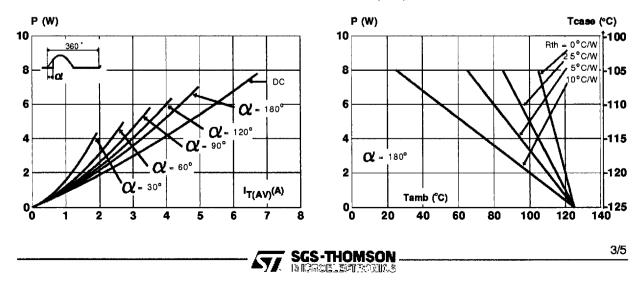


Fig.5 : Average on-state current versus case temperature (TXN).

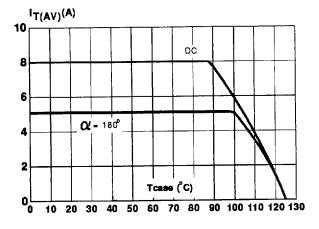


Fig.7 : Relative variation of thermal impedance versus pulse duration.

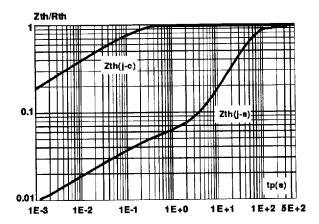


Fig.9 : Non repetitive surge peak on-state current versus number of cycles.

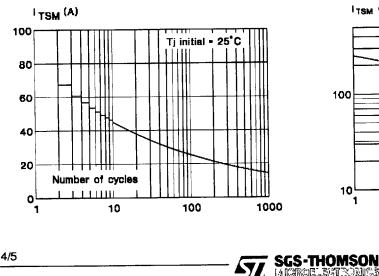


Fig.6 : Average on-state current versus case temperature (TYN).

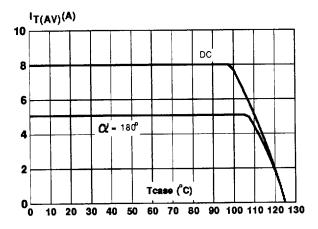


Fig.8 : Relative variation of gate trigger current versus junction temperature.

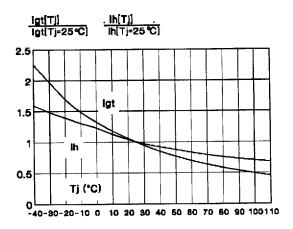
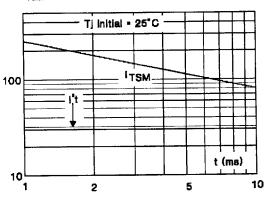
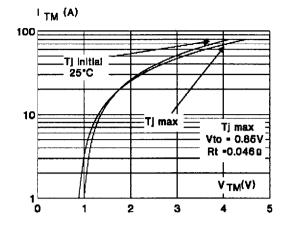


Fig.10 : Non repetitive surge peak on-state current for a sinusoidal pulse with width : $t \le 10$ ms, and corresponding value of l^2t .

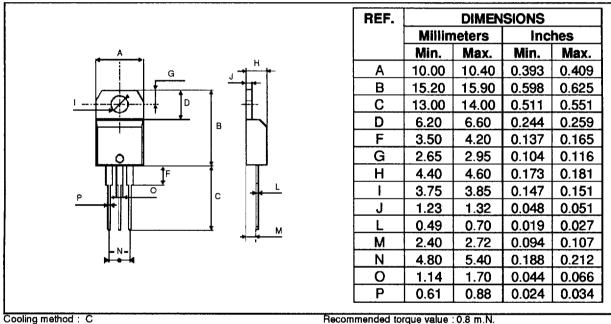


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Fig.11 : On-state characteristics (maximum values).



PACKAGE MECHANICAL DATA TO220AB Plastic



Marking : type number Weight : 2.3 g Recommended torque value : 0.8 m.N. Maximum torque value : 1 m.N.

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