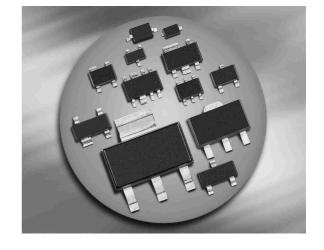


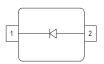
Silicon Variable Capacitance Diodes

- For tuning of extended frequency band in VHF TV / VTR tuners
- High capacitance ratio
- Low series inductance
- Low series resistance
- Excellent uniformity and matching due to "in-line" matching assembly procedure
- Pb-free (RoHS compliant) package





BB639 BB659



Туре	Package	Configuration	L _S (nH)	Marking
BB639	SOD323	single	1.8	yellow S
BB659	SCD80	single	0.6	DE

Maximum Ratings at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Diode reverse voltage	V_{R}	30	V
Peak reverse voltage	V_{RM}	35	
$(R \ge 5k\Omega)$			
Forward current	l _F	20	mA
Operating temperature range	T_{op}	-55 150	°C
Storage temperature	$T_{\rm stg}$	-55 150	

1

2011-06-15



Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

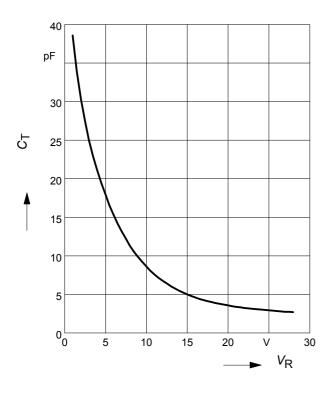
Parameter	Symbol		Unit		
		min.	typ.	max.	
DC Characteristics				•	
Reverse current	I_{R}				nA
V _R = 30 V		-	-	10	
V_{R} = 30 V, T_{A} = 85 °C		-	-	200	
AC Characteristics					
Diode capacitance	C _T				pF
$V_{R} = 1 \text{ V}, f = 1 \text{ MHz}$		36	38.3	40	
$V_{R} = 2 \text{ V}, f = 1 \text{ MHz}$		27.7	29.75	31.8	
$V_{R} = 25 \text{ V}, f = 1 \text{ MHz}$		2.5	2.85	3.2	
V_{R} = 28 V, f = 1 MHz		2.4	2.6	2.9	
Capacitance ratio	C _{T1} /C _{T28}	13.5	14.7	-	
$V_{R} = 1 \text{ V}, V_{R} = 28 \text{ V}, f = 1 \text{ MHz}$					
Capacitance ratio	C_{T2}/C_{T25}	9.8	10.4	-	
$V_{R} = 2 \text{ V}, V_{R} = 25 \text{ V}, f = 1 \text{ MHz}$					
Capacitance matching ¹⁾	$\Delta C_{T}/C_{T}$				%
V_{R} = 1 V, V_{R} = 28 V, f = 1 MHz, 7 diode sequen	d				
BB639		-	-	2.5	
V_{R} = 1 V, V_{R} = 28 V, f = 1 MHz, 4 diode sequen	d				
BB659		-	0.3	1	
V_{R} = 1 V, V_{R} = 28 V, f = 1 MHz, 7 diode sequen	d				
BB659		-	0.4	2	
Series resistance	$r_{\rm S}$	-	0.65	0.7	Ω
$V_{R} = 5 \text{ V}, f = 470 \text{ MHz}$					

¹For details please refer to Application Note 047.

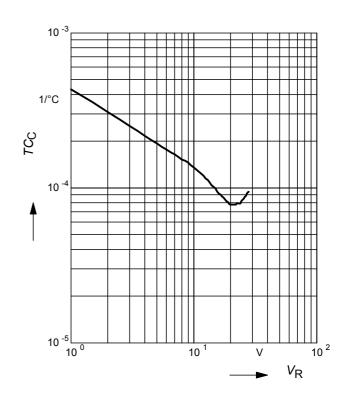


Diode capacitance $C_T = f(V_R)$

f = 1MHz

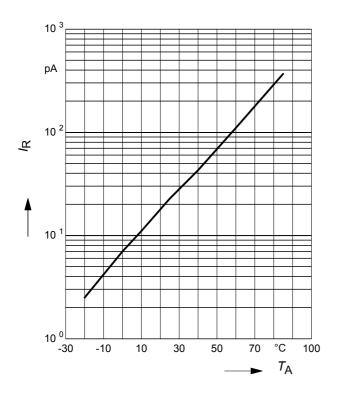


Temperature coefficient of the diode capacitance $T_{Cc} = f(V_R)$



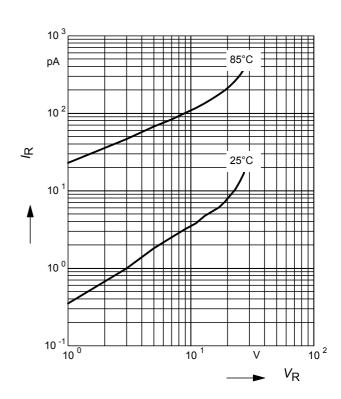
Reverse current $I_R = f(T_A)$

 $V_{R} = 28V$



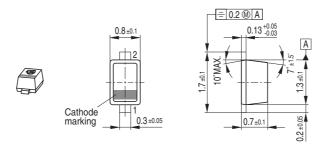
Reverse current $I_R = f(V_R)$

 T_A = Parameter





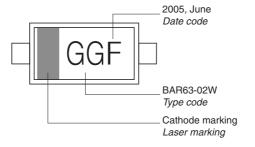
Package Outline



Foot Print



Marking Layout (Example)

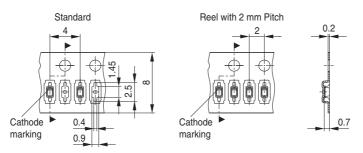


Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel

Reel ø180 mm = 8.000 Pieces/Reel (2 mm Pitch)

Reel ø330 mm = 10.000 Pieces/Reel



4



Date Code marking for discrete packages with one digit (SCD80, SC79, SC75¹⁾) CES-Code

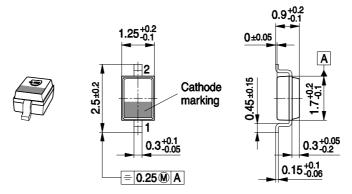
Month	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014
01	а	р	Α	Р	а	р	Α	Р	а	р	Α	Р
02	b	q	В	Q	b	q	В	Q	b	q	В	Q
03	С	r	С	R	С	r	С	R	С	r	С	R
04	d	S	D	S	d	S	D	S	d	S	D	S
05	е	t	Е	T	е	t	Е	Т	е	t	Е	Т
06	f	u	F	U	f	u	F	U	f	u	F	U
07	g	٧	G	V	g	٧	G	٧	g	٧	G	V
08	h	Х	Н	Х	h	Х	Н	Х	h	Х	Н	Х
09	j	У	J	Υ	j	у	J	Υ	j	У	J	Y
10	k	Z	K	Z	k	Z	K	Z	k	Z	K	Z
11	I	2	L	4	I	2	L	4	I	2	L	4
12	n	3	N	5	n	3	N	5	n	3	N	5

¹⁾ New Marking Layout for SC75, implemented at October 2005.

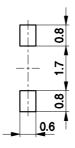
5 2011-06-15



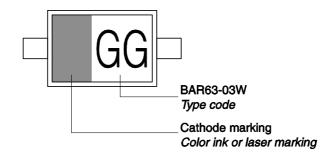
Package Outline



Foot Print

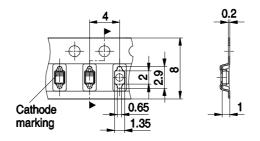


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel



6



Edition 2009-11-16

Published by Infineon Technologies AG 81726 Munich, Germany

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7

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