





Features:

- Low profile package.
- Excellent clamping capability.
- Glass passivity junction.
- V_z-tolerance ±5%.

Application:

Voltage stabilization.

Absolute Maximum Ratings T_j = 25°C

Parameter	Test Conditions	Symbol	Value	Unit	
Power dissipation	$T_{amb} \le 75^{\circ}C$	P _v	5	W	
Z-current	-	ا _z	P _v / V _z	mA	
Junction temperature	-	Tj	150	°C	
Storage temperature range	-	T _{stg}	-65 to +150		

Stresses exceeding maximum ratings may damage the device. Maximum ratings are stress ratings only. Functional operation above the recommended operating conditions is not implied. Extended exposure to stresses above the recommended operating conditions may affect device reliability.

Electrical Characteristics T_i = 25°C

Parameter	Test Conditions	Symbol	Maximum	Unit
Forward voltage	I _F = 1A	V _F	1.2	V





Specification Table

V _{Znom} 1)	l _{ZT} fo	or Z _{zT}	Z _{ZK at} I _{ZK} = 1 mA	I _{R a}	it V _R	I _R ²⁾	ΔV _Z ³⁾	I _{ZM} ⁴⁾	Part Number
V	mA	Ω	Ω	μΑ	V	Α	v	mA	_
5.1	240	1.5	400	1	1	14.4	0.39	930	1N5338B
5.6	220	1	400	1	2	13.4	0.25	865	1N5339B
6	200	1	300	1	3	12.7	0.19	790	1N5340B
6.2	200	1	200	1	3	12.4	0.1	765	1N5341B
6.8	175	1	200	10	5.2	11.5	0.15	700	1N5342B
7.5	175	1.5	200	10	5.7	10.7	0.15	630	1N5343B
8.2	150	1.5	200	10	6.2	10	0.2	580	1N5344B
9.1	150	2	150	7.5	6.9	9.2	0.22	520	1N5346B
10	125	2	125	5	7.6	8.6	0.22	475	1N5347B
11	125	2.5	125	5	8.4	8	0.25	430	1N5348B
12	100	2.5	125	2	9.1	7.5	0.25	395	1N5349B
13	100	2.5	100	1	9.9	7	0.25	365	1N5350B
14	100	2.5	75	1	10.6	6.7	0.25	340	1N5351B
15	75	2.5	75	1	11.5	6.3	0.25	315	1N5352B
16	75	2.5	75	1	12.2	6	0.3	295	1N5353B
17	70	2.5	75	0.5	12.9	5.8	0.35	280	1N5354B
18	65	2.5	75	0.5	13.7	5.5	0.4	265	1N5355B
22	50	3.5	75	0.5	16.7	4.7	0.45	216	1N5358B
24	50	3.5	100	0.5	18.2	4.4	0.55	198	1N5359B
27	50	5	120	0.5	20.6	4.1	0.6	176	1N5361B
30	40	8	140	0.5	22.8	3.7	0.6	158	1N5363B
33	40	10	150	0.5	25.1	3.5	0.6	144	1N5364B
36	30	11	160	0.5	27.4	3.3	0.65	132	1N5365B

¹⁾ Zener voltage (V_z): Based on DC-measurement at thermal equilibrium while maintaining the lead temperature (T_L) at 25°C, 9.5 mm (3/8") from the diode body.

²⁾ Surge current (I_R) is specified as the maximum allowable peak, non-recurrent square-wave current with a plus width, PW, of 8.3 ms.

³⁾ Voltage regulation (ΔV_z): Test conditions for voltage are as below, V_z measurements are made at 10% and then at 50% of the I_z max value listed in the electrical characteristics table. The test current time duration for each V_z measurements is 40 ±10 ms ($T_A = 25^{\circ}C + 8, -2^{\circ}C$).

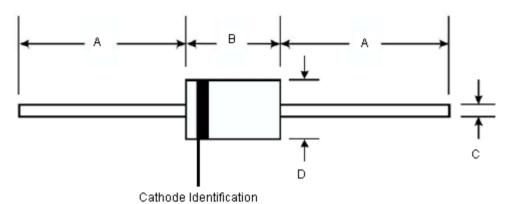
⁴⁾ Maximum regulator current (I_{ZM}): The maximum current shown is based on the maximum voltage of a 5% type unit; therefore, it applies only to the B-suffix device. The actual I_{ZM} for any device may not exceed the value of 5 watts divided by the actual V_z of the device. T_L = 75°C at 9.5 mm (3/8") from the diode body.

http://www.farnell.com http://www.newark.com http://www.cpc.co.uk



Zener Diode





Case : moulded plastic DO-15 Polarity : Cathode band

Dimensions							
Dimensions	Inc	hes	mm				
Dimensions	Minimum	um Maximum Minimum		Maximum			
A	1	-	25.4	-			
В	0.23	0.3	5.8	7.6			
С	0.026	0.034	0.7	0.9			
D	0.104	0.14	2.6	3.6			

Disclaimer This data sheet and its contents (the "Information") belong to the Premier Farnell Group (the "Group") or are licensed to it. No licence is granted for the use of it other than for information purposes in connection with the products to which it relates. No licence of any intellectual property rights is granted. The Information is subject to change without notice and replaces all data sheets previously supplied. The Information supplied is believed to be accurate but the Group assumes no responsibility for its accuracy or completeness, any error in or omission from it or for any use made of it. Users of this data sheets previously supplied. Information and the suitability of the products for their purpose and not make any assumptions based on information included or omitted. Liability for loss or damage resulting from any reliance on the Information or use of it (including liability resulting from regigence or where the Group was aware of the group. © Premier Farnell plc 2011.



