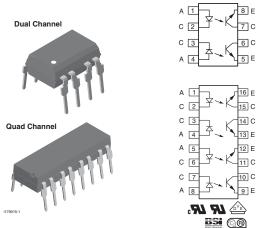


Optocoupler, Phototransistor Output (Dual, Quad Channel)



DESCRIPTION

The ILD74, ILQ74 is an optically coupled pair with a GaAlAs infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output.

The ILD74, ILQ74 is especially for driving medium-speed logic, where it may be used to eliminate troublesome ground loop and noise problems. Also it can be used to replace relays and transformers in many digital interface applications, as well as analog applications such as CTR modulation.

The ILD74 has two isolated channels in a single DIP package; the ILQ74 has four isolated channels per package.

FEATURES

- ILD74, ILQ74 TTL compatible
- Transfer ratio, 35 % typical
- Coupling capacitance, 0.5 pF
- Single, dual, and quad channel
- Industry standard DIP packages
- industry standard DIP packages
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

Pb-free



RoHS

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- CSA 93751
- BSI IEC 60950: IEC 60065
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending available with option 1
- FIMKO

ORDERING INFORMATION		
	DIP-#	Option 6
I L x 7 4 - X 0 # # T		
PART NUMBER PACKAGE OPTION TAPE AND REEL	7.62 mm Option 7	10.16 mm Option 9
x = D (Dual) or Q (Quad)	> 0.7 mm	> 0.1 mm

AGENCY CERTIFIED/PACKAGE	DUAL CHANNEL	QUAD CHANNEL		
AGENCY CENTIFIED/PACKAGE	CTR (%)			
UL, CSA, BSI, FIMKO	≥ 12.5	≥ 12.5		
DIP-8	ILD74	-		
SMD-8, option 9	ILD74-X009T (1)	-		
DIP-16	-	ILQ74		
SMD-16, option 9	-	ILQ74-X009T (1)		
VDE, UL, CSA, BSI, FIMKO	≥ 12.5	≥ 12.5		
DIP-8	ILD74-X001	-		
DIP-8, 400 mil, option 6	ILD74-X016	-		
SMD-8, option 7	ILD74-X017T	-		
DIP-16	-	ILQ74-X001		

Notes

- Additional options may be possible, please contact sales office.
- (1) Also available in tubes, do not put "T" on the end.

Optocoupler, Phototransistor Output (Dual, Quad Channel)



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT			
INPUT								
Peak reverse voltage			V _R	3	V			
Forward continuous current			I _F	60	mA			
Power dissipation			P _{diss}	100	mW			
Derate linearly from 55 %				1.33	mW/°C			
OUTPUT								
Collector emitter breakdown voltage			BV _{CEO}	20	V			
Emitter collector breakdown voltage			BV _{ECO}	5	V			
Collector base breakdown voltage			BV _{CBO}	70	V			
Power dissipation			P _{diss}	150	mW			
Derate linearly from 25 °C				2	mW/°C			
COUPLER								
Isolation test voltage	t = 1 s		V _{ISO}	5300	V_{RMS}			
Isolation resistance	$V_{IO} = 500 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$		R _{IO}	≥ 10 ¹²	Ω			
isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C		R _{IO}	≥ 10 ¹¹	Ω			
Total package discinction		ILD74	P _{tot}	400	mW			
Total package dissipation		ILQ74	P _{tot}	500	mW			
Devete linearly from 25 °C		ILD74		5.33	mW/°C			
Derate linearly from 25 °C		ILQ74		6.67	mW/°C			
Creepage distance				≥ 7	mm			
Clearance distance				≥ 7	mm			
Storage temperature			T _{stg}	- 55 to + 150	°C			
Operating temperature			T _{amb}	- 55 to + 100	°C			
Lead soldering time at 260 °C				10	S			

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	$I_F = 20 \text{ mA}$	V _F		1.3	1.5	V	
Reverse current	V _R = 3 V	I _R		0.1	100	μΑ	
Capacitance	$V_R = 0 V$	Co		25		pF	
OUTPUT							
Collector emitter breakdown voltage	I _C = 1 mA	BV _{CEO}	20	50		V	
Collector emitter leakage current	$V_{CE} = 5 \text{ V}, I_{F} = 0 \text{ A}$	I _{CEO}		5	500	nA	
Capacitance collector emitter	$V_{CE} = 0 \text{ V}, f = 1 \text{ Hz}$	C _{CE}		10		pF	
COUPLER							
Saturation voltage, collector emitter	$I_C = 2 \text{ mA}, I_F = 16 \text{ mA}$	V _{CEsat}		0.3	0.5	V	
Resistance (input to output)		R _{IO}		100		GΩ	
Capacitance (input to output)		C _{IO}		0.5		pF	

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.



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CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
DC current transfer ratio	$I_F = 16 \text{ mA}, V_{CE} = 5 \text{ V}$	CTR _{DC}	12.5	35		%

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Switching times	$R_L = 100 \Omega$, $V_{CE} = 10 V$, $I_C = 2 mA$	t _{on} , t _{off}		3		μs

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

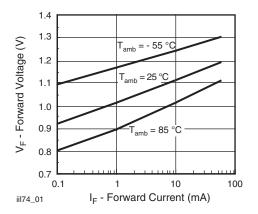


Fig. 1 - Forward Voltage vs. Forward Current

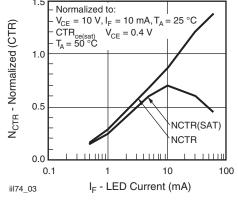


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

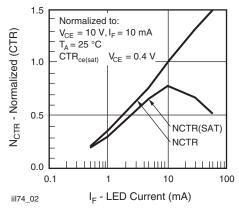


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

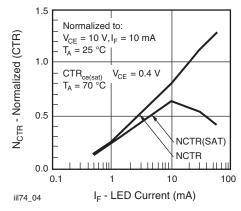


Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current

Optocoupler, Phototransistor Output (Dual, Quad Channel)



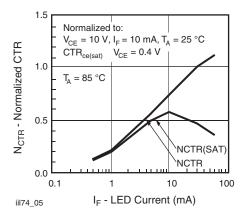


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

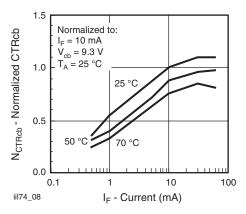


Fig. 8 - Normalized CTR_{cb} vs. LED Current and Temperature

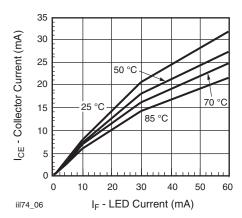


Fig. 6 - Collector Emitter Current vs. Temperature and LED Current

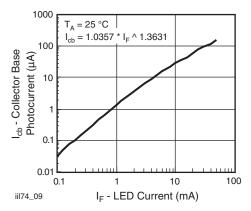


Fig. 9 - Collector Base Photocurrent vs. LED Current

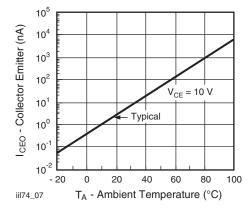


Fig. 7 - Collector Emitter Leakage Current vs.Temperature

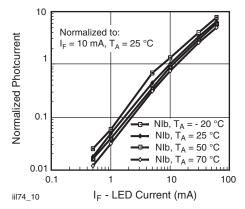


Fig. 10 - Normalized Photocurrent vs. IF and Temperature



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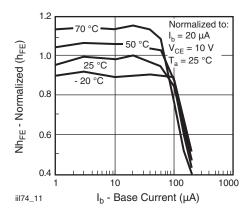


Fig. 11 - Normalized Non-Saturated h_{FE} vs. Base Current and Temperature

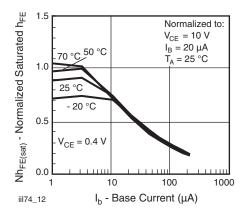


Fig. 12 - Normalized Saturated h_{FE} vs. Base Current and Temperature

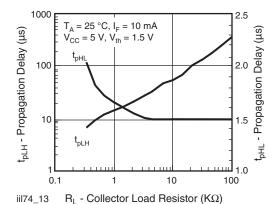
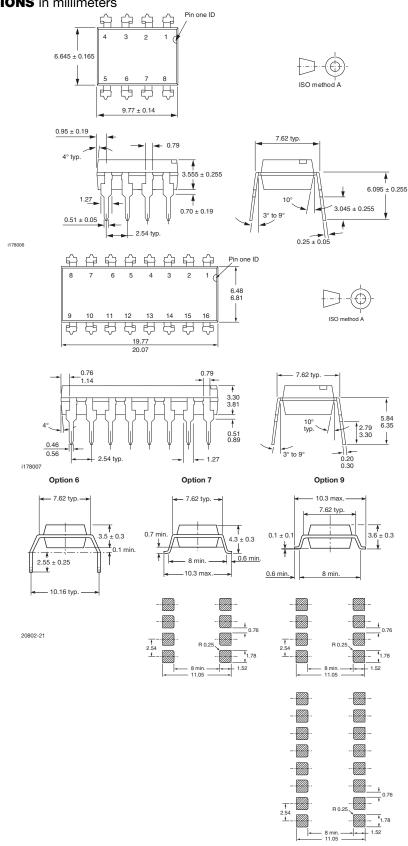


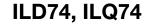
Fig. 13 - Propagation Delay vs. Collector Load Resistor

Optocoupler, Phototransistor Output (Dual, Quad Channel)



PACKAGE DIMENSIONS in millimeters







Optocoupler, Phototransistor Output (Dual, Quad Channel)

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PACKAGE MARKING





Notes

- Only options 1 and 7 reflected in the package marking
- The VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking



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Vishay

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