

#### 500mA/50V Digital transistor (with built-in resistors)

Parameter	Value
V <sub>CC</sub>	-50V
I <sub>C</sub>	-500mA
R <sub>1</sub>	2.2kΩ
R <sub>2</sub>	10kΩ

# SOT-346 SC-59

(SMT3)

#### Features

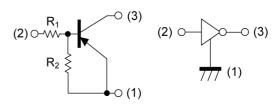
- 1)Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.(see equivalent circuit)
- 2)The bias resistors consist of thin-film resistors with complete isolation to allow positive

biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.

3)Only the on/off conditions need to be set for operation, making the device desigh easy.

#### •Inner circuit

Outline



- (1) GND (+) (EMITTER)
- (2) IN (BASE)
- (3) OUT (COLLECTOR)

## Application

INVERTER, INTERFACE, DRIVER

### Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTB123YK	SOT-346 (SMT3)	2928	T146	180	8	3000	F52

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V <sub>CC</sub>	-50	V
Input voltage	V <sub>IN</sub>	-12 to 5	V
Collector current	I <sub>C</sub> *1	-500	mA
Power dissipation	P <sub>D</sub> *2	200	mW
Junction temperature	Tj	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

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Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input valtage	$V_{I(off)}$	$V_{CC} = -5V, I_{O} = -100\mu A$	-	-	-0.3	- v	
Input voltage	V <sub>I(on)</sub>	$V_O = -0.3V$ , $I_O = -20$ mA	-2.0	-	-		
Output voltage	V <sub>O(on)</sub>	$I_0 = -50 \text{mA}, I_1 = -2.5 \text{mA}$	-	-100	-300	mV	
Input current	l <sub>l</sub>	V <sub>I</sub> = -5V	-	-	-3.6	mA	
Output current	I <sub>O(off)</sub>	$V_{CC} = -50V, V_{I} = 0V$	-	-	-500	nA	
DC current gain	G <sub>I</sub> *3	$V_O = -5V$ , $I_O = -50$ mA	56	-	-	-	
Input resistance	R <sub>1</sub>	-	1.54	2.2	2.86	kΩ	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	-	3.6	4.5	5.5	-	
Transition frequency	f <sub>T</sub> *1	V <sub>CE</sub> = -10V, I <sub>E</sub> = 50mA, f = 100MHz	-	200	-	MHz	

<sup>\*1</sup> Characteristics of built-in transistor

<sup>\*2</sup> Each terminal mounted on a reference land

<sup>\*3</sup> Pulsed

## ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

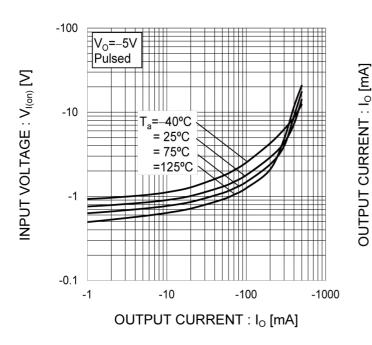


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

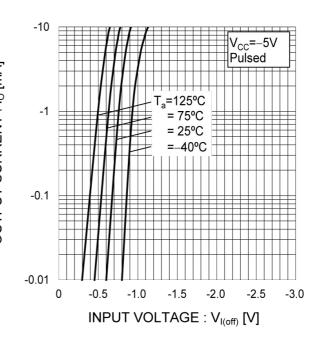


Fig.3 Output Current vs. Output Voltage

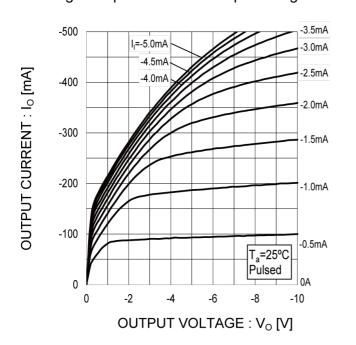
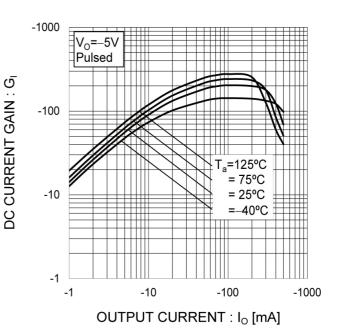


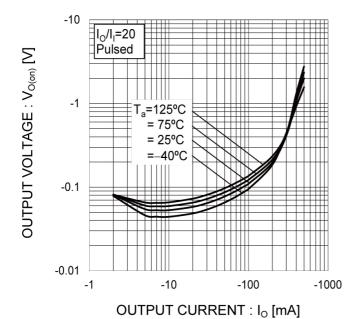
Fig.4 DC Current Gain vs. Output Current



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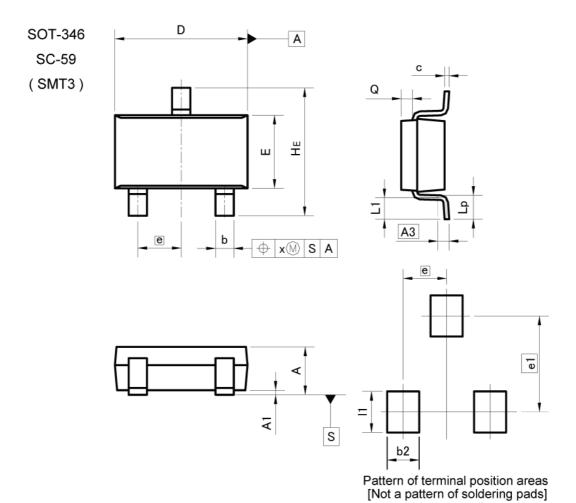
# ●Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.5 Output Voltage vs. Output Current





## Dimensions



DIM	DIM MILIMETERS		INC	ICHES	
DIM	MIN	MAX	MIN	MAX	
Α	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A3	0.3	25	0.0	10	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.0	37	
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
х	-,	0.10	-	0.004	
У	- ,,	0.10	c=-	0.004	

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b2		0.60	_	0.024	
e1	2.10		0.083		
- 11	-,:	0.90	-	0.035	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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