



N-Ch 80V Fast Switching MOSFETs

Description

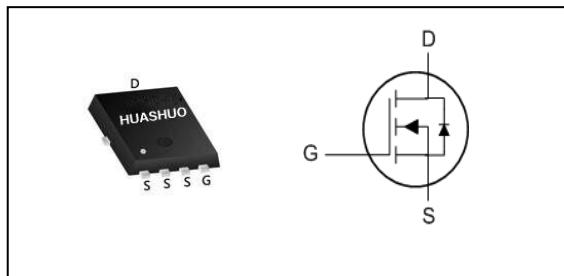
The HSBA8074A is the high cell density SGT N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous rectification applications.

The HSBA8074A meet the RoHS and Halogen-Free compliant product requirement, 100% EAS guaranteed with full function reliability approved.

Product Summary

V _{DS}	80	V
R _{DS(ON),typ}	2.3	mΩ
I _D	100	A

PRPAK5X6 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	80	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _c =25°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	100	A
I _D @T _c =100°C	Continuous Drain Current, V _{GS} @ 10V ^{1,6}	95	A
I _{DM}	Pulsed Drain Current ²	250	A
EAS	Single Pulse Avalanche Energy ³	784	mJ
I _{AS}	Avalanche Current	56	A
P _D @T _c =25°C	Total Power Dissipation ⁴	126	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-Ambient ¹	---	55	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	1	°C/W



Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	80	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=20\text{A}$	---	2.3	3	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}, \text{I}_D=250\mu\text{A}$	2	2.8	4	V
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=64\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=25^{\circ}\text{C}$	---	---	1	μA
		$\text{V}_{\text{DS}}=64\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{T}_J=55^{\circ}\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
R_g	Gate Resistance	$\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1.4	---	Ω
Q_g	Total Gate Charge (10V)	$\text{V}_{\text{DS}}=64\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=20\text{A}$	---	104	---	nC
Q_{gs}	Gate-Source Charge		---	24	---	
Q_{gd}	Gate-Drain Charge		---	29	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=40\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{R}_g=3\Omega, \text{I}_D=20\text{A}$	---	22	---	ns
T_r	Rise Time		---	16	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	51	---	
T_f	Fall Time		---	16	---	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=45\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	6580	---	pF
C_{oss}	Output Capacitance		---	1101	---	
Crss	Reverse Transfer Capacitance		---	50	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5}	$\text{V}_{\text{G}}=\text{V}_{\text{D}}=0\text{V}$, Force Current	---	---	100	A
V_{SD}	Diode Forward Voltage ²	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=\text{A}, \text{T}_J=25^{\circ}\text{C}$	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $\text{V}_{\text{DD}}=50\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{L}=0.5\text{mH}, \text{I}_{\text{AS}}=56\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.
- 6.The maximum current rating is package limited.



Typical Characteristics

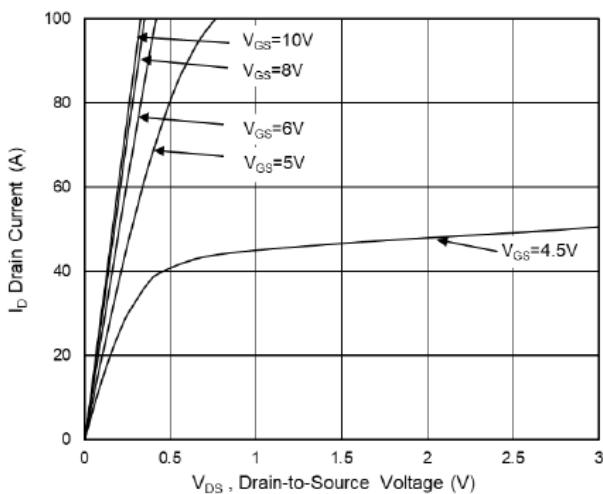


Fig.1 Typical Output Characteristics

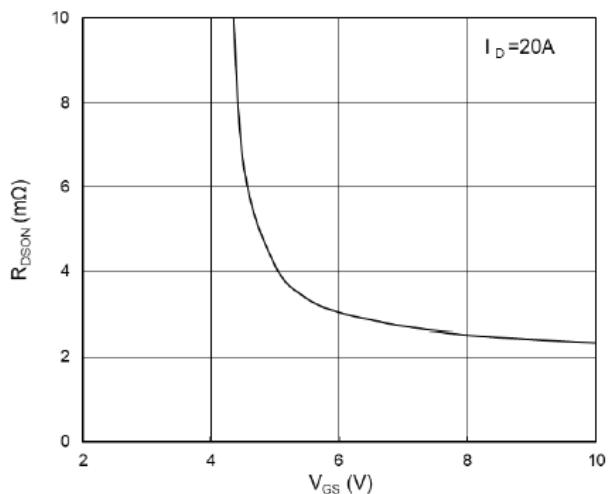


Fig.2 On-Resistance vs G-S Voltage

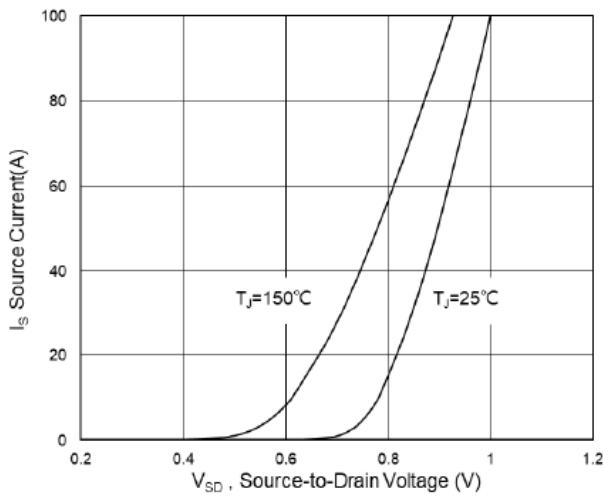


Fig.3 Source Drain Forward Characteristics

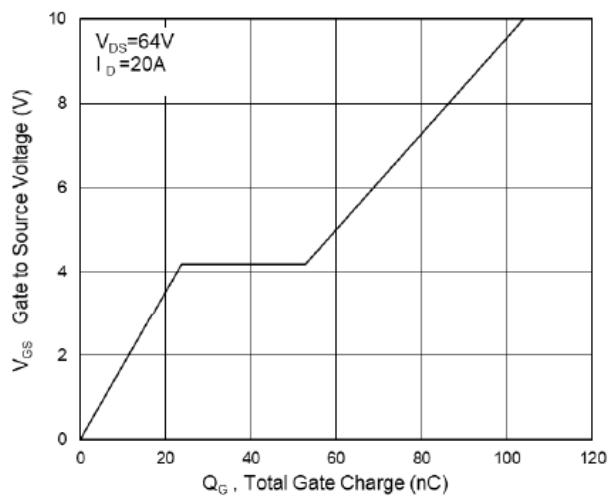


Fig.4 Gate-Charge Characteristics

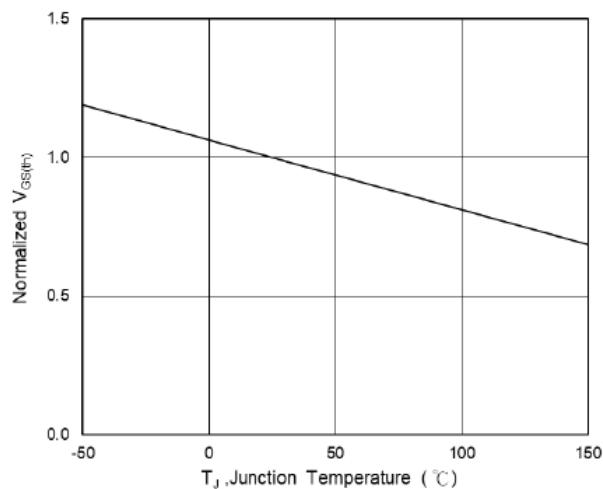


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

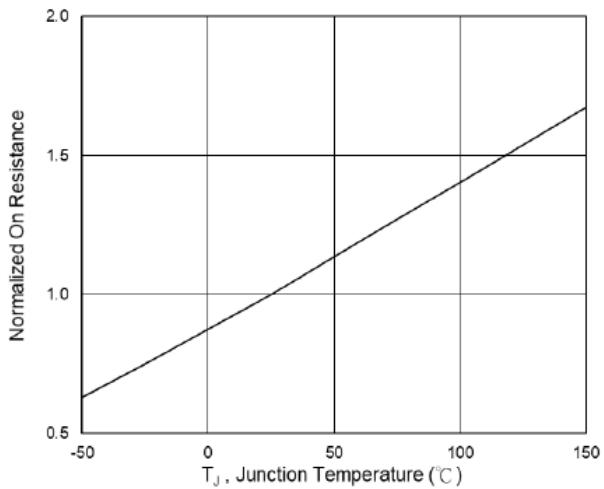


Fig.6 Normalized $R_{DS(on)}$ vs. T_J



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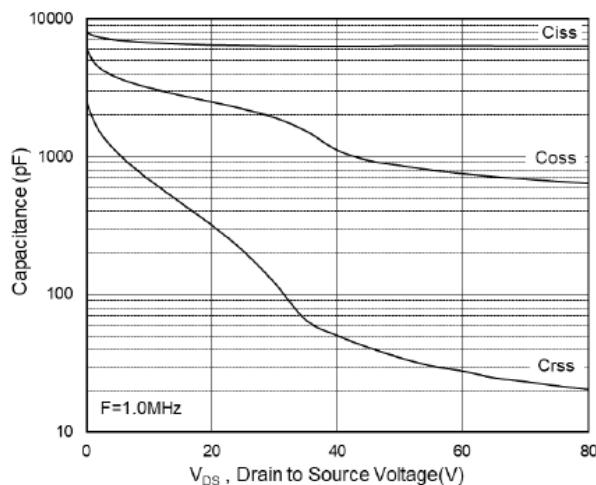


Fig.7 Capacitance

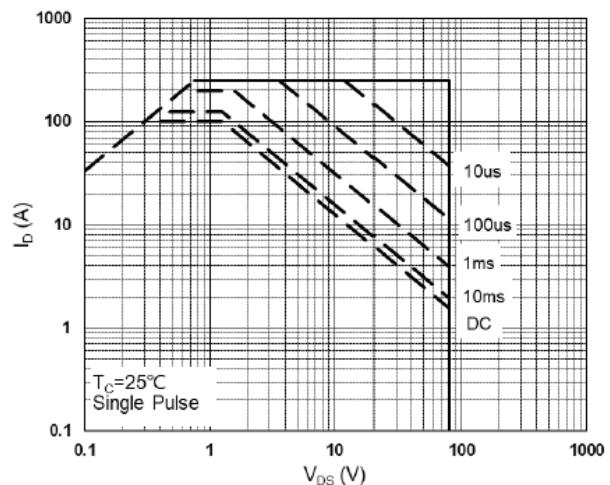


Fig.8 Safe Operating Area

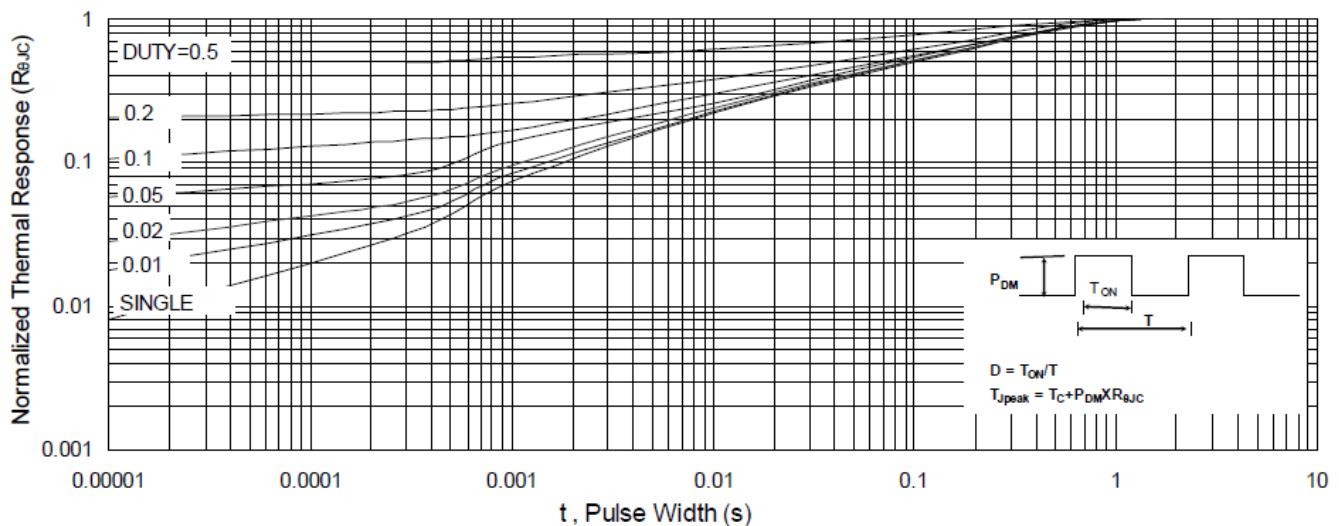


Fig.9 Normalized Maximum Transient Thermal Impedance

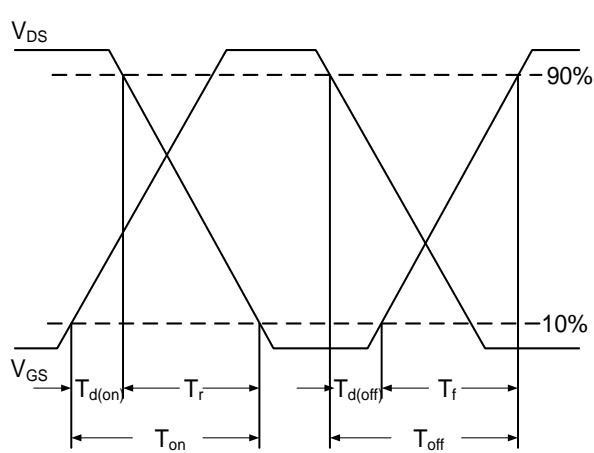


Fig.10 Switching Time Waveform

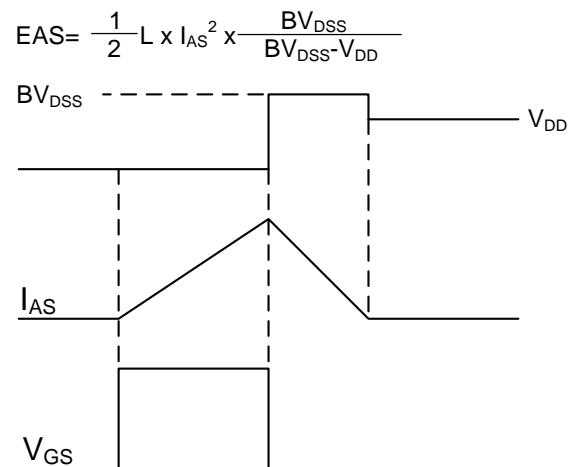
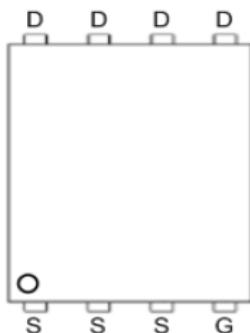
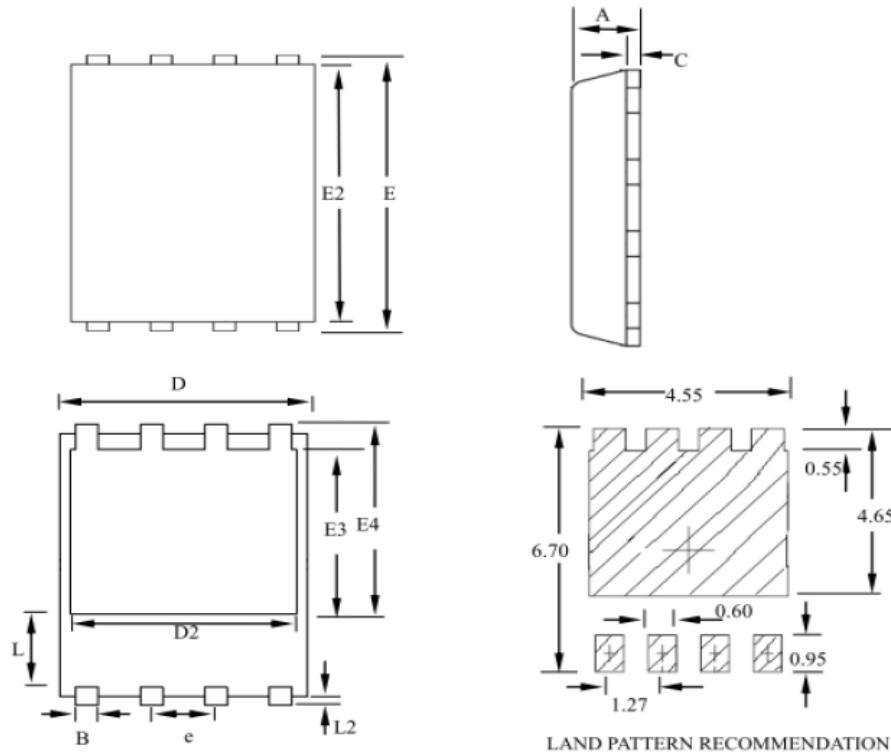


Fig.11 Unclamped Inductive Switching Waveform



Ordering Information

Part Number	Package code	Packaging
HSBA8074A	PRPAK5*6	3000/Tape&Reel



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	--	1.20	0.031	--	0.047
B	0.30	--	0.51	0.012	--	0.020
C	0.15	--	0.35	0.006	--	0.014
D	4.80	--	5.30	0.189	--	0.209
D2	3.61	--	4.35	0.142	--	0.171
E	5.90	--	6.35	0.232	--	0.250
E2	5.42	--	5.90	0.213	--	0.232
E3	3.23	--	3.90	0.127	--	0.154
E4	3.69	--	4.55	0.145	--	0.179
L	0.61	--	1.80	0.024	--	0.071
L2	0.05	--	0.36	0.002	--	0.014
e	--	1.27	--	--	0.050	--