

# PROTECTION PRODUCTS - MicroClamp™

#### Description

The  $\mu$ Clamp<sup>TM</sup> series of TVS arrays are designed to protect sensitive electronics from damage or latch-up due to ESD. It is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and other portable electronics. It features large cross-sectional area junctions for conducting high transient currents. They offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

The µClamp<sup>™</sup>1211T is in a 2-pin, RoHS/WEEE compliant, SLP1006P2T package. It measures 1.0 x 0.6 mm with a nominal height of only 0.4mm. The leads are spaced at a pitch of 0.65mm and are finished with lead-free NiPdAu. Each device will protect one line operating at 12 volts. It gives the designer the flexibility to protect single lines in applications where arrays are not practical. They may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 (±15kV air, ±8kV contact discharge). The combination of small size and high ESD surge capability makes them ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

#### Features

- Transient protection for data lines to
  IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
  IEC 61000-4-4 (EFT) 40A (tp = 5/50ns)
  Cable Discharge Event (CDE)
- Ultra-small package (1.0 x 0.6 x 0.4mm)
- Protects one data or power line
- Low reverse current: <1nA typical (VR=12V)</p>
- ◆ Working voltage: 12V / +12V
- ◆ Low capacitance: 3pF typical
- Solid-state silicon-avalanche technology

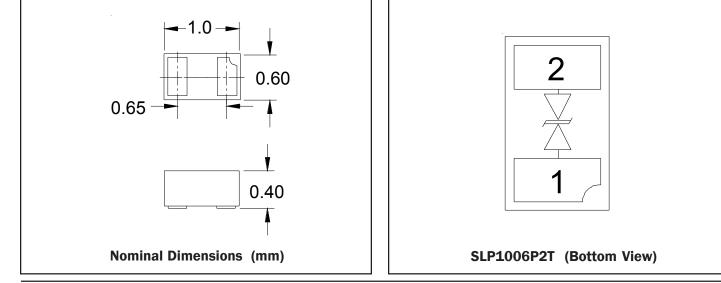
#### Mechanical Characteristics

- SLP1006P2T package
- RoHS/WEEE Compliant
- Nominal Dimensions: 1.0 x 0.6 x 0.4 mm
- Lead Finish: NiPdAu
- Molding compound flammability rating: UL 94V-0
- Marking : Marking code, cathode band
- Packaging : Tape and Reel

#### Applications

- Cellular Handsets & Accessories
- FM Antennas
- RFID
- Keypads, Side Keys, LCD Displays
- Battery, Power Lines
- Notebooks & Desktop Computers
- Portable Instrumentation

### Schematic & PIN Configuration



Downloaded from Arrow.com.

**Dimensions** 



# **PROTECTION PRODUCTS**

# Absolute Maximum Rating

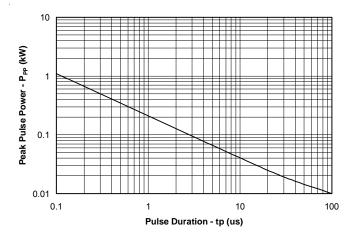
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Rating	Symbol	Value	Units
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V <sub>ESD</sub>	+/- 18 +/- 12	kV
Operating Temperature	T,	-55 to +125	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

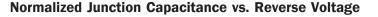
# Electrical Characteristics (T=25°C)

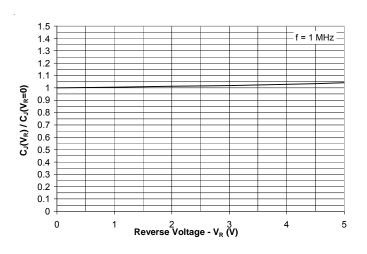
		-				
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>	Pin 1 to 2 or 2 to 1			12	V
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>t</sub> = 1mA Pin 1 to 2 or 2 to 1	13.3	16	19	V
Reverse Leakage Current	۱ <sub>R</sub>	V <sub>RWM</sub> = 12V, T=25°C Pin 1 to 2 or 2 to 1		<1	50	nA
Clamping Voltage	V <sub>c</sub>	I <sub>PP</sub> = 1A, tp = 8/20μs Pin 1 to 2 or 2 to 1			27	V
Junction Capacitance	C <sub>j</sub>	V <sub>R</sub> = OV, f = 1MHz		3	7	pF

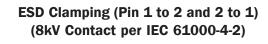


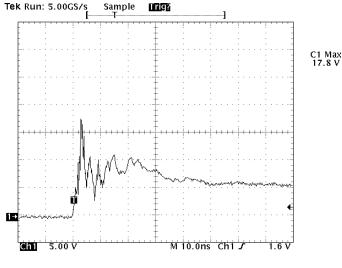
Non-Repetitive Peak Pulse Power vs. Pulse Time





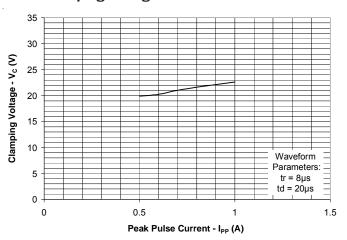




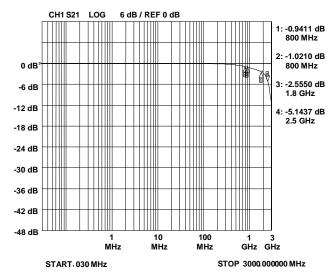


Note: Data is taken with a 10x attenuator

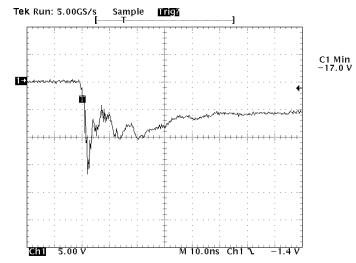
**Clamping Voltage vs. Peak Pulse Current** 

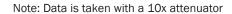


**Typical Insertion Loss (S21)** 



ESD Clamping (Pin 1 to 2 and 2 to 1) (-8kV Contact per IEC 61000-4-2)









## **PROTECTION PRODUCTS**

### Applications Information

#### **Device Connection Options**

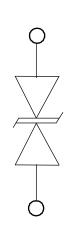
These TVS diodes are designed to protect one data, I/O, or power supply line. The device is bidirectional and may be used on lines where the signal polarity can go above and below ground.

# **Circuit Board Layout Recommendations for Suppression of ESD.**

Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

- Place the TVS near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the TVS and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.



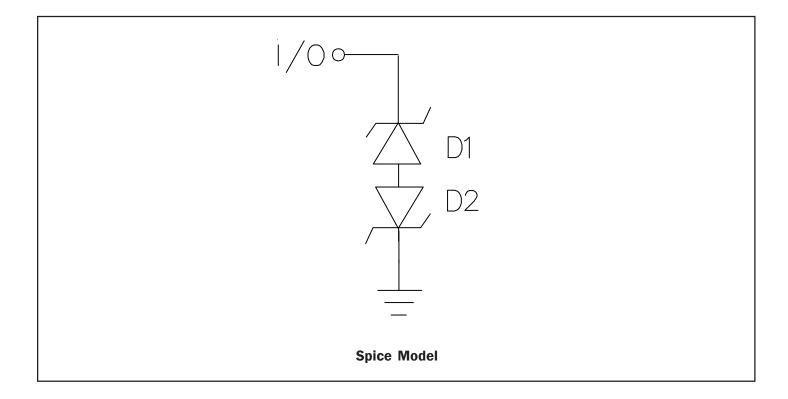






## **PROTECTION PRODUCTS**

Applications Information - Spice Model



uClamp1211T Spice Parameters							
Parameter	Unit	D1 (TVS)	D2 (TVS)				
IS	Amp	2.21E-14	2.21E-14				
BV	Volt	16.4	16.4				
L	Volt	0.7	0.7				
RS	Ohm	2.7	2.7				
IBV	Amp	1E-3	1E-3				
CJO	Farad	6E-12	6E-12				
TT	sec	2.541E-9	2.541E-9				
М		0.06	0.06				
N		1.1	1.1				
EG	eV	1.11	1.11				



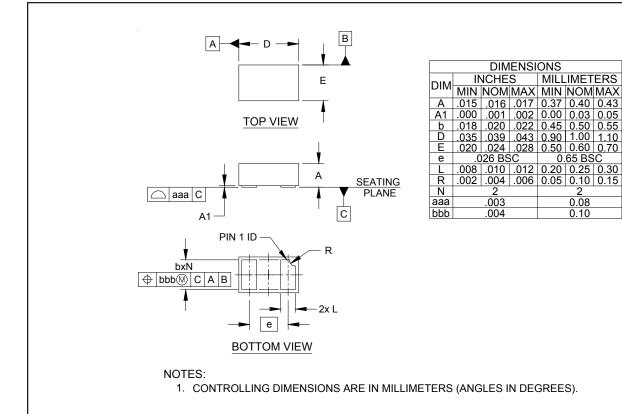
0.08

0.10

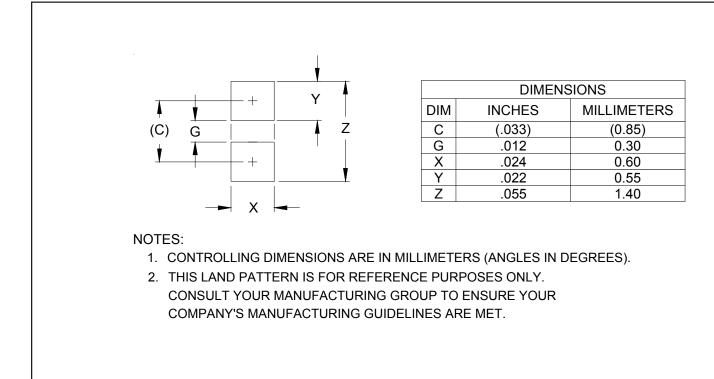


#### PROTECTION PRODUCTS

Outline Drawing - SLP1006P2T



# Land Pattern - SLP1006P2T



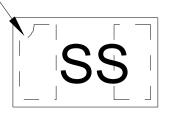


# uClamp1211T

**PROTECTION PRODUCTS** 

Marking Code

# PIN 1 ID



# Ordering Information

Part Number	Working	Qty per	Reel	
	Voltage	Reel	Size	
uClamp1211T.TCT	12V	3,000	7 Inch	

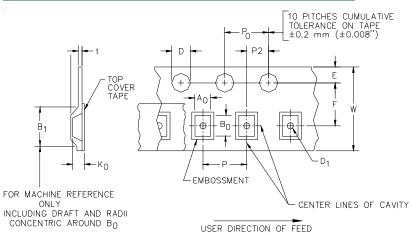
Notes:

1) This is a lead-free, RoHS/WEEE compliant product MicroClamp, uClamp and µClamp are marks of Semtech Corporation

Notes:

1)Device is electrically symmetrical

### Tape and Reel Specification



Pin 1 Location

User Direction of feed

**Device Orientation in Tape** 

AO	В0	ко
0.69 +/-0.10 mm	1.19 +/-0.10 mm	0.66 +/-0.10 mm

Tape Width	B, (Max)	D	D1	E	F	Ρ	PO	P2	Т	w
8 mm	4.2 mm (.165)	1.5 + 0.1 mm - 0.0 mm (0.59 +.005 000)	0.4 mm ±0.25 (.031)	1.750±.10 mm (.069±.004)	3.5±0.05 mm (.138±.002)	4.0±0.10 mm (.157±.00- 4)	4.0±0.1 mm (.157±.00- 4)	2.0±0.05 mm (.079±.002)	0.254±0.02 mm (.016)	8.0 mm + 0.3 mm - 0.1 mm (.312±.012)

# Contact Information

Semtech Corporation **Protection Products Division** 200 Flynn Rd., Camarillo, CA 93012 Phone: (805)498-2111 FAX (805)498-3804