

Product Overview

MC74HC00A: Quad 2-Input NAND Gate

For complete documentation, see the data sheet.

Quad 2 Input NAND Gate. High Performance Silicon Gate CMOS. The MC74HC00A is identical in pinout to the LS00. The device inputs are compatible with Standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs.

Features

- Output Drive Capability: 10 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS and TTL
- Operating Voltage Range: 2 to 6 V
- Low Input Current: 1 μ A
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance With the JEDEC Standard No. 7 A Requirements
- Chip Complexity: 32 FETs or 8 Equivalent Gates
- PbFree Packages are Available

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HC00ADG	Pb-free	Active	NAND	4	2	6	15	4	SOIC-14
	Halide free								
MC74HC00ADR2G	Pb-free	Active	NAND	4	2	6	null	4	SOIC-14
	Halide free								
MC74HC00ADTR2G	Pb-free	Active	NAND	4	2	6	15	4	TSSOP-14
	Halide free								
NLV74HC00ADG	AEC Qualified	Active	NAND	4	2	6	15	4	SOIC-14
	PPAP Capable								
	Pb-free								
	Halide free								
NLV74HC00ADR2G	AEC Qualified	Active	NAND	4	2	6	15	4	SOIC-14
	PPAP Capable								
	Pb-free								
	Halide free								
NLV74HC00ADTR2G	AEC Qualified	Active	NAND	4	2	6	15	4	TSSOP-14
	PPAP Capable								
	Pb-free								
	Halide free								

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