TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

# SSM3K116TU

### High Speed Switching Applications

- 2.5V drive
- Low on-resistance:  $R_{on} = 135m\Omega (max) (@V_{GS} = 2.5 V)$  $R_{on} = 100m\Omega (max) (@V_{GS} = 4.5 V)$

#### Absolute Maximum Ratings (Ta = 25°C)

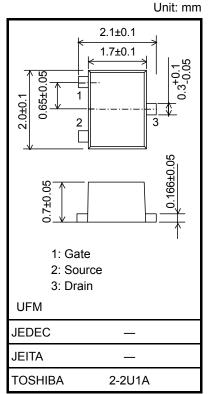
Characteristic		Symbol	Rating	Unit	
Drain-Source voltage		V <sub>DS</sub>	30	V	
Gate-Source voltage		V <sub>GSS</sub>	± 12	V	
Drain current	DC	I <sub>D</sub>	2.2	А	
	Pulse	I <sub>DP</sub>	4.4		
Drain power dissipation		PD (Note 1)	800	mW	
		P <sub>D (Note 2)</sub>	500		
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	–55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Mounted on ceramic board. (25.4 mm  $\times$  25.4 mm  $\times$  0.8 mm, Cu Pad: 645 mm<sup>2</sup> ) Note 2: Mounted on FR4 board.
- (25.4 mm imes 25.4 mm imes 1.6 mm, Cu Pad: 645 mm $^2$  )

### Electrical Characteristics (Ta = 25°C)



Weight: 6.6 mg (typ.)

Characte	eristic	Symbol	Test Conditions	Min	Тур.	Max	Unit	
Drain-Source breakdown voltage		V (BR) DSS	$I_D = 1 \text{ mA}, V_{GS} = 0$	30	—	_	v	
		V (BR) DSX	$I_D = 1 \text{ mA}, V_{GS} = -12 \text{ V}$	18	—	_		
Drain cut-of	ff current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0$	—	—	1	μA	
Gate leakag	e current	I <sub>GSS</sub>	$V_{GS}=\pm 12V,V_{DS}=0$	—	_	±1	μA	
Gate thresho	ld voltage	V <sub>th</sub>	$V_{DS} = 3 V, I_D = 0.1 mA$	0.5	—	1.1	V	
Forward transfe	er admittance	Y <sub>fs</sub>	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 0.25 \text{ A}$ (Not	te3) 1	2	_	S	
Drain-Source on-resistance		R <sub>DS (ON)</sub>	$I_D = 0.5 \text{ A}, V_{GS} = 4.5 \text{ V}$ (Not	te3) —	75	100	mΩ	
			$I_D = 0.25 \text{ A}, V_{GS} = 2.5 \text{ V}$ (Not	te3) —	95	135		
Input capa	icitance	C <sub>iss</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$	—	245	_	pF	
Output cap	acitance	C <sub>oss</sub>	$V_{DS} = 10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$	—	41	_	pF	
Reverse transfe	Reverse transfer capacitance $C_{rss}$ $V_{DS} = 10 V$ , $V_{GS} = 0$ , f = 1 MHz		—	33	_	pF		
Switching time	Turn-on time	t <sub>on</sub>	$V_{DD} = 10 \text{ V}, \text{ I}_{D} = 0.25 \text{ A},$	_	9		ns	
	Turn-off time	t <sub>off</sub>	$V_{GS}$ = 0 to 2.5 V, $R_G$ = 4.7 $\Omega$	—	15			
Drain-Source for	rward voltage	V <sub>DSF</sub>	$I_D = -2.2A, V_{GS} = 0 V \qquad (Note$	e3) —	-0.83	-1.2	V	

Note3: Pulse test

Start of commercial production 2005-06

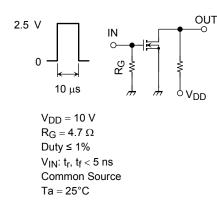
# **TOSHIBA**

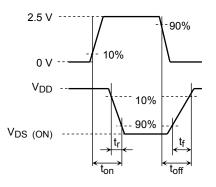
### Switching Time Test Circuit

(a) Test Circuit

(b) V<sub>IN</sub>

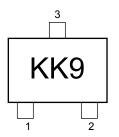
(c) V<sub>OUT</sub>





### Marking

### Equivalent Circuit (top view)



# 

### Precaution

 $V_{th}$  can be expressed as the voltage between gate and source when the low operating current value is I<sub>D</sub>=0.1mA for this product. For normal switching operation, V<sub>GS (on)</sub> requires a higher voltage than V<sub>th</sub>, and V<sub>GS (off)</sub> requires a lower voltage than V<sub>th</sub>.

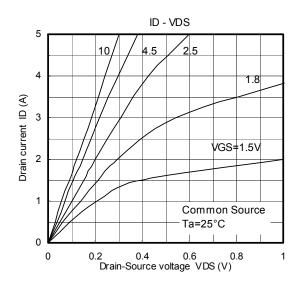
(The relationship can be established as follows:  $V_{GS (off)} < V_{th} < V_{GS (on)}$ )

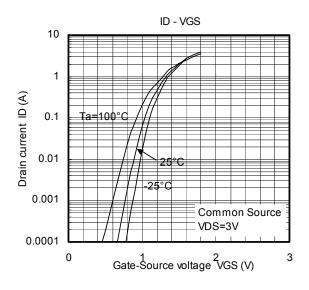
Take this into consideration when using the device.

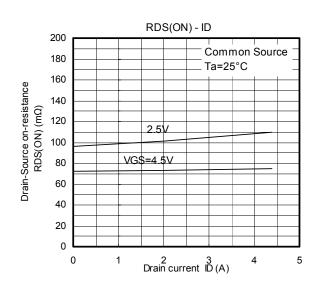
### **Handling Precaution**

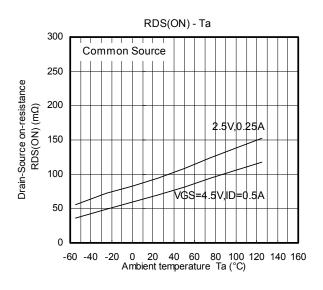
When handling individual devices which are not yet mounted on a circuit board, be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

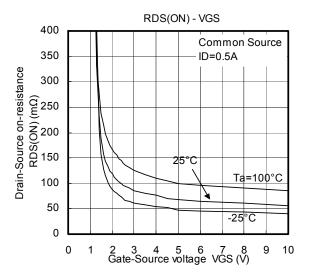
# **TOSHIBA**

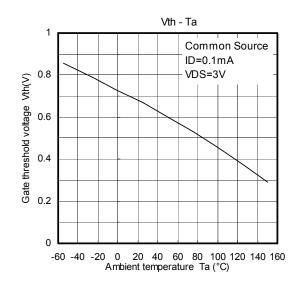












### TOSHIBA

а

400

200

0

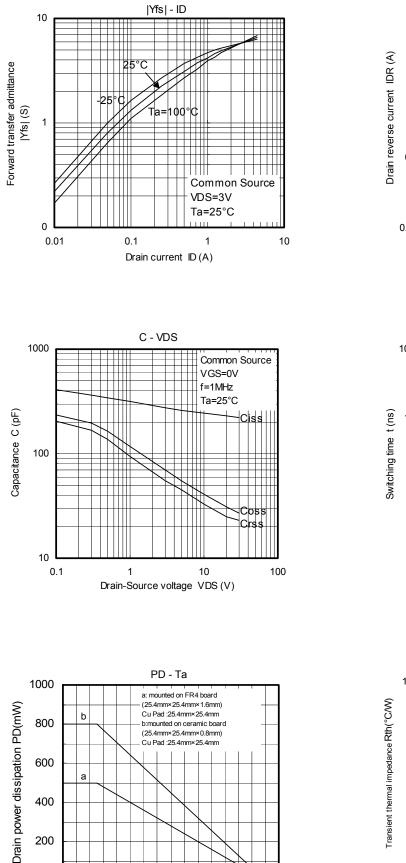
0

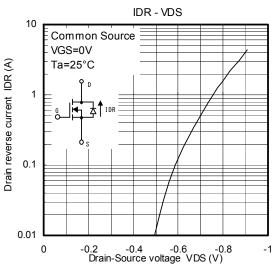
20 40 60 80

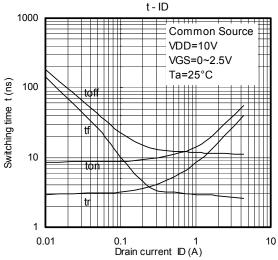
Ambient temperature Ta(°C)

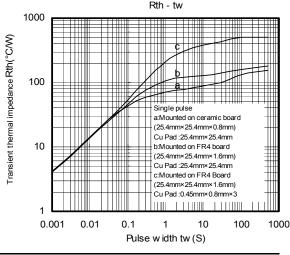
100 120 140 160

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