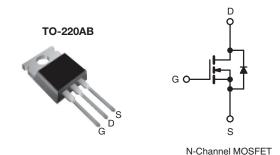


### Power MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	600			
$R_{DS(on)}(\Omega)$	V <sub>GS</sub> = 10 V 1.2			
Q <sub>g</sub> (Max.) (nC)	60			
Q <sub>gs</sub> (nC)	8.3			
Q <sub>gd</sub> (nC)	30			
Configuration	Single			



### **FEATURES**

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- · Fast Switching
- · Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC



#### **DESCRIPTION**

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION		
Package	TO-220AB	
Lead (Pb)-free	IRFBC40PbF	
Lead (FD)-life	SiHFBC40-E3	
SnPb	IRFBC40	
SILD	SiHFBC40	

ABSOLUTE MAXIMUM RATINGS ( $T_{C}$	= 25 °C, unl	ess otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			$V_{DS}$	600	V	
Gate-Source Voltage			$V_{GS}$	± 20		
Continuous Drain Current	V <sub>GS</sub> at 10 V	$T_{\rm C} = 25 ^{\circ}{\rm C}$ $T_{\rm C} = 100 ^{\circ}{\rm C}$	1	6.2		
	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 100 °C	I <sub>D</sub>	3.9	Α	
Pulsed Drain Current <sup>a</sup>			I <sub>DM</sub>	25		
Linear Derating Factor				1.0	W/°C	
Single Pulse Avalanche Energy <sup>b</sup>			E <sub>AS</sub>	570	mJ	
Repetitive Avalanche Current <sup>a</sup>			I <sub>AR</sub>	6.2	А	
Repetitive Avalanche Energy <sup>a</sup>			E <sub>AR</sub>	13	mJ	
Maximum Power Dissipation $T_C = 25  ^{\circ}C$			$P_{D}$	125	W	
Peak Diode Recovery dV/dtc			dV/dt	3.0	V/ns	
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C	
Soldering Recommendations (Peak Temperature) for 10 s			300 <sup>d</sup>			
Mounting Torque	6-32 or M3 screw			10	lbf ⋅ in	
				1.1	N⋅m	

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b.  $V_{DD}$  = 50 V, starting  $T_J$  = 25 °C, L = 27 mH,  $R_g$  = 25  $\Omega$ ,  $I_{AS}$  = 6.2 A (see fig. 12).
- c.  $I_{SD} \le 6.2$  A,  $dI/dt \le 80$  A/ $\mu$ s,  $V_{DD} \le V_{DS}$ ,  $T_J \le 150$  °C.
- d. 1.6 mm from case.

<sup>\*</sup> Pb containing terminations are not RoHS compliant, exemptions may apply



THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R <sub>thJA</sub>	-	62	
Case-to-Sink, Flat, Greased Surface	R <sub>thCS</sub>	0.50	-	°C/W
Maximum Junction-to-Case (Drain)	$R_{thJC}$	-	1.0	

PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0$	0 V, I <sub>D</sub> = 250 μA	600	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	to 25 °C, I <sub>D</sub> = 1 mA	-	0.7	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		2.0	-	4.0	V
Gate-Source Leakage	I <sub>GSS</sub>	Vo	<sub>SS</sub> = ± 20 V	-	-	± 100	nA
Zero Gate Voltage Drain Current	I	$V_{DS} = 6$	$800 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	100	μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 480 \text{ V}, \text{ V}$	V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C	1	-	500	μΑ
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	$I_D = 3.7A^b$	1	-	1.2	Ω
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub> = 1	00 V, $I_D = 3.7 A^b$	4.7	-	-	S
Dynamic							
Input Capacitance	$C_{iss}$	$V_{GS} = 0 \text{ V},$ $V_{DS} = 25 \text{ V},$ $f = 1.0 \text{ MHz, see fig. 5}$		ı	1300	-	pF
Output Capacitance	C <sub>oss</sub>			-	160	-	
Reverse Transfer Capacitance	$C_{rss}$			1	30	-	
Total Gate Charge	$Q_g$		L 60AV 260V	ı	-	60	
Gate-Source Charge	$Q_{gs}$	$V_{GS} = 10 \text{ V}$	$I_D = 6.2 \text{ A}, V_{DS} = 360 \text{ V},$ see fig. 6 and 13 <sup>b</sup>	-	-	8.3	nC
Gate-Drain Charge	$Q_{gd}$		See lig. 0 and 13	-	-	30	
Turn-On Delay Time	t <sub>d(on)</sub>			-	13	-	
Rise Time	t <sub>r</sub>	$V_{DD} = 3$	00 V, I <sub>D</sub> = 6.2 A,	-	18	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_{q} = 9.1 \Omega, R_{D} = 47 \Omega, \text{ see fig. } 10^{b}$		-	55	-	ns
Fall Time	t <sub>f</sub>	, s	, ,	1	20	-	
Internal Drain Inductance	$L_{D}$	Between lead, 6 mm (0.25") from		ı	4.5	-	الم
Internal Source Inductance	L <sub>S</sub>	package and ce die contact	nter of	-	7.5	-	− nH
Drain-Source Body Diode Characteristic	s					,	
Continuous Source-Drain Diode Current	I <sub>S</sub>	MOSFET symbo	ol	-	-	6.2	
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>	<ul> <li>showing the integral reverse</li> <li>p - n junction die</li> </ul>	ode sold	-	-	25	А
Body Diode Voltage	V <sub>SD</sub>	T <sub>J</sub> = 25 °C, I	<sub>S</sub> = 6.2 A, V <sub>GS</sub> = 0 V <sup>b</sup>	-	-	1.5	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	T 05 °C 1			450	940	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	1 <sub>J</sub> = 25 °C, I <sub>F</sub> =	6.2 A, $dI/dt = 100 A/\mu s^b$	-	3.8	7.9	μС
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn	on time is negligible (turn	-on is do	minated h	v L and	1-2)

#### **Notes**

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).
- b. Pulse width  $\leq$  300  $\mu$ s; duty cycle  $\leq$  2 %.





### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

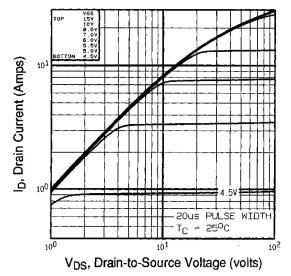


Fig. 1 - Typical Output Characteristics,  $T_C$  = 25 °C

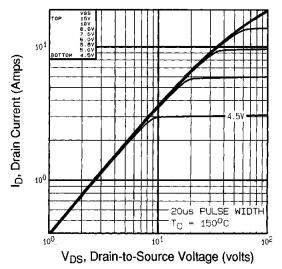
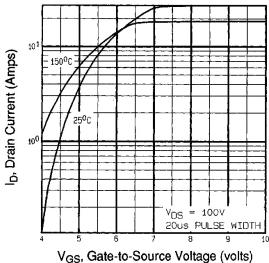
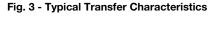


Fig. 2 - Typical Output Characteristics, T<sub>C</sub> = 150 °C





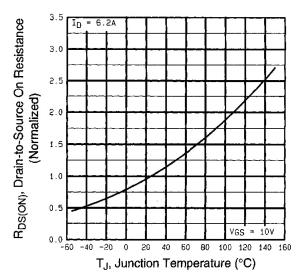


Fig. 4 - Normalized On-Resistance vs. Temperature



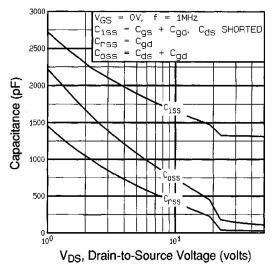


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

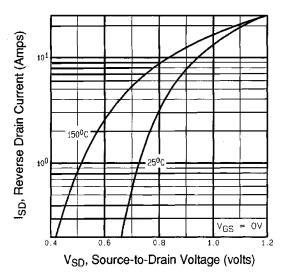


Fig. 7 - Typical Source-Drain Diode Forward Voltage

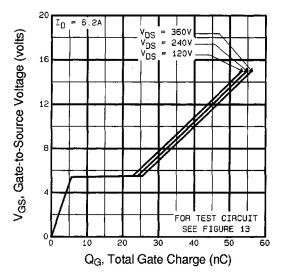


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

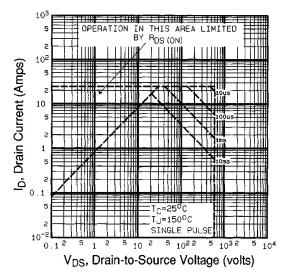


Fig. 8 - Maximum Safe Operating Area





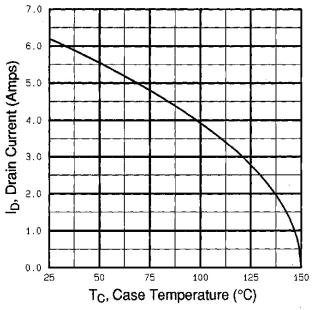


Fig. 9 - Maximum Drain Current vs. Case Temperature

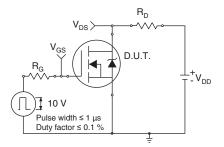


Fig. 10a - Switching Time Test Circuit



Fig. 10b - Switching Time Waveforms

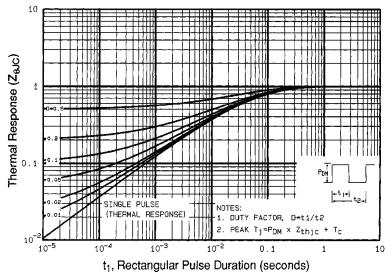


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case



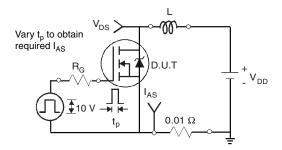


Fig. 12a - Unclamped Inductive Test Circuit

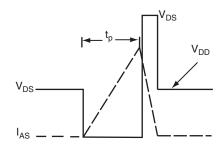


Fig. 12b - Unclamped Inductive Waveforms

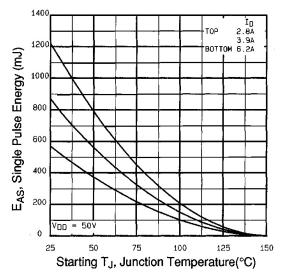


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

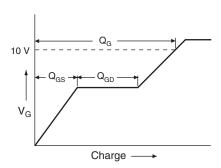


Fig. 13a - Basic Gate Charge Waveform

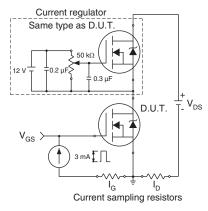
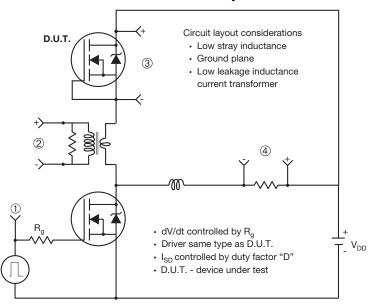


Fig. 13b - Gate Charge Test Circuit



### Peak Diode Recovery dV/dt Test Circuit



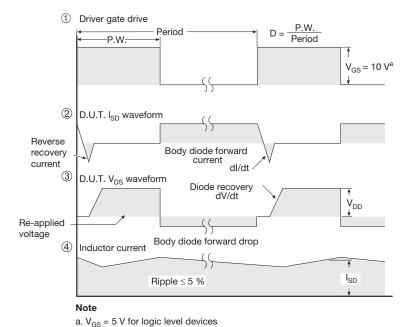


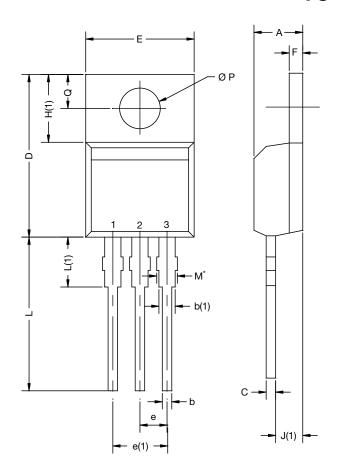
Fig. 14 - For N-Channel

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## TO-220-1



DIM.	MILLIN	METERS	INCHES		
	MIN.	MAX.	MIN.	MAX.	
Α	4.24	4.65	0.167	0.183	
b	0.69	1.02	0.027	0.040	
b(1)	1.14	1.78	0.045	0.070	
С	0.36	0.61	0.014	0.024	
D	14.33	15.85	0.564	0.624	
Е	9.96	10.52	0.392	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.10	6.71	0.240	0.264	
J(1)	2.41	2.92	0.095	0.115	
L	13.36	14.40	0.526	0.567	
L(1)	3.33	4.04	0.131	0.159	
ØР	3.53	3.94	0.139	0.155	
Q	2.54	3.00	0.100	0.118	

#### Note

 $\bullet$   $M^{\star}=0.052$  inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM



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