CMOS Digital Integrated Circuits Silicon Monolithic

# 74HC00D

### 1. Functional Description

• Quad 2-Input NAND Gate

### 2. General

The 74HC00D is a high speed CMOS 2-INPUT NAND GATE fabricated with silicon gate C<sup>2</sup>MOS technology. It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

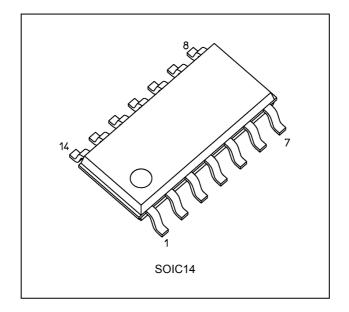
The internal circuit is composed of 3 stages including buffer output, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

### 3. Features

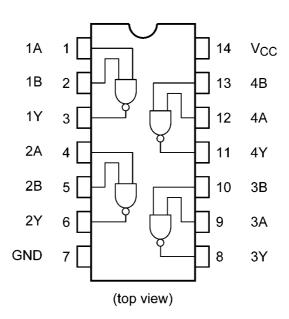
- (1) High speed:  $t_{pd}$  = 6 ns (typ.) at  $V_{CC}$  = 5 V
- (2) Low power dissipation:  $I_{CC} = 1.0 \ \mu A \ (max) T_a = 25 \ ^{\circ}C$
- (3) Balanced propagation delays:  $t_{PLH} \approx t_{PHL}$
- (4) Wide operating voltage range:  $V_{CC(opr)} = 2.0$  to 6.0 V

### 4. Packaging

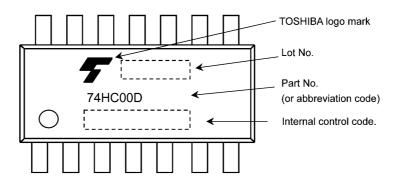


### 5. Pin Assignment

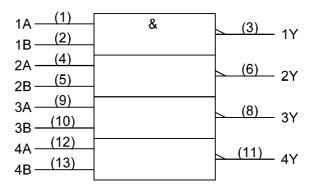
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### 6. Marking



7. IEC Logic Symbol



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#### 8. Truth Table

А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

### 9. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V <sub>CC</sub>		-0.5 to 7.0	V
Input voltage	V <sub>IN</sub>		-0.5 to V <sub>CC</sub> + 0.5	V
Output voltage	V <sub>OUT</sub>		-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>		±20	mA
Output diode current	I <sub>ОК</sub>		±20	mA
Output current	I <sub>OUT</sub>		±25	mA
V <sub>CC</sub> /ground current	I <sub>CC</sub>		±50	mA
Power dissipation	PD	(Note 1)	500	mW
Storage temperature	T <sub>stg</sub>		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $P_D$  derates linearly with -8 mW/°C above 85 °C

### 10. Operating Ranges (Note)

Characteristics	Symbol	Test Condition	Rating	Unit
Supply voltage	V <sub>CC</sub>	—	2.0 to 6.0	V
Input voltage	V <sub>IN</sub>	—	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	—	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	—	-40 to 125	°C
Input rise and fall times	t <sub>r</sub> ,t <sub>f</sub>	—	0 to 50	μS

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

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#### **11. Electrical Characteristics**

### 11.1. DC Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Test Conditior	ı	V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
High-level input voltage	V <sub>IH</sub>	—		2.0	1.50	_	_	V
				4.5	3.15	_	_	
				6.0	4.20	_	_	
Low-level input voltage	VIL	_		2.0	_	_	0.50	V
				4.5	_	_	1.35	
				6.0	_	—	1.80	
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OH</sub> = -20 μA	2.0	1.9	2.0	_	V
				4.5	4.4	4.5	_	
				6.0	5.9	6.0	_	
			I <sub>OH</sub> = -4 mA	4.5	4.18	4.31	_	
			I <sub>OH</sub> = -5.2 mA	6.0	5.68	5.80	_	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OL</sub> = 20 μA	2.0		0.0	0.1	V
				4.5		0.0	0.1	
				6.0		0.0	0.1	
			I <sub>OL</sub> = 4 mA	4.5	_	0.17	0.26	
			I <sub>OL</sub> = 5.2 mA	6.0	_	0.18	0.26	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	±0.1	μA
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		6.0	_	_	1.0	μA

### 11.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Conditior	ı	V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	_		2.0	1.50	_	V
				4.5	3.15	_	
				6.0	4.20	_	
Low-level input voltage	V <sub>IL</sub>	—		2.0	_	0.50	V
				4.5	_	1.35	
				6.0		1.80	
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OH</sub> = -20 μA	2.0	1.9	_	V
				4.5	4.4	_	
				6.0	5.9	—	
			I <sub>OH</sub> = -4 mA	4.5	4.13	_	
			I <sub>OH</sub> = -5.2 mA	6.0	5.63	_	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OL</sub> = 20 μA	2.0		0.1	V
				4.5	_	0.1	
				6.0	_	0.1	
			I <sub>OL</sub> = 4 mA	4.5	_	0.33	
			I <sub>OL</sub> = 5.2 mA	6.0	_	0.33	
Input leakage current	l <sub>iN</sub>	$V_{IN} = V_{CC}$ or GND		6.0	_	±1.0	μA
Quiescent supply current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND		6.0		10.0	μA

### 11.3. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 125 °C)

Characteristics	Symbol	Test Conditior	ı	V <sub>CC</sub> (V)	Min	Max	Unit
High-level input voltage	V <sub>IH</sub>	—		2.0	1.50	—	V
				4.5	3.15	_	
				6.0	4.20	_	]
Low-level input voltage	V <sub>IL</sub>	—		2.0	_	0.50	V
				4.5	_	1.35	
				6.0	_	1.80	
High-level output voltage	V <sub>OH</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OH</sub> = -20 μA	2.0	1.9	_	V
				4.5	4.4	_	
				6.0	5.9	_	]
			I <sub>OH</sub> = -4 mA	4.5	3.7	_	
			I <sub>OH</sub> = -5.2 mA	6.0	5.2	_	
Low-level output voltage	V <sub>OL</sub>	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I <sub>OL</sub> = 20 μA	2.0	_	0.1	V
				4.5	_	0.1	
				6.0	_	0.1	
			I <sub>OL</sub> = 4 mA	4.5	_	0.4	]
			I <sub>OL</sub> = 5.2 mA	6.0	_	0.4	]
Input leakage current	I <sub>IN</sub>	$V_{IN} = V_{CC}$ or GND		6.0	—	±1.0	μA
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	40.0	μA

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#### 11.4. AC Characteristics (Unless otherwise specified, $C_L = 15 \text{ pF}$ , $V_{CC} = 5 \text{ V}$ , $T_a = 25 \text{ °C}$ , Input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub> ,t <sub>THL</sub>	—	—	4	8	ns
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	—	_	6	12	ns

### 11.5. AC Characteristics

(Unless otherwise specified,  $C_L = 50 \text{ pF}$ ,  $T_a = 25 \text{ °C}$ , Input:  $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Note	V <sub>CC</sub> (V)	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub> ,t <sub>THL</sub>		2.0	—	25	75	ns
			4.5	—	7	15	
			6.0	—	6	13	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>		2.0	_	27	75	ns
			4.5	_	9	15	
			6.0	_	8	13	1
Input capacitance	C <sub>IN</sub>		_	_	3	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 1)	_	_	7	_	pF

Note 1: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation.

 $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/4$  (per gate)

### 11.6. AC Characteristics

### (Unless otherwise specified, $C_L$ = 50 pF, $T_a$ = -40 to 85 °C, Input: $t_r$ = $t_f$ = 6 ns)

Characteristics	Symbol	V <sub>CC</sub> (V)	Min	Max	Unit
Output transition time	t <sub>TLH</sub> ,t <sub>THL</sub>	2.0	_	95	ns
		4.5	_	19	
		6.0	—	16	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	2.0	—	95	ns
		4.5	—	19	
		6.0	_	16	

### 11.7. AC Characteristics

### (Unless otherwise specified, $C_L = 50 \text{ pF}$ , $T_a = -40 \text{ to } 125 \text{ °C}$ , Input: $t_r = t_f = 6 \text{ ns}$ )

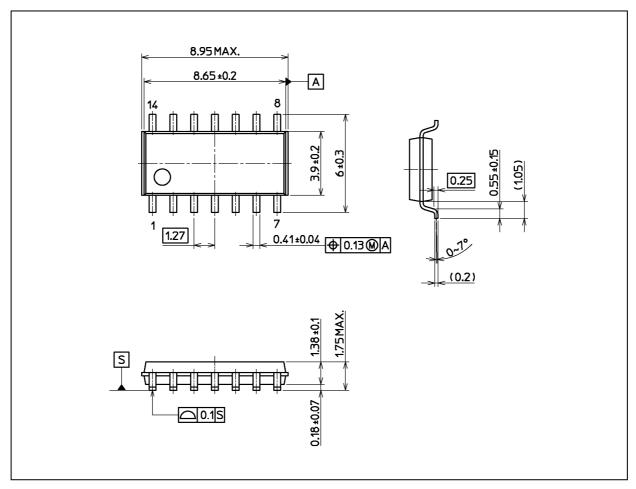
Characteristics	Symbol	V <sub>CC</sub> (V)	Min	Max	Unit
Output transition time	t <sub>TLH</sub> ,t <sub>THL</sub>	2.0	_	110	ns
		4.5	—	22	
		6.0	—	19	
Propagation delay time	t <sub>PLH</sub> ,t <sub>PHL</sub>	2.0	_	135	ns
		4.5	_	27	
		6.0	_	23	



### **Package Dimensions**

74HC00D

Unit: mm



Weight: 0.13 g (typ.)

Package Name(s) Nickname: SOIC14

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