Radial Leaded Ceramic Disc Capacitors

Safety Standard Recognized, C900, Encapsulated, AS Type, X1 760 VAC/Y1 500 VAC (Industrial Grade)



Overview

KEMET's 900, encapsulated radial leaded ceramic disc capacitors are specifically designed for interference-suppression AC line filtering applications. Having internationally recognized safety certifications, these capacitors are well-suited for applications that require keeping potentially disruptive or damaging line transients and EMI out of susceptible equipment. They are also an ideal solution when needing to suppress line disturbances at the source.

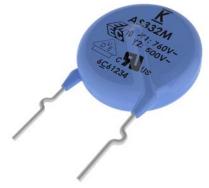
Safety Certified Capacitors are classified as either X and/or Y capacitors. Class X capacitors are primarily used in line-to-line (across-the-line) applications. In this application, there is no danger of electric shock to humans should the

capacitor fail, but could result in a risk of fire. The class Y capacitor is primarily used in line-to-ground (line by-pass) applications. In this application, failure of the capacitor could lead to danger of electric shock.

With a working voltage of 760 VAC in line-to-line (Class X) and 500 VAC in line-to-ground (Class Y) applications, these safety capacitors meet the impulse test criteria outlined in IEC Standard 60384. Meeting subclass X1 and Y1 requirements, these devices are certified to withstand impulses up to 4 KV (X1) and 8 KV (Y1) respectively. These encapsulated devices also meet the flame test requirements outlined in UL Standard 94 V-0.

Benefits

- Safety standard recognized (IEC 60384-14)
- Reliable operation up to 125°C
- Class X1/Y1
- 10 mm lead spacing
- · Lead (Pb)-free and RoHS Compliant
- Halogen-free
- Available capacitance tolerances of ±5%, ±10%, and ±20%
- High reliability
- · Preformed (crimped) or straight lead configurations
- Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated lead finish, allowing for excellent solderability
- Encapsulation meets flammability standard UL 94 V-0



Applications

Typical applications include:

- · Line-to-line (Class X) filtering
- · Line-to-ground (Class Y) filtering
- · Antenna coupling
- Primary and secondary coupling (switching power supplies)
- · Line disturbances suppression (motors and motor controls, relays, switching power supplies, and invertors)

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Ordering Information

C9	6	1	U	222	M	W	W	D	A	A	7317
Ceramic Series	Body Diameter	Lead Spacing ¹	Spec.	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage	Dielectric/ Temp. Char.	Design	Lead Config. ¹	Failure Rate	Packaging (C-Spec)
C9 = Ceramic 900	1 = 8.0 mm 2 = 9.0 mm 3 = 10.0 mm 5 = 12.0 mm 6 = 13.0 mm 7 = 14.0 mm 8 = 15.0 mm	1 = 10.0 mm	U = Safety	Two significant digits and number of zeroes	K = ±10% M = ±20%	W = X1 760 VAC/ Y1 500 VAC	Y = Y5P W = Y5U	D = Disc	A = Straight B = Vertical Kink C = Outside Kink	A = N/A	See "Packaging C-Spec Ordering Options Table"

¹ "Vertical Kink" and "Outside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors ordered with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

Packaging C-Spec Ordering Options Table

Packaging Type	Lead Length (mm) ^{2,3}	Packaging Ordering Code (C-Spec)
Ammo Pack	See Note 5	7317
	3.0±1.0	WL30
	3.5±1.0	WL35
Dulk Dog	4.0±1.0	WL40
Bulk Bag	4.5±1.0	WL45
	5.0±1.0	WL50
	20.0 minimum⁴	WL20

¹ Preformed (crimped) lead configurations include "Vertical Kink" and "Outside Kink." See "Lead Configurations" and "Ordering Information" sections of this document for further details.

² "Vertical Kink" and "Outside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

³ For nonstandard lead length inquiries, please contact KEMET.

⁴ Lead length of 20.0 mm minimum only available for straight leads.

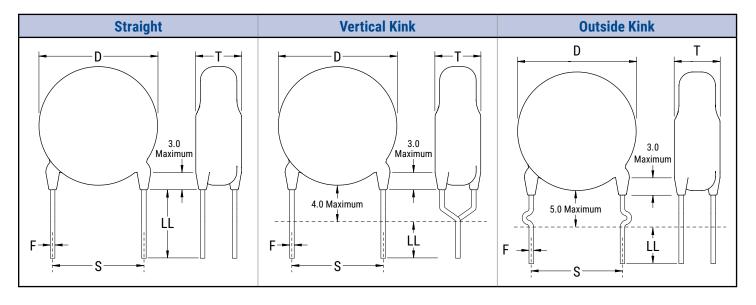
 $^{^{5}}$ Lead length for ammo pack packaging is defined by the H and H $_{
m 0}$ dimensions in Table 3.

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Lead Configurations



Dimensions - Millimeters

Lead	Lead	S	Lead	D	Т	е	ØF
Configuration	Configuration Ordering Code ¹	Lead Spacing ²	Spacing Tolerance	Body Diameter ²	Body Thickness	Lead Meniscus	Lead Diameter
Straight	А	10.0	±1.0	See Table 1 - "Product Ordering Codes and Ratings"			
Vertical Kink (Preformed)	В	10.0	±1.0			3.0 maximum	0.55±0.1
Outside Kink (Preformed)	С	10.0	±1.0				

¹ Lead Configuration is identified in the 13th character of the ordering code. See "Lead Configuration" and "Ordering Information" sections of this document for further details.

² Body diameter of capacitor will limit available lead spacing and packaging options. See "Product Ordering Codes and Ratings" sections of this document for further details.

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Approval Standard and Certification No.

Safety Standard	Standard No.	Subclass	Working Voltage	Certificate No.	
VDE	IEC 60384-14	X1	760 VAC	40042063	
(ENEC)	IEC 00384-14	Y1	500 VAC	40042003	
UL	UL 60384-14 and	X1	760 VAC	F2F6200	
CAN/CSA	E60384-14	Y1	500 VAC	<u>E356389</u>	

These devices are VDE/ENEC and UL recognized for antenna coupling and AC line-to-line (class X) and line-to-ground (class Y) applications per IEC60384–14 and UL 60384–14.

Environmental Compliance

These devices are Halogen-free and RoHS Compliant. They meet all requirements set forth by both EU and China RoHS directives.





General Specifications/Performance Characteristics

Dielectric/Temperature Characteristic	Y5P	Y5U	
Operating Temperature Range:	-40°C to +125°C		
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	±10% +20%/-55%		
Dielectric Withstanding Voltage	4,000 VAC (60 ±5 seconds at 25°C)		
Quality Factor (Q)	See "Dissipa	ition Factor"	
Dissipation Factor (tanδ) at +25°C1	2.50% 2.50%		
Insulation Resistance (IR) Limit at +25°C	10,000 MΩ Minimum (500 VDC applied for 60 ±5 seconds at 25°C)		

¹ Capacitance and Dissipation Factor (DF) measured under the following conditions:

Note: When measuring capacitance, it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

¹ kHz ±50 Hz and 1.0 ±0.2 Vrms

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Table 1 - Product Ordering Codes and Ratings

B: 1				Dir	mensions (mm)	Lead Spacing	
Dielectric/ Temp. Char.	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Bulk Packaging	Ammo Packaging
	C911U101KWYD(1)A(2)	100 pF		8.0				
	C911U151KWYD(1)A(2)	150 pF		8.0		0.55±0.05	10 mm	
	C911U221KWYD(1)A(2)	220 pF		8.0				
Y5P	C911U331KWYD(1)A(2)	330 pF	±10%	8.0				
126	C921U471KWYD(1)A(2)	470 pF	±10%	9.0				
	C931U561KWYD(1)A(2)	560 pF		10.0				
	C931U681KWYD(1)A(2)	680 pF		10.0	7.0			
	C951U102KWYD(1)A(2)	1,000 pF		12.0	7.0	0.5510.05		
	C921U102MWWD(1)A(2)	1,000 pF		9.0				
	C931U152MWWD(1)A(2)	1,500 pF		10.0				
Y5U	C961U222MWWD(1)A(2)	2,200 pF	120%	13.0				
150	C961U332MWWD(1)A(2)	3,300 pF	±20%	13.0				
	C971U392MWWD(1)A(2) 3,90			14.0				
	C981U472MWWD(1)A(2)	4,700 pF		15.0				

⁽¹⁾ To properly complete ordering code, insert the one-digit character code to reflect the required lead configuration: (See "Lead Configuration" section of this document, page 2, for further details.)

- A = Straight
- B = Vertical Kink
- C = Outside Kink

⁽²⁾ To properly complete ordering code, enter the four-digit numeric or alphanumeric "Packaging C-Spec Ordering Code". See "Dimensions" section of this document, page 2, for available options.

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Table 2 - Performance & Reliability: Test Methods and Conditions

It	em	Specification			Test Method			
Operating Tem	perature Range				-40°C to +125°C			
	Between lead wires	No failures		The capacitor shall not be damaged when 4,000 VAC (rms) is applied between the lead wires for 60 seconds.				
Dielectric Strength	Body Insulation	No failures		into a container filled with metal balls approximately 1 mm in diameter. 4,000 VAC			is tightly acitor m each ted solo VAC veen the	
Insulation R	esistance (IR)	10,000 MΩ) minimum	1	he insulation resupplied after 60 ±	istance shall be me 5 seconds of charg	easured with 500 ±50 VDC ging.	
Capac	citance	Within specif	fied tolerance	(Capacitance is me	easured at 1 kHz ±	20% and 5 V _{rms} or less	
Dissipation F	actor (DF) or Q	DF≤	2.5%	(20 ±2°C)		THIS	
				/	capacitance me	asurement is made	e at each step specified:	
					Step	Temperature		
					1	+20 ±2°C		
	Temperature Capacitance		Capacitance		2	-25 ±2°C		
Temperature	Characteristics	ristics Characteristics	Change		3	+20 ±2°C		
		Y5U	Within +22%/-56%		4	+85 ±2°C		
			<u> </u>		5	+20 ±2°C		
				(Pretreatment: Capacitor is store condition ¹ for 24 ±	d at 85 ±2°C for 1 l 2 hours before me	hour and then placed at room easurement.	
	Tensile	•	citor body shall not eak.	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical. A tensile force of 10 N is applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen.			
Terminal Strength	Bending	Lead wire or capacitor body shall not break.		With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical. A mass force of 5 N is then suspended from the end of the termination. The body of the specimen is then inclined within a period of 2 to 3 seconds, through an angle of approximately 90° in the vertical plane and then resumed to its initial position over the same period of time. This operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.				
Solde	Solderability		ve a uniform coating al direction and over cumference.	The lead wire of the capacitor is dipped into molten solder for 5 ±0.5 seconds. The depth of immersion is up to 1.5 mm (+5/-0 mm) from the root of lead wires. Solder temperature: Lead-free solder (Sn-3Ag – 0.5 Cu) 245°C ±5°C.			on is up to 1.5 mm es.	

¹ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

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Table 2 - Performance & Reliability: Test Methods and Conditions cont.

lte	m	Specifi	cation	Test M	lethod		
	Appearance	No visua	al defect	As shown in the figure below, the molten solder up to 1.5 mm (+5/-			
	IR	1,000	Ο ΜΩ	epoxy meniscus (root of lead wire). Duration/Solder Temperature: 3.5 ±0.5 seconds/350°C ±10°C or			
	Dielectric Strength	Per item 1		10 ±1 seconds/260°C ±5°C Thermal Capacitor			
Soldering Effect (Non-Preheat)	Capacitance	Within	ı ±10%	Pretreatment: Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition¹ for 24 ±2 hours before initial measurements. Post-treatment: Capacitor is stored for 1 to 2 hours at room condition¹.			
	Appearance	No visua	al defect	Capacitor is stored at 120°C +0/- Then, as shown in the figure belo	-5°C for 60 +0/-5 seconds.		
	IR	1,000	Ο ΜΩ	in molten solder up to 1.5 mm (+5	5/-0mm) from the end of the		
	Dielectric Strength	Per it	em 1	epoxy meniscus (root of lead wire Duration/Solder Temperature: 7.5	e). 5 +0/-1 seconds/260°C ±5°C		
Soldering Effect (Preheat)	Capacitance	Within	±10%	Thermal Screen 1.5 to 2.0 mm Molten Solder Pretreatment: Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition¹ for 24 ±2 hours before initial measurements. Post-treatment: Capacitor is stored for 1 to 2 hours at room condition¹.			
	Appearance	No visual defect		Steady State Humidity:	Load Humidity:		
	Capacitance	Temperature Characteristics	Capacitance Change	90 to 95% humidity at 40°C	90 to 95% humidity at 40°C		
Biased Humidity		Y5U	Within ±30%	±2°C for 500 ±12 hours.	±2°C for 500 ±12 hours with full rated voltage applied.		
	DF	5.0% ma	aximum	Post-treatment: Capacitor is stored for 1 to 2	Post-treatment:		
	IR	3,000 ΜΩ	minimum	hours at room condition ¹ .	Capacitor is stored for 1 to 2 hours at room condition ¹ .		
	Dielectric Strength	No fa	ilures				
	Appearance	No visua	al defect	Impulse Voltage: Each individual 8 kv impulses prior to life testing	capacitor is subjected to three		
	Capacitance Change	Within	±20%	Vp Upg Cx tr	td		
	IR	3,000 MΩ	minimum	0.5Vp (us) (us) (0.01 1.2 0.1 1.5)	46		
High Temperature Life	Dielectric Strength	No failures		Capacitors are placed in a circulating air oven for a period of 1,000 hours. The air in the oven is maintained at a temperature of 125°C ±2°C throughout the test. The capacitors are subjected to AC 850 V _{rms} . Each hour the voltage is increased to 1,000 V _{rms} for 0.1 seconds.			

 $^{^1}$ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

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Table 2 - Performance & Reliability: Test Methods and Conditions cont.

Item	Specification	Test Method			
Flame Test	The capacitor flame extinguishes as follows Cycle Time 1 ~ 4 30 seconds maximum 5 60 seconds maximum	The capacitor is exposed to a flame for 15 seconds and then removed for 15 seconds. This test is repeated for 5 cycles. Capacitor Flame 76 Gas Burner (Unit:mm)			
Active Flammability	The cheesecloth should not ignite.	The capacitors are individually wrapped in at least one, but not more than two, complete layers of cheesecloth. They are then subjected to 20 discharges. The interval between successive discharges is 5 seconds. The VAC is maintained for 2 minutes after the last discharge. C1			

[&]quot;Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

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Table 2 - Performance & Reliability: Test Methods and Conditions cont.

Ite	Item Specification			Test Method				
Passive Flammability		The burning time sh seco The tissue paper s	nds.	The capacitor under test is held into a flame and in a position which best promotes burning. Each specimen is exposed to the flame once. Test Specimen About 10mm Thick Board Time of exposure to flame: 30 seconds Length of flame: 12 ±1 mm Gas burner length: 35 mm minimum Inside diameter: 0.5 ±0.1 mm Outside diameter: 0.9 mm maximum Gas butane gas purity: 95% minimum				
	Appearance	No visual defect			tor is subjected to 5 tem ture Cycle	perature cycle	S.	
	Capacitance	Temperature Characteristics	Capacitance Change	Step	Temperature (°C)	Dwell Time (minutes)	Transition Time (minutes)	
		Y5V	Within ±20%	1	-40 +0/-3	30	(
Temperature Cycle				2	Room temperature	3	3	
9,010	DF/Q	DF≤	7.5%	3	125 +3/-0	30		
				4	Room temperature	3		
	IR	3,000 MΩ minimum		Pretreatme	ent: capacitor shall be st	ored at 85 ±2 f	or 1 hour then	
	Dielectric Strength	No fai	lures		noom condition ¹ for 24 ±2 ment: capacitor is stored		s at room	

 $^{^1}$ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

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Soldering and Mounting Information

Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could reflow the solder joint between the lead and ceramic element and/or may result in thermal shocks that can crack the ceramic element.

When soldering these capacitors with a soldering iron, it should be performed under the following conditions:

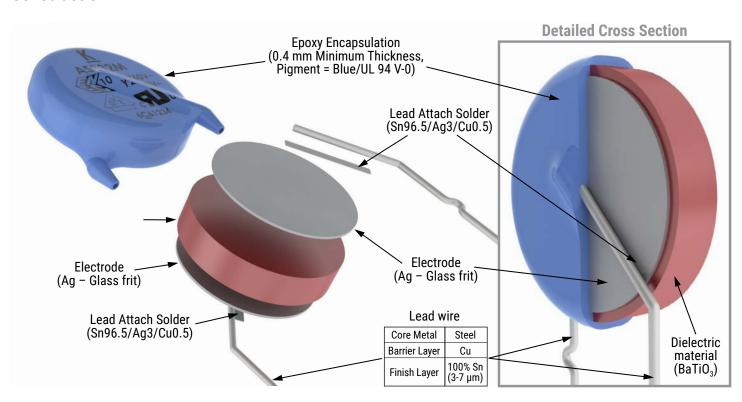
- Temperature of iron-tip: 400°C maximum
- · Soldering iron wattage: 50 W maximum
- · Soldering time: 3.5 seconds maximum

Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions:

- · Rinse bath capacity: output of 20 watts per liter or less
- · Rinsing time: 5 minute maximum
- Do not vibrate the PCB/PWB directly
- Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires

Construction



Radial Leaded Ceramic Disc Capacitors

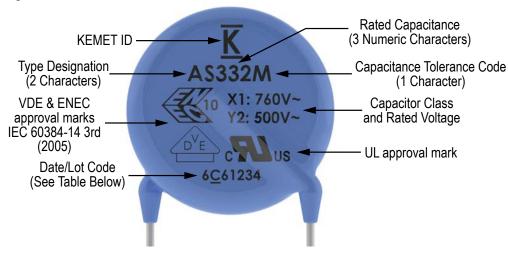
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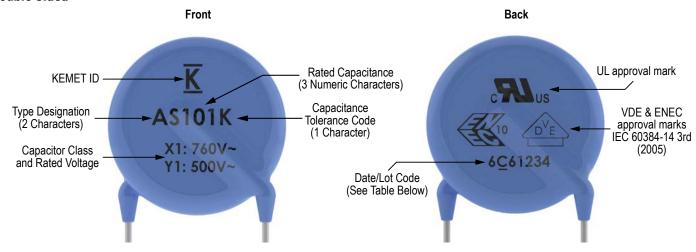
Marking

These capacitors shall be stamped or laser marked with KEMET's trademark, type designation, capacitor class, rated voltage, rated capacitance, and capacitance tolerance codes. In addition, all devices are marked with the recognized approval mark and a date/lot code for traceability. Marking will be supplied either on one side or both sides of the encapsulated capacitor body. All marking shall be legible to allow for clear identification of the component. Marking appears in legible contrast. Illustrated below is an example of the marking format and content. (Two-sided marking is limited to capacitors with body diameters ≤ 8.0 mm)

Single Sided



Double Sided



Date/Lot Code Explanation

6	<u>C</u>	6	1234
Last digit of year, e.g., 6 = 2016	Manufacturing Location Code	Manufacturing Month: 1-9 = Jan - Sept A = October N = November D = December	Last 4 digits of lot number

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Packaging Quantities

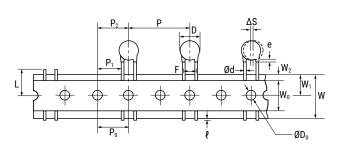
		- " -	Ammo Pack (Carrier Tape)		
Capacitor Body Diameter (mm)	Body Diameter Code ¹	Bulk Bag (Loose)	Component pitch on carrier tape ²		
body blameter (mm)		(EUUSE)	25.4 mm		
7.0	0				
8.0	1				
9.0	2		1,000 pieces/box		
10.0	3	EOO niceas/bag			
11.0	4	500 pieces/bag			
13.0	6				
14.0	7		500 pieces/box		
15.0	8				

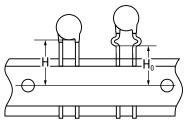
¹ The "Body Diameter Code" is located in the third character position of the ordering code. This code identifies the maximum diameter of the capacitor body in millimeters. For more information regarding the ordering code, see "Ordering Information" section of this document.

² For details regarding component pitch on carrier tape, see "Ammo Pack Taping Format" and "Ammo Pack Taping Specifications" sections of this document.



Figure 1 - Ammo Pack Taping Format (10 mm Lead Spacing)





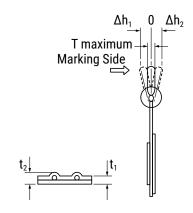


Table 3 - Ammo Pack Taping Specifications

Lead Spacing		10 mm		
Lead Style		Straight	Preformed ¹	
Item	Symbol	Dimensions (mm)		
Lead Spacing	F	10.0±1.0		
Component Pitch	Р	25.4±2		
Sprocket Hole Pitch	P ₀	12.7±0.3		
Sprocket Hole Center to Component Center	P ₂	12.7±1.5		
Sprocket Hole Center to Lead Center	P ₁	7.7±1.5		
Body Diameter	D	See Table 1 - Product Ordering Codes and Ratings		
Component Alignment (side/side)	ΔS	0±2.0		
Carrier Tape Width	W	18.0+1.0/-0.5		
Sprocket Hole Position	W ₁	9.0±0.5		
Height to Seating Plane ² (preformed leads ¹)	H _o	N/A 18.0 +2.0/-0		
Height to Seating Plane ² (straight leads)	Н	20.0 +1.5/-1.0	N/A	
Lead Protrusion	ę	2.0 maximum		
Diameter of Sprocket Hole	D _o	4.0±0.2		
Lead Diameter	φd	0.55±0.1		
Carrier Tape Thickness	t ₁	0.6±0.3		
Total Thickness (Carrier Tape, Hold-Down Tape and Lead)	t ₂	1.5 maximum		
Component Alignment (front/back)	Δh ₁	2.0 maximum		
	Δh ₂	2.0 maximum		
Cut Out Length	L	11.0 maximum		
Hold-Down Tape Width	W ₀	11.0 minimum		
Hold-Down Tape Position	W ₂	1.5±1.5		
Coating Extension on Leads (meniscus)	е	3.0 maximum for straight lead; not to exceed the bend for preformed lead configurations.		
Body Thickness	T	7.0 Maximum		

¹ Preformed (crimped) lead configurations include vertical kink and outside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

² Also referred to as "lead length" in this document.

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Application Notes:

Storage and Operating Conditions:

The Insulating coating of these devices does not form an air and moisture tight seal. Avoid exposure to moisture and do not use or store these devices in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt, or the like are present. Before cleaning, bonding, or molding these devices, it is important to verify that your process does not affect product quality and performance. KEMET recommends testing and evaluating the performance of a cleaned, bonded, or molded product prior to implementing and/or qualifying any of these processes. Store the capacitors where the temperature and relative humidity do not exceed 40 degrees centigrade and 70% respectively. For optimum solderability, capacitor stock should be used promptly, preferably within 6 months of receipt.

Working Voltage:

Application voltage (Vp-p or Vo-p) must not exceed the voltage rating of the capacitor. Irregular voltages can be generated for a transient period of time when voltage is initially applied and/or removed from a circuit. It is important to choose a capacitor with a voltage rating greater than or equal to these irregular voltages.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement	Vo-p	Vo-p	Vp-p	Vp-p	Vp-p

Operating Temperature and Self-Generating Heat:

The surface temperature of a capacitor should be kept below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. Temperature rise due to self-generated heating should not exceed 20°C (while operated at an atmosphere temperature of 25°C).

Handling - Vibration and Impact:

Do not expose these devices or their leads to excessive shock or vibration during use.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

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