



Product Summary

BV _{DSS}	Rds(on) Max	I _D Max T _A = +25°C
	110mΩ @ V _{GS} = -10V	-4.2A
-60V	130mΩ @ V _{GS} = -4.5V	-3.9A

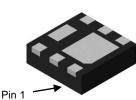
Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

U-DFN2020-6 (Type F)

- Battery Management Application
- Power Management Functions
- DC-DC Converters





Top View

Bottom View

P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Notes 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

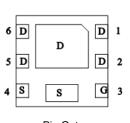
This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

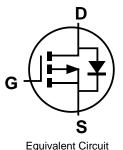
 An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMP6110SFDFQ</u>)

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208⁽²⁾
- Weight: 0.007 grams (Approximate)







Ordering Information (Note 4)

Part Number	Case	Packaging
DMP6110SFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel
DMP6110SFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1:



P0 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date	Code	Key
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Year	2015		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	С		Н		J	K	L	М	Ν	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2:



 $\begin{array}{l} P0 = Product Type Marking Code \\ YWX = Date Code Marking \\ Y = Year (ex: 0 = 2020) \\ W = Week (ex: a = week 27; z represents week 52 and 53) \\ X = Internal Code (ex: U = Monday) \end{array}$

Date Code Key

Year	2015		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	5		0	1	2	3	4	5	6	7	8	9
Week		1-26			27-52				53			
Code		A	-Z			a-z			Z			
Internal Code	Sur	۱ I	Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т		U		V	V	V	Х		Y		Z



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit			
Drain-Source Voltage			VDSS	-60	V	
Gate-Source Voltage			V _{GSS}	±20	V	
	Steady State	T _A = +25°C T _A = +70°C	lo	-3.5 -2.8	А	
Continuous Drain Current (Note 6) $V_{GS} = -10V$	t<10s	T _A = +25°C T _A = +70°C	lo	-4.2 -3.4	A	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	-20	A	
Continuous Source-Drain Diode Current (Note 6)	T _A = +25°C	ls	-2.1	А		
Avalanche Current (Note 7) L = 0.1mH	las	-19	A			
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	18	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Dower Dissipation (Note 5)	T _A = +25°C	D -	0.76	W	
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.47		
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{0JA}	167	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	Reja	121		
Total Power Dissipation (Note 6)	T _A = +25°C	D-	1.97	W	
Total Fower Dissipation (Note 0)	T _A = +70°C	PD	1.30		
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Davi	64	°C/W	
	t<10s	Reja	42		
Thermal Resistance, Junction to Case (Note 6)	Steady State	Rejc	8		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)	Symbol	IVIIII	тур	IVIAX	Unit	Test condition
Drain-Source Breakdown Voltage	BVDSS	-60	_	_	V	Vgs = 0V, Id = -250µA
Zero Gate Voltage Drain Current $T_J = +25^{\circ}C$				-1	μA	$V_{\rm GS} = 00, 10 = -250 \mu \text{A}$ $V_{\rm DS} = -48 \text{V}, V_{\rm GS} = 0 \text{V}$
Gate-Source Leakage	IDSS			±100	nA	
ON CHARACTERISTICS (Note 8)	Igss			±100	IIA	$V_{GS} = \pm 16V, V_{DS} = 0V$
Gate Threshold Voltage	Veerru	-1	_	-3	V	Vps = Vgs, Ip = -250µA
	Vgs(th)	-1		-3	v	
Static Drain-Source On-Resistance	RDS(ON)	_	_		mΩ	$V_{GS} = -10V, I_D = -4.5A$
	. ,		_	130		$V_{GS} = -4.5V, I_D = -3.5A$
Diode Forward Voltage	Vsd	—	-0.7	-1.2	V	$V_{GS} = 0V$, $I_S = -1A$
DYNAMIC CHARACTERISTICS (Note 9)			1			
Input Capacitance	Ciss	—	969	—		$V_{DS} = -30V, V_{GS} = 0V, f =$
Output Capacitance	Coss	—	58	—	pF	1.0MHz
Reverse Transfer Capacitance	Crss	—	44	—		
Gate Resistance	Rg	—	14	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = -4.5V)	Q _G	_	8.2	—		
Total Gate Charge (V _{GS} = -10V)	QG	—	17.2	—	-0	
Gate-Source Charge	QGS	—	3.0	—	nC	$V_{DS} = -30V, I_{D} = -12A$
Gate-Drain Charge	Q _{GD}	—	3.1	—		
Turn-On Delay Time	td(on)	—	4.4	_		
Turn-On Rise Time	t _R	—	23	—		$V_{GS} = -10V, V_{DS} = -30V, R_{GEN} =$
Turn-Off Delay Time	tD(OFF)	—	34	—	ns	6Ω, I _D = -12A
Turn-Off Fall Time	tF	—	42	—		
Reverse Recovery Time	t _{RR}	—	13.2	—	ns	I _S = -12A, di/dt = -100A/µs
Reverse Recovery Charge	Qrr	_	6.2	—	nC	Is = -12A, di/dt = -100A/µs

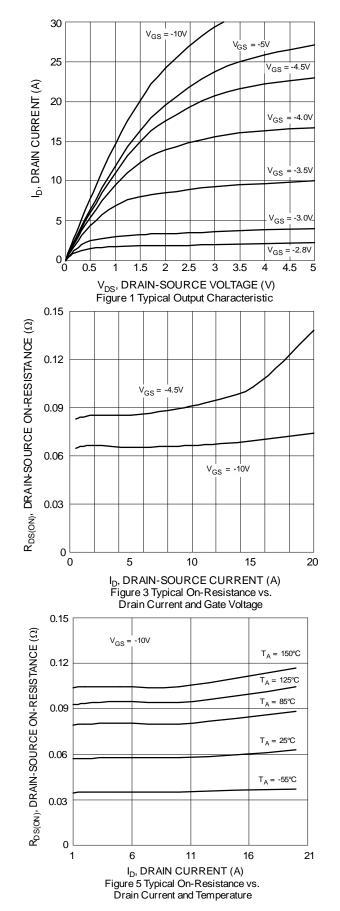
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate. Notes:

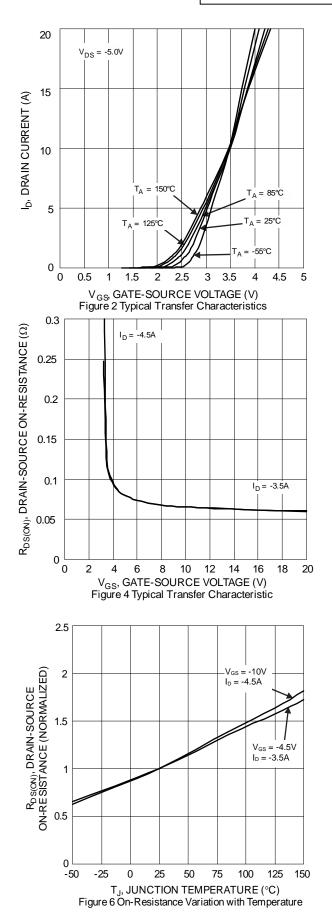
7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

8. Short duration pulse test used to minimize self-heating effect.

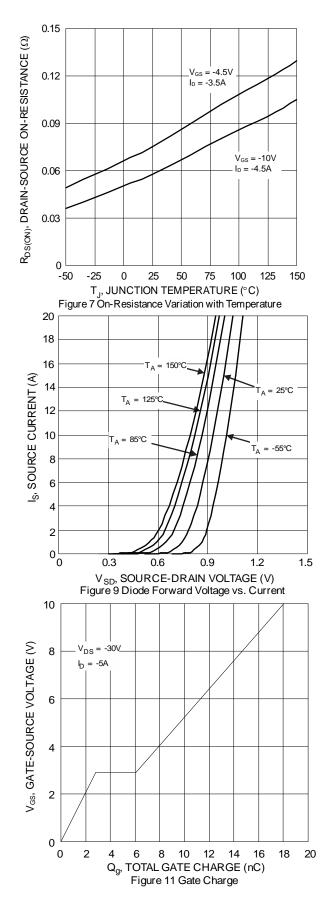
9. Guaranteed by design. Not subject to product testing.

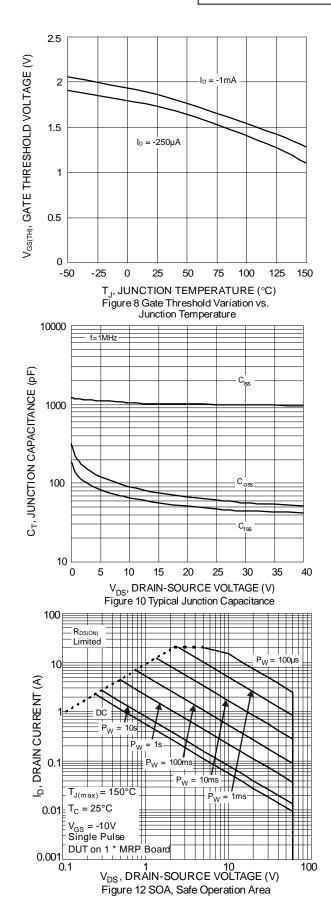




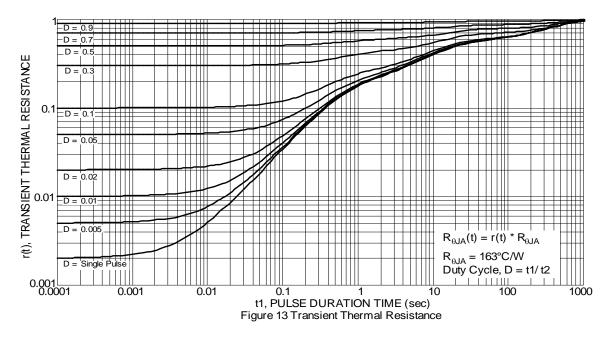








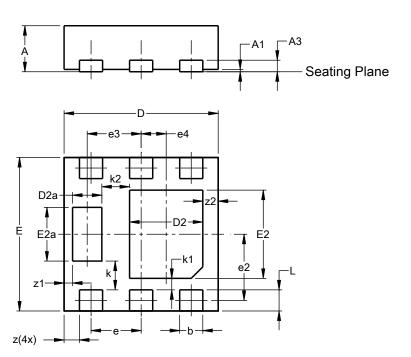






Package Outline Dimension

Please see http://www.diodes.com/package-outlines.html for the latest version.



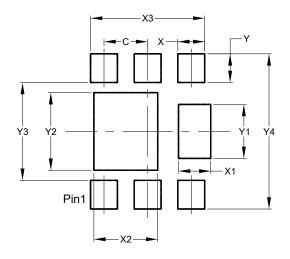
	U-DFN2020-6 (Type F)						
Dim	Min	Max	Тур				
Α	0.57	0.63	0.60				
A1	0.00	0.05	0.03				
A3	-	-	0.15				
b	0.25	0.35	0.30				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
D2a	0.33	0.43	0.38				
E	1.95	2.05	2.00				
E2	1.05	1.25	1.15				
E2a	0.65		0.70				
е		0.65 BS					
e2).863 BS					
e3		0.70 BS					
e4).325 BS					
k		0.37 BS					
k1		0.15 BS	-				
k2		0.36 BS					
L	0.225	0.325	0.275				
z		0.20 BS					
z1).110 BS					
z2		0.20 BS					
All C)imens	ions in	mm				

U-DFN2020-6 (Type F)

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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