



COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

Device	BV _{DSS}	Rds(on) Max	I _D Max T _A = +25°C
Q1	20V	0.5Ω @ V _{GS} = 4.5V	1030mA
Qi	200	0.9Ω @ V _{GS} = 1.8V	740mA
Q2	-20V	1.0Ω @ V _{GS} = -4.5V	-700mA
Q2	-20V	2.0Ω @ V _{GS} = -1.8V	-460mA

Description

This new generation MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Load Switch

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage V_{GS(TH)} <1V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- Ultra-Small Surface Mount Package
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SOT563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.003 grams (Approximate)

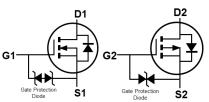


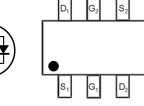




Top View Bottom View

SOT563





Internal Schematic Top View Pin Out

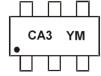
Ordering Information (Note 4)

Part Number	Case	Packaging
DMC2400UV-7	SOT563	3000/Tape & Reel
DMC2400UV-7B	SOT563	8000/Tape & Reel (Note 5)
DMC2400UV-13	SOT563	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.
- 5. Change the pitch from 4mm to 2mm in T&R.

Marking Information



CA3 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2011		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	Υ		Н	I	J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings - Q1 N-CHANNEL (@TA = +25°C, unless otherwise specified.)

Characteristic	Characteristic					
Drain-Source Voltage	V_{DSS}	20	V			
Gate-Source Voltage	V _{GSS}	±12	V			
St		$T_A = +25$ °C $T_A = +70$ °C	lD	1030 800	mA	
Continuous Drain Current (Note 7) V _{GS} = 4.5V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	1150 900	mA	
Continuous Dunis Courant (Nata 7) V 4 0)/	Steady State	$T_A = +25$ °C $T_A = +70$ °C	lο	740 570	mA	
Continuous Drain Current (Note 7) V _G S = 1.8V	t<10s	T _A = +25°C T _A = +70°C	lσ	870 700	mA	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	I _{DM}	3	Α			
Maximum Body Diode Continuous Current			Is	800	mA	

Maximum Ratings - Q2 P-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit	
Drain-Source Voltage	VDSS	-20	V			
Gate-Source Voltage	Vgss	±8	V			
Ocaliana Paris Ocarant (Nata 7) V	Steady State	T _A = +25°C T _A = +70°C	ID	-700 -550	mA	
Continuous Drain Current (Note 7) V _{GS} = -4.5V	t<10s	T _A = +25°C T _A = +70°C	I _D	-820 -640	mA	
Continuous Davis Courset (Nata 7) V 4 0)/	Steady State	T _A = +25°C T _A = +70°C	I _D	-460 -350	mA	
Continuous Drain Current (Note 7) V _{GS} = -1.8V	t<10s	T _A = +25°C T _A = +70°C	lo	-550 -420	mA	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%		I _{DM}	-2	А		
Maximum Body Diode Continuous Current			Is	-800	mA	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)	PD	0.45	W	
Thermal Designation to Ambient (Note 6)	Steady State	D	281	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	RөJA	210	°C/W
Total Power Dissipation (Note 7)		P _D	1	W
Thermal Desistance Junction to Ambient (Note 7)	Steady State	D	129	°C/W
Thermal Resistance, Junction to Ambient (Note 7) t<10s		RөJA	97	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Notes:

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

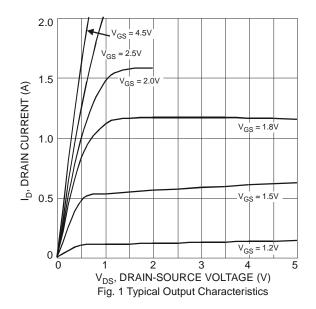


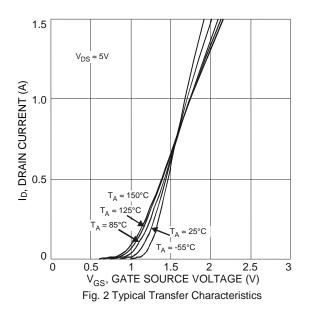
Electrical Characteristics - Q1 N-CHANNEL (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	VGS = 0V, ID = 1mA
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	100	nA	V _{DS} = 20V, V _{GS} = 0V
Coto Source Legicare		_	_	±1		Vgs = ±5V, Vps = 0V
Gate-Source Leakage	IGSS	_	_	±4.0	μA	Vgs = ±8V, Vps = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	Vgs(th)	0.5		0.9	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
		_	0.3	0.48		$V_{GS} = 5.0V, I_D = 200mA$
		_	0.35	0.5		$V_{GS} = 4.5V, I_D = 200mA$
Static Drain-Source On-Resistance	D	_	0.45	0.7	Ω	$V_{GS} = 2.5V, I_D = 200mA$
Static Drain-Source On-Resistance	RDS(ON)	_	0.55	0.9		$V_{GS} = 1.8V, I_D = 100mA$
		_	0.65	1.5		$V_{GS} = 1.5V, I_D = 50mA$
		_	2	_		V _G S = 1.2V, I _D = 1mA
Forward Transfer Admittance	Y _{fs}	_	1.4	_	S	V _{DS} = 3V, I _D = 200mA
Diode Forward Voltage	VsD	_	0.7	1.2	V	V _G S = 0V, I _S = 500mA
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	_	37.1	_		
Output Capacitance	Coss	_	6.5	_	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	4.8	_		1 = 1.0ivii iz
Gate Resistance	Rg	_	68	_	Ω	V _{DS} = 0V, V _{GS} = 0V
Total Gate Charge	Qg	_	0.5	_		
Gate-Source Charge	Qgs	_	0.07	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250mA$
Gate-Drain Charge	Q _{gd}	_	0.1	_		ID = 250MA
Turn-On Delay Time	tD(ON)	_	4.06	_		\/ 40\/ \/ 45\/
Turn-On Rise Time	t _R	_	7.28	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$ $R_L = 47\Omega, R_G = 10\Omega,$
Turn-Off Delay Time	tD(OFF)	_	13.74	_	115	$RL = 47\Omega$, $RG = 10\Omega$, ID = 200 mA
Turn-Off Fall Time	tF	_	10.54	_		15 - 2001174

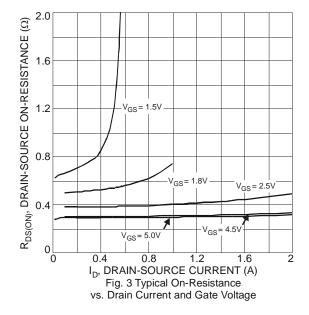
Notes:

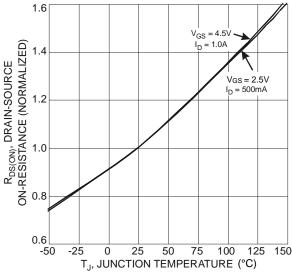
- 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to product testing.

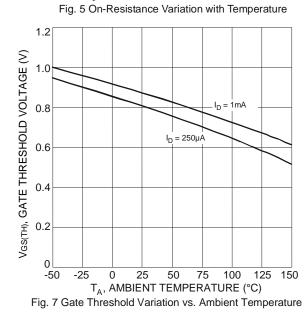












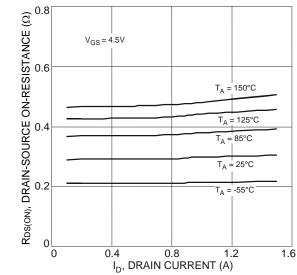


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

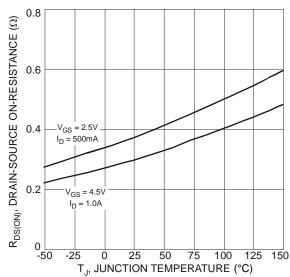


Fig. 6 On-Resistance Variation with Temperature

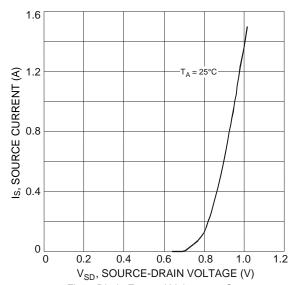
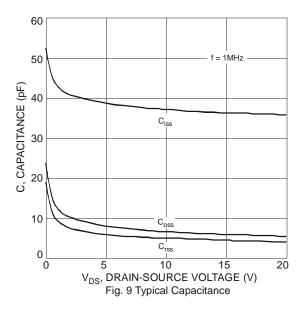
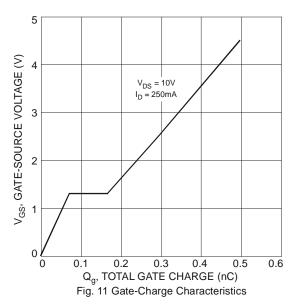


Fig. 8 Diode Forward Voltage vs. Current







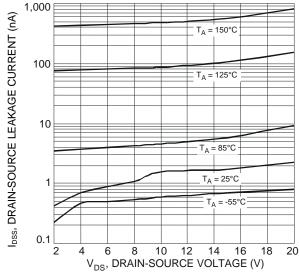
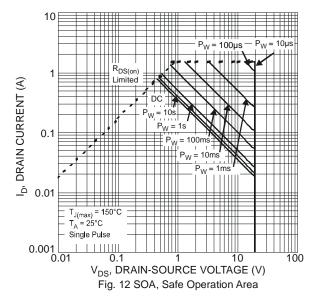


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage



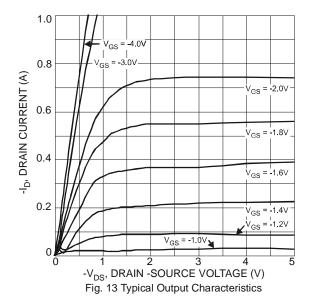


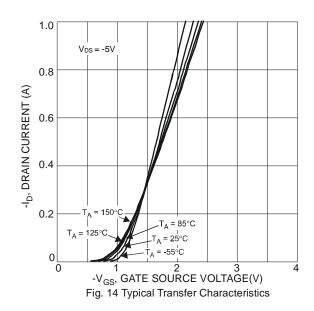
Electrical Characteristics - Q2 P-CHANNEL (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V$, $I_D = -1mA$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	-100	nA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	looo	_	_	±1.0	μΑ	$V_{GS} = \pm 5V$, $V_{DS} = 0V$
Gate-Source Leakage	Igss	_	_	±5.0	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	-0.5	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
		_	0.67	0.97		$V_{GS} = -5V, I_{D} = -100mA$
		_	0.7	1.0		$V_{GS} = -4.5V$, $I_{D} = -100mA$
Static Drain-Source On-Resistance	Dro(ot)	_	0.9	1.5	Ω	$V_{GS} = -2.5V, I_D = -80mA$
Static Dialit-Source Off-Nesistance	RDS(ON)	_	1.2	2.0	Ω	$V_{GS} = -1.8V, I_{D} = -40mA$
			1.5	3.0		$V_{GS} = -1.5V, I_{D} = -30mA$
		-	5	_		$V_{GS} = -1.2V, I_{D} = -1mA$
Forward Transfer Admittance	Y _{fs}	-	0.7	_	S	$V_{DS} = -3V, I_{D} = -100mA$
Diode Forward Voltage	V_{SD}	-	-0.75	-1.2	V	$V_{GS} = 0V, I_{S} = -330mA$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	-	46.1	_		101/11/101/
Output Capacitance	Coss	-	7.2	_	pF	V _{DS} = -10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss		4.9	_		1 = 1.0WH IZ
Gate Resistance	R_g	-	14.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$
Total Gate Charge (Vgs = -4.5V)	Qg		0.5	_		
Total Gate Charge (V _{GS} = -10V)	Qg		0.85	_	nC	$V_{DS} = -10V, I_{D} = -250mA$
Gate-Source Charge	Qgs	_	0.09	_	nc	·
Gate-Drain Charge	Qgd	_	0.09	_		
Turn-On Delay Time	t _{D(ON)}	_	8.5	_		V 0V V 0.5V
Turn-On Rise Time	t _R	_	4.3	_	no	$V_{DD} = -3V$, $V_{GS} = -2.5V$,
Turn-Off Delay Time	tD(OFF)	_	20.2	_	ns	$R_L = 300\Omega$, $R_G = 25\Omega$, $I_D = -100$ mA
Turn-Off Fall Time	tF		19.2	_		ID = -TOUTIA

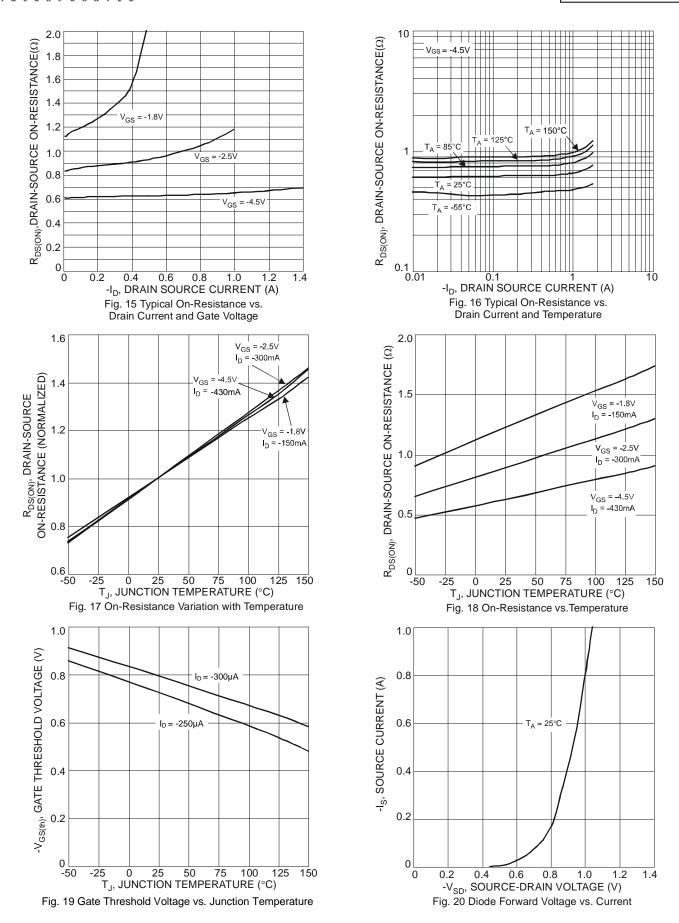
Notes:

- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to product testing.

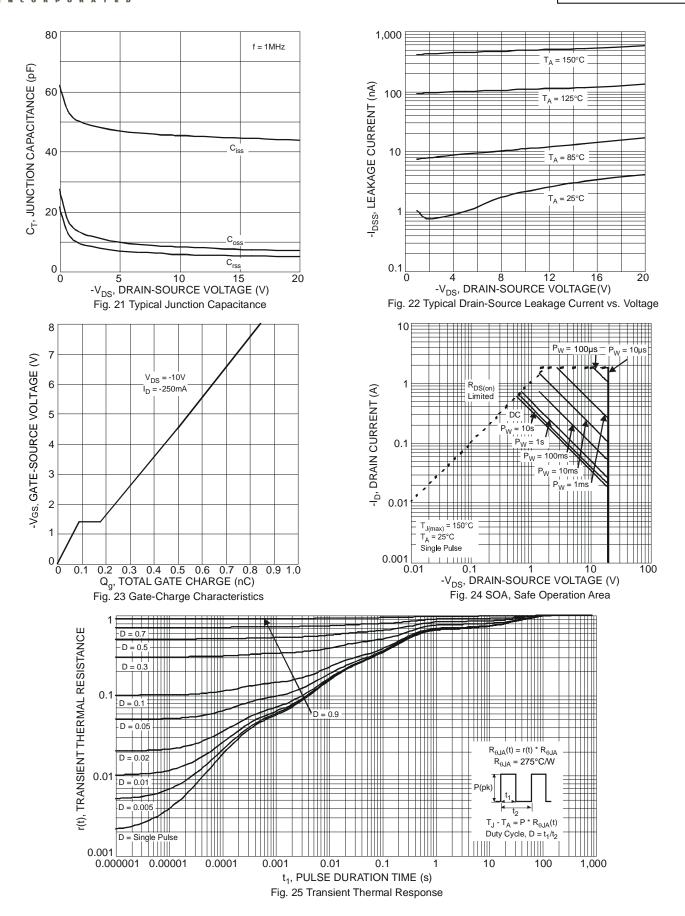










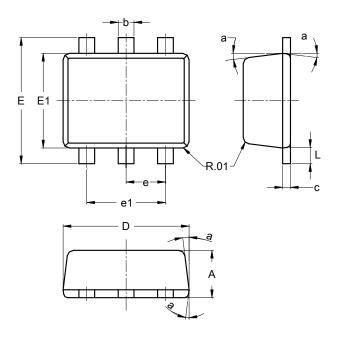




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

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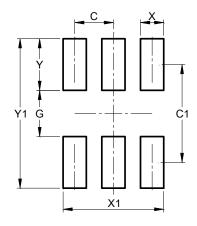


SOT563						
Dim	Min	Max	Тур			
Α	0.55	0.60	0.60			
b	0.15	0.30	0.20			
С	0.10	0.18	0.11			
D	1.50	1.70	1.60			
E	1.55	1.70	1.60			
E1	1.10	1.25	1.20			
е			0.50			
e1	0.90	1.10	1.00			
L	0.10	0.30	0.20			
а	8°	9°	7°			
All	Dimens	sions in	mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT563



Dimensions	Value (in mm)
С	0.500
C1	1.270
G	0.600
Х	0.300
X1	1.300
Y	0.670
V1	1 040



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