



DT1446-04TS

### 4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

## **Product Summary**

V <sub>BR (min)</sub>	I <sub>PP (max)</sub>	C <sub>T (typ)</sub>
6V	4.7A	0.55pF

## **Description**

The DT1446-04TS is a high performance device suitable for protecting four high speed I/Os and one  $V_{CC}$ . These devices are assembled in TSOT26 package. They have high ESD surge capability and low capacitance.

# **Applications**

 Typically Used for High Speed Ports such as USB 2.0, IEEE1394, HDMI, Laptop and Personal Computers, Flat Panel Displays, Video Graphics Displays, SIM Ports

TSOT26



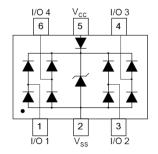
Top View

### **Features**

- IEC 61000-4-2 (ESD): Air ±19kV, Contact ±16kV
- Low Channel Input Capacitance of 0.55pF Max
- ESD Protection for four I/Os and one V<sup>CC</sup>
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

### **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020 (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.013 grams (approximate)



**Device Schematic** 

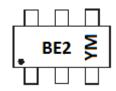
### **Ordering Information** (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DT1446-04TS-7	Standard	BE2	7	8	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



BE2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: A = 2013) M = Month (ex: 9 = September)

Date Code Key

_	Date Code ite						
	Year	2013	2014	2015	2016	2017	2018
	Code	Α	В	С	D	E	F

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# 

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current ,per IEC 61000-4-5	I <sub>PP_I/O</sub>	4.7	А	I/O to V <sub>SS</sub> , 8/20μs
Operating Voltage (DC)	$V_{DC}$	6	V	V <sub>CC</sub> to V <sub>SS</sub>
ESD Protection – Contact Discharge	V <sub>ESD_I/O</sub>	±16	kV	I/O to V <sub>SS</sub> , per IEC 61000-4-2
LSD Flotection – Contact Discharge	$V_{ESD}V_{CC}$	±30	kV	V <sub>CC</sub> to V <sub>SS</sub> , per IEC 61000-4-2
ESD Protection – Air Discharge, per IEC 61000-4-2	V <sub>ESD_I/O</sub>	±19	kV	I/O to V <sub>SS</sub> , per IEC 61000-4-2
L3D Flotection - All Discharge, per IEC 61000-4-2	$V_{ESD}V_{CC}$	±30	kV	V <sub>CC</sub> to V <sub>SS</sub> , per IEC 61000-4-2

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	$R_{\theta JA}$	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	VRWM	_	_	5.0	V	V <sub>CC</sub> to V <sub>SS</sub>
Reverse Current (Note 6)	I <sub>R(</sub> V <sub>CC to</sub> V <sub>SS)</sub>	_	_	5.0	μA	$V_R = V_{RWM} = 5V$ , $V_{CC}$ to $V_{SS}$
Reverse Current (Note 6)	I <sub>R(IO to</sub> V <sub>SS)</sub>		_	1.0	μA	$V_R = V_{RWM} = 5V$ , any I/O to $V_{SS}$
Reverse Breakdown Voltage	V <sub>BR</sub>	6.0	_	9.0	V	$I_R = 1mA$ , $V_{CC}$ to $V_{SS}$
Forward Clamping Voltage	V <sub>F</sub>		0.8	1.0	V	$I_F = 15mA$ , $V_{SS}$ to $V_{CC}$
Reverse Clamping Voltage (Note 7)	V <sub>C_I/O</sub>	_	8.5	_	V	$I_{PP}$ =4.7A, I/O to V <sub>SS</sub> , 8/20µs
ESD Clamping Voltage	VESD_VCC	_	10	_	V	TLP, 20A, tp = 100 ns, $V_{CC}$ to $V_{SS}$
ESD Clamping Voltage	Vesd_i/o	_	12	_	V	TLP, 20A, tp = 100 ns, I/O to V <sub>SS</sub>
Dynamia Registance	$R_{DIF}V_{CC}$	_	0.14	_	Ω	TLP, 20A, tp = 100 ns, $V_{CC}$ to $V_{SS}$
Dynamic Resistance	R <sub>DIF_I/O</sub>	_	0.3	_	Ω	TLP, 20A, tp = 100 ns, I/O to V <sub>SS</sub>
Channel Input Capacitance	C <sub>I/O to</sub> V <sub>SS</sub>	_	0.55	0.65	pF	$V_R = 2.5V$ , $V_{CC} = 5V$ , $f = 1MHz$
Channel Input Capacitance	C <sub>I/O to</sub> V <sub>SS</sub>	_	0.65	_	pF	$V_R = 2.5V$ , $V_{CC} =$ floating, $f = 1MHz$
Variation of Channel Input Capacitance	CI/OMAX-CI/OMIN	_	0.03	_	pF	$V_{CC} = 5V$ , $V_{SS} = 0V$ , $I/O = 2.5V$ , $f = 1MHz$ , $T = +25^{\circ}C$ , $C_{I/OMAX} - C_{I/OMIN}$
Variation of Channel Input Capacitance	C <sub>I/OMAX</sub> -C <sub>I/OMIN</sub>	_	0.05	_	pF	$V_{CC}$ = floating , $V_{SS}$ = 0V, I/O = 2.5V, f = 1MHz, T = +25°C , $C_{I/OMAX}$ - $C_{I/OMIN}$

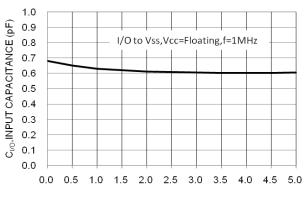
Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Clamping voltage value is based on an 8x20µs peak pulse current (Ipp) waveform.

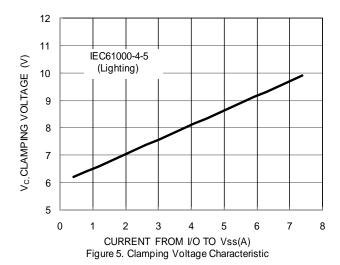


# 100 BEAK PULSE DERATING IN % OH CARRENT OF THE PERATING IN % O

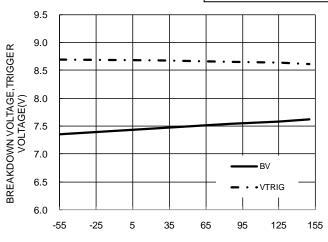
T<sub>A</sub>,AMBIENT TEMPERATURE(°C) Figure1. Pulse Derating Curve



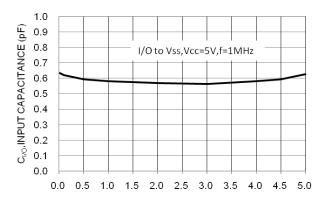
 $V_{\text{I/O}}$ , INPUT VOLTAGE (V) Figure 3. Input Capacitance vs. Input Voltage



### DT1446-04TS



T<sub>A</sub>, AMBIENT TEMPERATURE (°C) Figure 2. BV, Trigger Voltage vs. Ambient Temperature



V<sub>I/O,</sub> INPUT VOLTAGE (V) Figure 4. Input Capacitance ∨s. Input Voltage

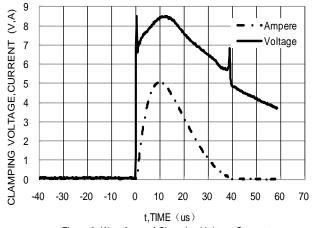


Figure 6. Waveform of Clamping Voltage, Current vs. Time(8/20us, VO to Vss)



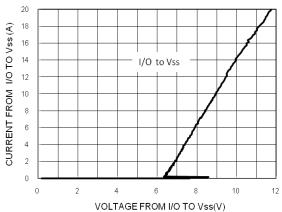
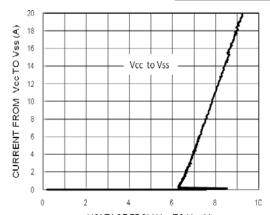


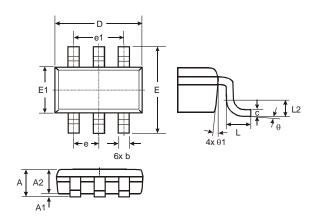
Figure 7. Transmission Line Pulsing (TLP) Measurement
Current vs. Voltage



VOLTAGE FROM Vcc TO Vss(V)
Figure 8. Transmission Line Pulsing (TLP) Measurement
Current vs. Voltage

# **Package Outline Dimensions**

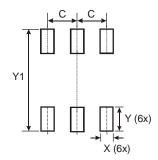
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	TSOT26								
Dim	Min	Max	Тур						
Α	-	1.00	_						
<b>A</b> 1	0.01	0.10	_						
A2	0.84	0.90	_						
D	_	-	2.90						
Е	_	-	2.80						
E1	_	-	1.60						
b	0.30	0.45	_						
С	0.12	0.20	_						
е	_	-	0.95						
e1	e1 _		1.90						
L	0.30	0.50							
L2	_	-	0.25						
θ	<b>θ</b> 0°		4°						
θ1	4°	12°	_						
All D	All Dimensions in mm								

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.950
Х	0.700
Υ	1.000
Y1	3.199

July 2014



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