

AFBR-S4N66P024M 2×1 NUV-MT Silicon Photomultiplier Array



Description

The Broadcom[®] AFBR-S4N66P024M is a Silicon Photomultiplier (SiPM) array used for ultra-sensitive precision measurements of single photons. Two 6 mm × 6 mm SiPMs are arranged in a 2×1 element array with a pitch of 7 mm. Larger areas can be covered with a SiPM-pitch of 7 mm by tiling multiple AFBR-S4N66P024M arrays. The passivation layer is a clear epoxy mold compound (EMC) highly transparent down to UV wavelengths. This results in a broad response in the visible light spectrum with high sensitivity towards blue and near-UV region of the light spectrum. The array is best suited for the detection of low-level pulsed light sources, especially for detection of Cherenkov or scintillation light from the most common organic (plastic) and inorganic scintillator materials (for example, LSO, LYSO, BGO, Nal, Csl, BaF, LaBr). This product is lead-free and RoHS compliant.

Block Diagram

Figure 1: AFBR-S4N66P024M Block Diagram of Single SiPM Element

$R_{q} \ge R_{q} R_{q} \ge R_{q}$

Features

- 2×1 SiPM array
- Array size 13.54 mm × 6.54 mm
- High PDE (63% at 420 nm)
- Excellent SPTR and CRT
- Excellent uniformity of breakdown voltage
- Excellent uniformity of gain
- Four-side tilable, with high fill factors
- Cell pitch 40 µm
- Highly transparent epoxy protection layer
- Operating temperature range from 0°C to +60°C
- RoHS, CFM, and REACH compliant

Applications

- X-ray and gamma ray detection
- Gamma ray spectroscopy
- Safety and security
- Nuclear medicine
- Positron emission tomography
- Life sciences
- Flow cytometry
- Fluorescence luminescence measurements
- Time-correlated single photon counting
- High energy physics
- Astrophysics

Mechanical Drawing and Pin Layout

Figure 2: Package Drawing with Dimensions



Figure 3: Recommended Landing Pattern Diagram



Note: Dimensions are in millimeters.

Reflow Soldering Diagram

Figure 4: Recommended Reflow Soldering Profile



Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause damage to the devices. Limits apply to each parameter in isolation. Absolute maximum ratings are those values beyond which damage to the device may occur if these limits are exceeded for other than a short period of time.

Parameter	Symbol	Min.	Max.	Units
Storage Temperature	T _{STG}	-20	+80	°C
Operating Temperature ^a	T _A	0	+60	°C
Soldering Temperature ^{b, c}	T _{SOLD}		245	°C
Lead Soldering Time ^{b, c}	t _{SOLD}		60	S
Electrostatic Discharge Voltage Capability HBM	ESD _{HBM}		2	kV
Electrostatic Discharge Voltage Capability CDM	ESD _{CDM}		500	V
Operating Over Voltage	V _{OV}	—	16	V

a. Biased at constant voltage = 12V above breakdown.

b. The AFBR-S4N66P024M is reflow-solderable according to solder diagram as shown in Figure 4.

c. Baking at 125°C for 16 hours is mandatory prior to soldering. MLD is according to MSL 6 with a floor life of 4 hours at 30°C and 60% relative humidity.

Device Identification

Each device can be identified and tracked by a unique data matrix code (DMC) on the back of the PCB. The code is structured as follows: YYWWNNNNN (Y – year, W – week, N – running number). An example DMC is shown in Figure 5.

Figure 5: Example Data Matrix Code for Device Identification



Device Specification

Features measured at 25°C unless otherwise specified.

Geometric Features

Parameter	Symbol	Value	Units
Device area	DA	13.54 × 6.54	mm ²
Total active area	AA	6 × 6 × 2	mm ²
Element active area	EAA	6 × 6	mm ²
Micro cell pitch	L _{cell}	40	μm
Number of micro cells per element	N _{cells}	22428	_

Optical and Electrical Features

Values are measured at 12V above breakdown.

Parameter	Symbol	Min.	Typ. ^a	Max.	Units	Reference Plots
Spectral range	λ	360		900	nm	Figure 6
Peak sensitivity wavelength	λ _{PK}	—	420	—	nm	Figure 6
Photo detection efficiency	PDE at λ_{PK}	—	63	—	%	Figure 7
Dark current per element	Ι _D	—	8.6	—	μA	Figure 8
Dark count rate per unit area ^b	DCR/mm ²	—	125	_	kcps	Figure 9
Dark count rate per element	DCR	_	4.4	_	Mcps	Figure 9
Gain	G	—	7.3	—	×10 ⁶	Figure 10
Optical crosstalk ^c	P _{Xtalk}	—	23	—	%	Figure 11
Afterpulsing probability	P _{AP}	—	< 1	—	%	
Recharge time constant	T _{fall}	—	55	—	ns	Figure 12
Breakdown Voltage	V _{BD}	—	32.5	—	V	Figure 8
Nominal terminal capacitance per element ^d	C _T	_	1550	_	pF	
Temperature coefficient of breakdown voltage (2327°C)	ΔV _{BR} /ΔT	_	30	_	mV/°C	
Temperature coefficient of gain (2327°C) ^e	ΔG/ΔΤ		1.46		×10 ⁴ /°C	

a. Typical values are measured at 12V above breakdown

b. Measured at 0.5 p.e. amplitude. Measurement does not include delayed correlated events.

c. Calculated as the sum of direct, delayed crosstalk, and after-pulsing probabilities.

d. Measured at 40V using input sine wave with f = 200 kHz and V_{in} = 500 mV.

e. Calculated from gain dependence on V and breakdown voltage temperature coefficient: $\Delta G/\Delta T = \Delta G/\Delta V \times \Delta V_{BR}/\Delta T$.

Reference Plots

Typical features measured at 25°C unless otherwise specified.

Figure 6: PDE vs. Wavelength at 12V OV



Figure 8: Reverse IV Curve in Dark Conditions



Figure 10: Gain vs. Overvoltage



80 70 60 50 40 30 5 5 10 15 20 Over voltage (V)

Figure 7: PDE vs. OV at 420 nm

Figure 9: Dark Count Rate vs. Overvoltage



Figure 11: Total Correlated Noise vs. Overvoltage



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Figure 12: Typical Pulse Waveform in Response of a Picosecond Laser-Pulse on a Load Impedance of 25Ω and Applied Bias of 12V above Breakdown



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