

PROTECTION PRODUCTS - TransClampTM Description

A TransClampTM is a low capacitance TVS array designed to protect high speed data interfaces. This series has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by **ESD** (electrostatic discharge), **CDE** (Cable Discharge Events), and **Lightning.**

These devices integrate low capacitance, surge-rated compensation diodes with a high power transient voltage suppressor (TVS). The compensation diodes are arranged in a bridge pattern allowing the device to be connected in common mode and/or differential mode. This allows the designer maximum flexibility and reduces parts count. The capacitance of the device is limited to 12pF maximum from line-to-line to ensure correct signal transmission on high-speed lines. These devices may be used to meet Bellcore GR-1089-CORE short-haul (intra-building) surge requirements and will withstand a minimum 100 A surge for a $2/10\mu$ s pulse.

The TClampTM0602N is in a 10-pin, RoHS/WEEE compliant, SLP2626P10 package. It measures 2.6 x 2.6 x 0.60mm. The leads are spaced at a pitch of 0.5mm and are finished with lead-free NiPdAu. They are particularly well suited for applications where board space is at a premium such as integrated connectors/magnetics and T1/E1 equipment.

Features

- Transient protection for high-speed data lines to Bellcore 1089 (Intra-Building) 100A (2/10µs) IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns) IEC 61000-4-5 (Lightning) L5, 95A (8/20µs)
- ◆ Protects two lines in common and differential mode
- Low capacitance (12pF line-to-line)
- Low operating voltages (6V)
- Low clamping voltage
- Small SLP Package saves board space
- ◆ Solid-state technology

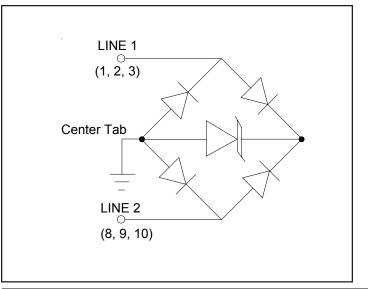
Mechanical Characteristics

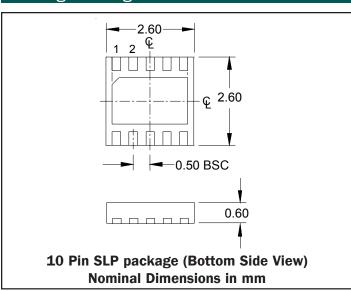
- SLP2626P10 10L package
- RoHS/WEEE Compliant
- Nominal Dimensions: 2.6 x 2.6 x 0.60 mm
- Lead Pitch: 0.5mm
- Molding compound flammability rating: UL 94V-0
- Marking: Marking Code
- Packaging: Tape and Reel

Applications

- ♦ T1/E1
- ♦ T3/E3
- 10/100 Ethernet
- Integrated Magnetics
- Carrier Class Equipment
- ♦ ISDN Interfaces

Package Configuration





Circuit Diagram

TClamp0602N



PROTECTION PRODUCTS

Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20µs)	P _{pk}	2500	Watts
Peak Pulse Current (tp = 2/10µs)	I _{PP}	120	A
Peak Pulse Current (tp = 8/20µs)	I _{PP}	95	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	30 30	kV
Operating Temperature	T,	-55 to +125	°C
Storage Temperature	T _{stg}	-55 to +150	°C

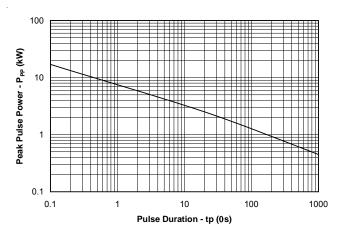
Electrical Characteristics (T=25°C)

TClamp0602N								
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units		
Reverse Stand-Off Voltage	V _{RWM}				6	V		
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA	6.8			V		
Reverse Leakage Current	I _R	V _{RWM} = 6V, T=25°C			5	μA		
Clamping Voltage	V _c	I _{PP} = 100A, tp = 2/10μs Line-to-Ground			25	V		
Clamping Voltage	V _c	$I_{pp} = 100A$, tp = 2/10µs Line-to-Line			29	V		
Junction Capacitance	C _j	Line-to-Gnd V _R = OV, f = 1MHz			25	pF		
		Line-to-Line V _R = OV, f = 1MHz			12	pF		

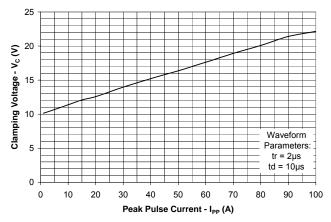


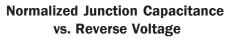
Typical Characteristics

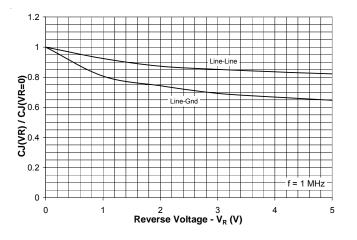
Non-Repetitive Peak Pulse Power vs. Pulse Time

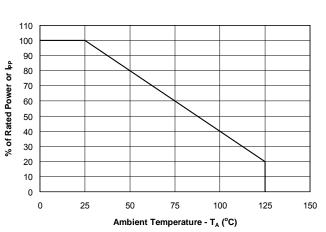


Clamping Voltage vs. Peak Pulse Current Line-to-Ground



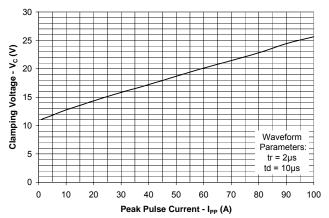






Power Derating Curve

Clamping Voltage vs. Peak Pulse Current Line-to-Line



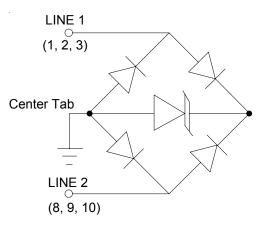


Applications Information

Device Connection Options for Protection of Two High-Speed Data Lines

These devices are designed to protect two high-speed data lines (one differential pair) from transient overvoltages which result from lightning and ESD. They can be configured to protect in differential (Line-to-Line) and common (Line-to-Ground) mode. Data line inputs/ outputs are connected at pins 1, 2 and 3, and 8, 9 and 10 as shown. For proper operation, pins 1 - 3 must be connected together and pins 8 - 10 must be connected together. Pins 4, 5, 6, and 7 left unconnected. For differential operation, the center tab is also left not connected. For common mode operation, the center tab is connected to ground. The ground connection should be made directly to a ground plane on the board for best results. The use of multiple vias is recommnded for reduced ground loop inductance.

Circuit Diagram



Pin Configuration (Top Side View)

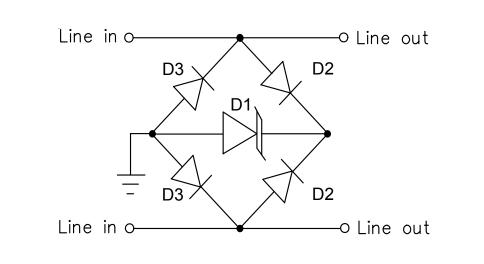
1 2 3 4	10 9 8 7
5	6

Pin	Identification		
1, 2, 3	Line 1 in/out		
8, 9, 10	Line 2 in/out		
4, 5, 6, 7	No Connect		
Center Tab	Ground		





Applications Information - Spice Model



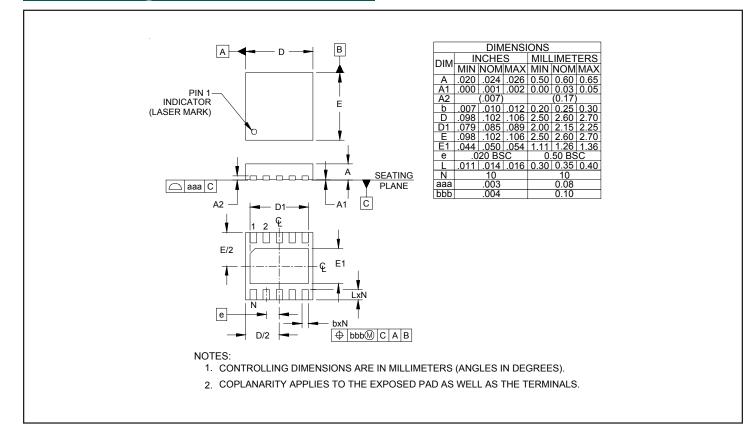
TClamp0602N Spice Model

TClamp0602N Spice Parameters							
Parameter	Unit	D1 (TVS)	D2 (LCRD)	D3 (LCRD)			
IS	Amp	1.4E-11	1.001E-20	1.001E-20			
BV	Volt	8.3	150	150			
VJ	Volt	0.56	0.59	0.59			
RS	Ohm	0.029	0.075	0.064			
IBV	Amp	1E-3	1E-3	1E-3			
CJO	Farad	300e-12	11.0E-12	11.0E-12			
TT	sec	2.541E-9	2.541E-9	2.541E-9			
М		0.256	0.01	0.01			
N		1.1	1.1	1.1			
EG	eV	1.11	1.11	1.11			

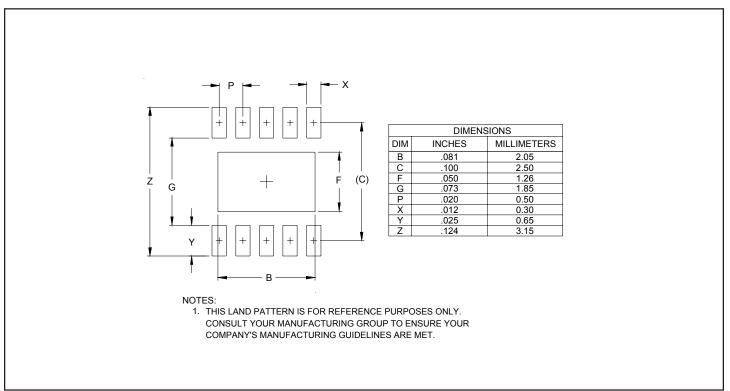




Outline Drawing - SLP2626P10



Land Pattern - SLP2626P10





TClamp0602N

PROTECTION PRODUCTS

Marking



Ordering Information

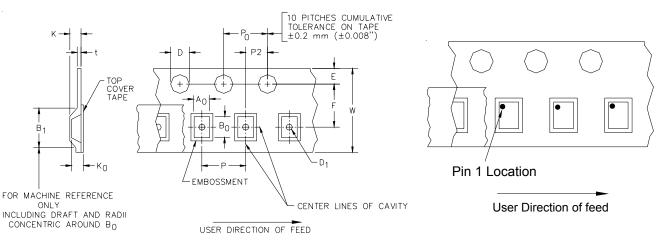
Part Number	Qty per Reel	Reel Size		
TClamp0602N.TCT	3,000	7 Inch		

Note: Lead finish is lead-free NiPdAu

TransClamp and TClamp are marks of Semtech Corporation

YY = year WW = Week

Tape and Reel Specification



Device Orientation in Tape

AO	В0	КО		
2.77 +/-0.05 mm	2.77 +/-0.05 mm	0.80 +/-0.05 mm		

	pe dth	B, (Max)	D	D1	E	F	K (MAX)	Ρ	PO	P2	T(MAX)	w
8 r	nm	4.2 mm (.165)	1.5 + 0.1 mm - 0.0 mm	1.0 mm ±0.05	1.750±.10 mm	3.5±0.05 mm	2.4 mm	4.0±0.1 mm	4.0±0.1 mm	2.0±0.05 mm	0.4 mm	8.0 mm + 0.3 mm - 0.1 mm

Contact Information

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