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April 1993 Revised April 2005

74LVX245

Low Voltage Octal Bidirectional Transceiver

General Description

The LVX245 contains eight non-inverting bidirectional buffers and is intended for bus-oriented applications. The Transmit/Receive (T/R) input determines the direction of data flow through the bidirectional transceiver. Transmit (active-HIGH) enables data from A ports to B ports; Receive (active-LOW) enables data from B ports to A ports. The Output Enable input, when HIGH, disables both A and B ports by placing them in a high impedance condition.

Features

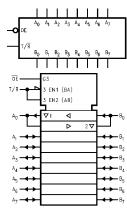
- Ideal for low power/low noise 3.3V applications
- Guaranteed simultaneous switching noise level and dynamic threshold performance

Ordering Code

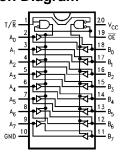
Order Number	Package Number	Package Description						
74LVX245M	M20B	20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300" Wide						
74LVX245SJ	M20D	Pb-Free 20-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide						
74LVX245MTC	MTC20	20-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide						

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code. Pb-Free package per JEDEC J-STD-020B.

Logic Symbols



Connection Diagram



Pin Descriptions

Pin Names	Description
ŌĒ	Output Enable Input
T/R	Transmit/Receive Input
A ₀ -A ₇	Side A Inputs or 3-STATE Outputs
B ₀ -B ₇	Side B Inputs or 3-STATE Outputs

Truth Table

Inp	uts	Outputs				
OE	T/R	Outputs				
L	L	Bus B Data to Bus A				
L	Н	Bus A Data to Bus B				
Н	X	HIGH-Z State				

H = HIGH Voltage Level L = LOW Voltage Level X = Immaterial

Absolute Maximum Ratings(Note 1)

Supply Voltage (V_{CC}) -0.5V to +7.0V

DC Input Diode Current (I_{IK})

 $V_I = -0.5V$ -20 mA

DC Input Voltage T/\overline{R} , \overline{OE} (V_I) -0.5V to 7V

DC Diode Current (I_{OK})

 $V_{O} = -0.5V$ -20 mA $V_{O} = V_{CC} + 0.5V$ +20 mA

DC Bus I/O Voltage ($V_{I/O}$) -0.5V to $V_{CC} + 0.5V$

DC Output Source

or Sink Current (I_O) ± 25 mA

 ${\rm DC} \; {\rm V}_{\rm CC} \; {\rm or} \; {\rm Ground} \; {\rm Current} \\$

(I_{CC} or I_{GND}) ±75 mA

Storage Temperature (T_{STG}) $-65^{\circ}C$ to $+150^{\circ}C$

Power Dissipation 180 mW

Recommended Operating Conditions (Note 2)

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions

Note 2: Unused inputs must be held HIGH or LOW. They may not float.

for actual device operation.

DC Electrical Characteristics

Symbol	Parameter	V _{CC}		T _A = +25°C	;	T _A = -40°	C to +85°C	Units	Conditions
Syllibol		• 00	Min	Тур	Max	Min	Max	Units	Conditions
V _{IH}	HIGH Level	2.0	1.5			1.5			
	Input	3.0	2.0			2.0		V	
	Voltage	3.6	2.4			2.4			
V _{IL}	LOW Level	2.0			0.5		0.5		
	Input	3.0			0.8		0.8	V	
	Voltage	3.6			0.8		0.8		
V _{OH}	HIGH Level	2.0	1.9	2.0		1.9			$V_{IN} = V_{IH} \text{ or } V_{IL} I_{OH} = -50 \mu\text{A}$
	Output	3.0	2.9	3.0		2.9		V	$I_{OH} = -50 \mu A$ $I_{OH} = -4 \text{ mA}$
	Voltage	3.0	2.58			2.48			$I_{OH} = -4 \text{ mA}$
V _{OL}	LOW Level	2.0		0.0	0.1		0.1		$V_{IN} = V_{IH} \text{ or } V_{IL} I_{OL} = 50 \mu\text{A}$
	Output	3.0		0.0	0.1		0.1	V	$I_{OL} = 50 \mu A$
	Voltage	3.0			0.36		0.44		$I_{OL} = 4 \text{ mA}$
l _{OZ}	3-STATE	3.6			±0.25		±2.5	μА	$V_{IN} = V_{IH}$ or V_{IL}
	Output								$V_{OUT} = V_{CC}$ or GND
	Off-State Current								
I _{IN}	Input Leakage Current	3.6			±0.1		±1.0	μА	V _{IN} = 5.5V or GND
I _{CC}	Quiescent Supply Current	3.6			4.0		40.0	μА	V _{IN} = V _{CC} or GND

Noise Characteristics (Note 3)

Symbol	Parameter	V _{CC} T _A =		25°C	Units	Conditions
Syllibol	r al allietei		Тур	Limit	Units	C _L (pF)
V _{OLP}	Quiet Output Maximum Dynamic V _{OL}	3.3	0.5	0.8	V	50
V _{OLV}	Quiet Output Minimum Dynamic V _{OL}	3.3	-0.5	-0.8	V	50
V _{IHD}	Minimum HIGH Level Dynamic Input Voltage	3.3		2.0	V	50
V _{ILD}	Maximum LOW Level Dynamic Input Voltage	3.3		0.8	V	50

Note 3: Input $t_r = t_f = 3 \text{ ns}$

AC Electrical Characteristics

Symbol	Parameter	v _{cc}	$T_A = +25^{\circ}C$			T _A =-40°0	C to +85°C	Units	Conditions
Cymbol		(V)	Min	Тур	Max	Min	Max	Onno	Conditions
t _{PLH}	Propagation Delay Time	2.7		6.1	10.7	1.0	13.5		C _L = 15 pF
t _{PHL}				8.6	14.2	1.0	17.0	ns	C _L = 50 pF
		3.3 ± 0.3		4.7	6.8	1.0	8.0	113	C _L = 15 pF
				7.2	10.1	1.0	11.5		C _L = 50 pF
t _{PZL}	3-STATE Output	2.7		9.0	16.9	1.0	20.5		$C_L = 15 \text{ pF}, R_L = 1 \text{ k}\Omega$
t _{PZH}	Enable Time			11.5	20.4	1.0	24.0	ns	$C_L = 50 \text{ pF}, R_L = 1 \text{ k}\Omega$
		3.3 ± 0.3		7.1	11.0	1.0	13.0	115	$C_L = 15 \text{ pF}, R_L = 1 \text{ k}\Omega$
				9.6	14.5	1.0	16.5		$C_L = 50 \text{ pF}, R_L = 1 \text{ k}\Omega$
t _{PLZ}	3-STATE Output	2.7		11.5	18.0	1.0	21.0	ns	$C_L = 50 \text{ pF}, R_L = 1 \text{ k}\Omega$
t _{PHZ}	Disable Time	3.3 ± 0.3		9.6	12.8	1.0	14.5	115	$C_L = 50 \text{ pF}, R_L = 1 \text{ k}\Omega$
t _{OSLH}	Output to Output Skew	2.7			1.5		1.5	ns	C _L = 50 pF (Note 4)
t _{OSHL}	(Note 4)	3.3			1.5		1.5	115	

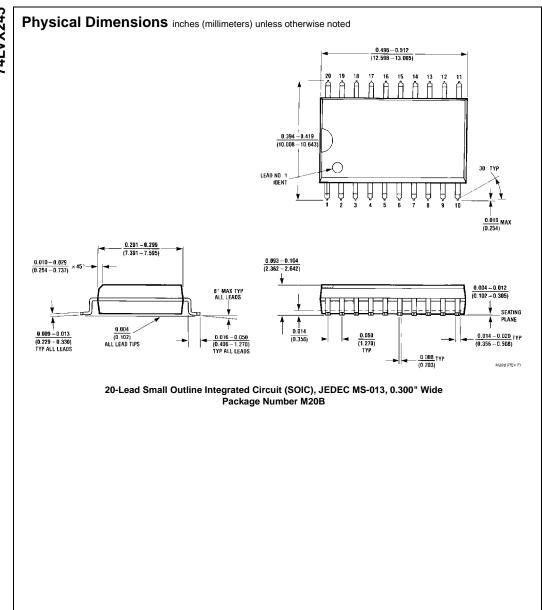
Note 4: Parameter guaranteed by design. $t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|$

Capacitance

Symbol	Parameter	$T_A = +25^{\circ}C$			T _A = -40°0	C to +85°C	Units
Cymbol	T drameter	Min	Тур	Max	Min	Max	os
C _{IN}	Input Capacitance T/R, OE		4	10		10	pF
C _{I/O}	Output Capacitance A _n , B _n		8				pF
C _{PD}	Power Dissipation Capacitance (Note 5)		21				pF

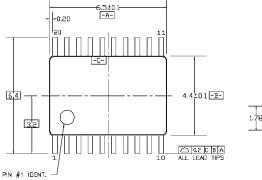
Note 5: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

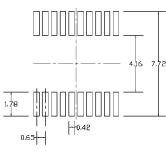
Average operating current can be obtained by the equation: $I_{CC(opt.)} = \frac{C_{PD} \times V_{CC} \times f_{IN} + I_{CC}}{8 \text{ (per bit)}}$



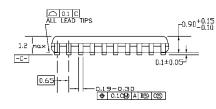
Physical Dimensions inches (millimeters) unless otherwise noted (Continued) 12.6±0.10 0.40 TYP --A-5.3±0.10 9.27 TYP 7.8 -B-3.9 0.2 C B A ALL LEAD TIPS 10 PIN #1 IDENT.-0.6 TYP 1.27 TYP LAND PATTERN RECOMMENDATION ALL LEAD TIPS SEE DETAIL A 0.1 C 1.8±0.1 -C-L _{0.15±0.05} 0.15-0.25 -1.27 TYP 0.35-0.51 ⊕ 0.12 **(** C A DIMENSIONS ARE IN MILLIMETERS GAGE PLANE 0.25 NOTES: A. CONFORMS TO EIAJ EDR-7320 REGISTRATION, ESTABLISHED IN DECEMBER, 1998. B. DIMENSIONS ARE IN MILLIMETERS. C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS. 0.60±0.15 SEATING PLANE 1.25 -M20DRevB1 DETAIL A Pb-Free 20-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide Package Number M20D

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)





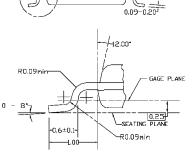
LAND PATTERN RECOMMENDATION



DIMENSIONS ARE IN MILLIMETERS



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 B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLDS FLASH, AND TIE BAR EXTRUSIONS.
- D. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M. 1982.



SEE DETAIL A

DETAIL A

MTC20REVD1

20-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide Package Number MTC20

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