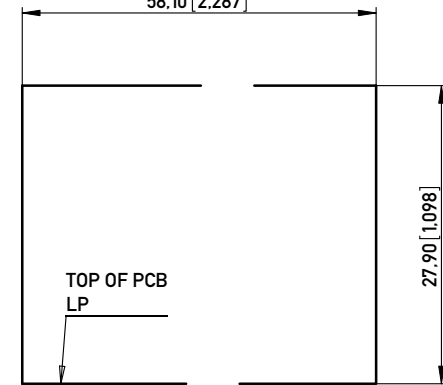


RECOMMENDED PANEL CUTOUT
EMPFOHLENER FRONTPLATTEN-AUSSCHNITT



Technical specifications

Materials & Finish	Standard applic.	Value
Insulation body	Standard description	PBT 30%
Contact material	Standard description	CuSn5
Contact finish, mating zone	Thickness of plating	30 µin Au over 50 µin Ni
Contact finish termination zone	Thickness of plating	see chart
Shell/shield material	Standard description	C2680 (acc. JIS)
Shell/shield plating	Thickness of plating	50 µin Ni

Assembly process	Value
Packaging	Tray
Solder temperature	235°C at 3-5s
Suitable assembly process	wave

Approvals	Value
UL insulation body	UL 94 V0
UL File No.	E145613
RoHS compliant	Yes

Test Data	Standard applic.	Value
Mechanical properties		
Insertion/withdrawal force	IEC 603-7	max. 20 N
Mechanical operations	IEC 512-5, 9a	min. 1.000
Effectiveness of connector coupling device	IEC 512-8, 15f	50 N

Electrical properties	Standard applic.	Value
Creepage / clearance distances		
a) Contact - contact	IEC 807-3	0,52 mm
b) Contact - shell	IEC 807-3	min 1,0 mm

Voltage proof (Dielectric Withstand Voltage)		
a) Contact - contact	IEC 512-2, 4a	min. 1.000 V AC/DC
b) Contact - shell/testpanel	IEC 512-2, 4a	min. 1.500 V AC/DC
Current carrying capacity	IEC 512-3, 5b	1,5 A @ 25° C
Contact resistance	IEC 512-2, 2a	max. 30 mOhm
Insulation resistance	IEC 512-2, 3a	min. 500 MOhm

Environmental properties	Value
Operation temperature	0 - 70° C

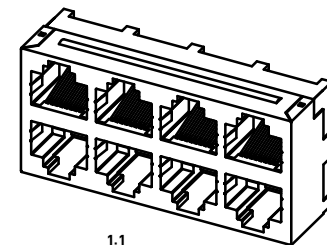
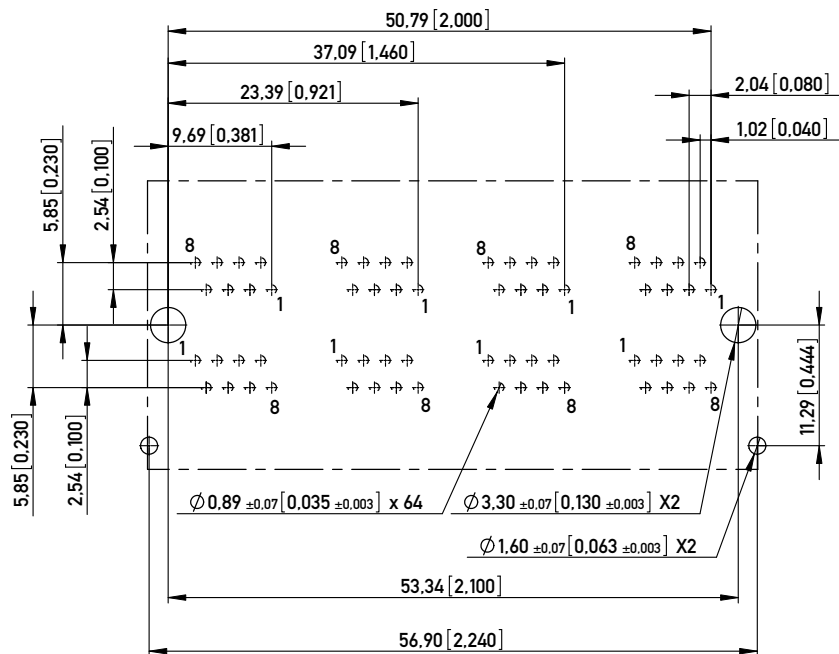
NOTE 1 : C-UL APPROVED E145613 AND MEETS FCC REQUIREMENTS

NOTE 2 : RoHS COMPLIANT



PART NO. IDENT. NR.		CONTACT FINISH OBERFLÄCHEN- BEHANDLUNG	
133190		Round wire contacts 0,8 µm Au [30 µin], over 1,27 [50 µin] Ni	
Information:		Tolerances	Scale 2:1
All rights reserved. Only for Information. To insure that this is the latest version of this drawing, please contact one of the ERNI companies before using.		 All Dimensions in mm (in)	Subject to modification without prior notice. Drawing will not be updated.
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C	18.09.2007	I (1/2)	
Index	Date	A3	
Class		MJ	

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RECOMMENDED PCB LAYOUT (COMPONENT SIDE VIEW)
 EMPFOHLENES LEITERPLATTEN-LAYOUT (BESTUECKUNGSSEITE)
 TOL. ±0.05 [0.002] UNLESS NOTED



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Information:	Tolerances	 All Dimensions in mm [in]	Scale	2:1
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