SPECIFICATIONS

Customer	利思泰
Product Name	Multi-layer Chip Ferrite Bead
Sunlord Part Number	GZ2012D471TF
Customer Part Number	

[New Released, Revised]

SPEC No.: GZ10170075

[This SPEC is total 9 pages including specifications and appendix.] [ROHS Compliant Parts]



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[For Customer approval Only] Date: Qualification Status: Full Restricted Rejected				
Approved By	Verified By	Re-checked By	Checked By	
Commenter				
Comments:				

[Version change history]

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
01	Jun.06, 2017	New release	1	Hai Guo

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2012 [0805]

1. Scope

This specification applies to GZ2012D471TF of multi-layer ferrite chip bead.

2. Product Description and Identification (Part Number)

- 1) Description:
 - GZ2012D471TF of multi-layer ferrite chip bead.
- 2) Product Identification (Part Number)





3	Material Code	
	D	

5	D Packing	
Т	Tape Carrier Package	

6	HSF Products	
Hazardous Substance Free Products		

④ Nomi	nal Impedance
Example	Nominal Value
471	470Ω
	•

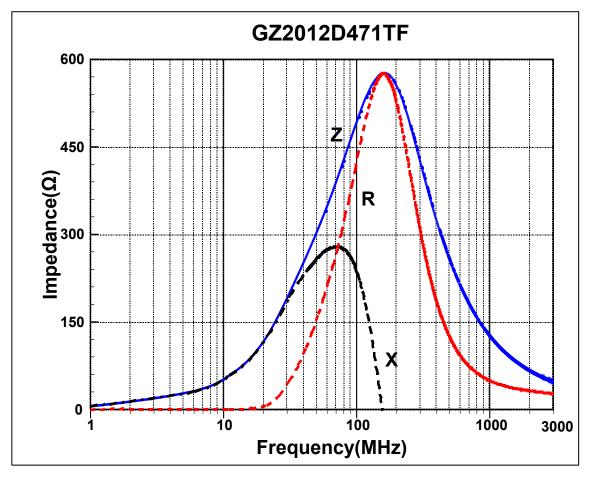
External Dimensions (L X W)[mm]

2.0 X 1.25

3. Electrical Characteristics

Part Number	Impedance (Ω)	Z Test Freq. (MHz)	DCR (Ω) Max.	Ir (mA) Max.
GZ2012D471TF	470±25%	100	0.30	500

Impedance Frequency Characteristics

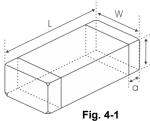


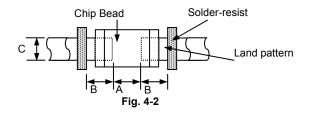
1) Operating and storage temperature range (individual chip without packing): -55 $^{\circ}$ C ~ +125 $^{\circ}$ C.

2) Storage temperature range (packaging conditions):-10 $^\circ\!\mathrm{C}$ ~+40 $^\circ\!\mathrm{C}$ and RH 70% (Max.)

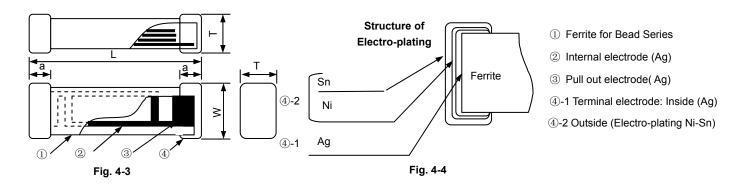
4. Shape and Dimensions

- 1) Dimensions and recommended PCB pattern for reflow soldering: See Fig.4-1, Fig.4-2 and Table 4-1.
- 2) Structure: See Fig. 4-3 and Fig. 4-4.





	[Table 4-1]			Unit: mm [i	nch]		
Туре	L	W	Т	а	А	В	С
2012 [0805]	2.0 (+0.3, -0.1) [0.079(+0.012,-0.004)]	1.25±0.2 [0.049±0.008]	0.85±0.2 [0.033±0.008]	0.5±0.3 [0.020±0.012]	0.80~1.20	0.80~1.20	0.90~1.60



3) Material Information: See Table 4-2.

[Table 4-2]				
Code	Part Name Material Nam			
1	Ferrite Body Ferrite Powder			
2	Inner Coils Silver Paste			
3	Pull-out Electrode (Ag)	Silver Paste		
④-1	Terminal Electrode: Inside Ag	Termination Silver Composition		
④-2	Electro-Plating: Ni/Sn plating	Plating Chemicals		

5. Test and Measurement Procedures

5.1 Test Conditions

- 5.1.1 Unless otherwise specified, the standard atmospheric conditions for measurement/test as:
 - a. Ambient Temperature: 20±15°C
 - b. Relative Humidity: 65±20%
 - c. Air Pressure: 86kPa to 106kPa
- 5.1.2 If any doubt on the results, measurements/tests should be made within the following limits:
 - a. Ambient Temperature: $20\pm2^{\circ}C$
 - b. Relative Humidity: 65±5%
 - c. Air Pressure: 86kPa to 106kPa

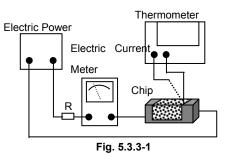
5.2 Visual Examination

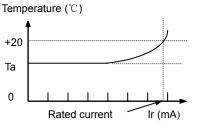
a. Inspection Equipment: 20× magnifier

5.3 Electrical Test

- 5.3.1 DC Resistance (DCR)
 - a. Refer to Item 3.
 - b. Test equipment (Analyzer): High Accuracy Milliohmmeter-HP4338B or equivalent.
- 5.3.2 Impedance (Z)
 - a. Refer to Item 3.
 - Test equipment: High Accuracy RF Impedance /Material Analyzer-E4991A or equivalent. Test fixture: HP16197A for 0603, HP16192A for 1005/1608/2012/3216. Test signal: -20dBm or 50mV
 - c. Test frequency refers to Item 3.

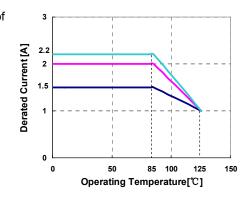
- 5.3.3 Rated Current
 - a. Refer to Item 3.
 - b. Test equipment (see Fig.5.3.3-1): Electric Power, Electric current meter, Thermometer.
 - c. Measurement method (see Fig. 5.3.3-1):
 - 1. Set test current to be 0 mA.
 - 2. Measure initial temperature of chip surface.
 - 3. Gradually increase voltage and measure chip temperature for corresponding current.
 - d. Definition of Rated Current (Ir): Ir is direct electric current as chip surface temperature rose just 20°C against chip initial surface temperature (Ta) (see Fig. 5.3.3-2)







e. When operating temperatures exceeding +85°C, derating of current is necessary for chip ferrite beads for which rated current is 1000mA over. Please apply the derating curve shown in chart according to the operating temperature.



5.4 Reliability Test

Items	Requirements	Test Methods and Remarks
5.4.1 Terminal Strength	No removal or split of the termination or other defects shall occur. Chip Chip Glass Epoxy Board Fig.5.4.1-1	 Solder the bead to the testing jig (glass epoxy board shown in Fig. 5.4.1-1) using leadfree solder. Then apply a force in the direction of the arrow. 10N force for 2012 series. Keep time: 10±1s. Speed: 1.0mm/s.
5.4.2 Resistance to Flexure	Type a b c 2012[0805] 1.2 4.0 1.65	 Solder the bead to the test jig (glass epoxy board shown in Fig. 5.4.2-1) Using a leadfree solder. Then apply a force in the direction shown Fig. 5.4.2-2. Flexure: 2mm. Pressurizing Speed: 0.5mm/sec. Keep time: 30 sec.
	b 0 0 0 0 0 0 0 0 0 0 0 0 0	²⁰ 10 Flexure 45[1.772] Fig. 5.4.2-2
5.4.3 Vibration	 No visible mechanical damage. Impedance change: within ±20% Cu pad Solder mask Cu pad Fig. 5.4.3-1 	 Solder the bead to the testing jig (glass epoxy board shown in Fig. 5.4.3-1) using leadfree solder. The bead shall be subjected to a simple harmonic motion having total amplitude of 1.5 mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3mutually perpendicular directions (total of 6 hours).
5.4.4 Dropping	 No visible mechanical damage. Impedance change: within ±20%. 	Drop chip bead 10 times on a concrete floor from a height of 100 cm.
5.4.5 Temperature	Impedance change should be within $\pm 20\%$ of initial value measuring at $20^\circ\!\mathrm{C}$.	Temperature range: -55℃ ~ 125℃. Reference temperature: +20℃.
5.4.6 Solderability	 No visible mechanical damage. Wetting shall exceed 75% coverage for 0603 series; exceed 95% for others 	 Solder temperture:240±2℃ Duration: 3 sec. Solder: Sn/3.0Ag/0.5Cu. Flux: 25% Resin and 75% ethanol in weight.
5.4.7 Resistance to Soldering Heat	 No visible mechanical damage. Wetting shall exceed 75% coverage for 0603 series; exceed 95% for others Impedance change: within ±20%. 	 Solder temperature :260±3°C Duration: 5 sec. Solder: Sn/3.0Ag/0.5Cu. Flux: 25% Resin and 75% ethanol in weight. The chip shall be stabilized at normal condition for 1~2 hours before measuring.

5.4.8	1 No mechanical damage.	① Temperature, Time: (See Fig.5.4.8-1)
Thermal Shock	 Impedance change: Within ±20%. 	-55°C for 30±3 min \rightarrow 125°C for 30±3min.
	125°C 30 min. 30 min. Ambient	 2 Transforming interval: Max. 20 sec. 3 Tested cycle: 100 cycles. 4 The chip shall be stabilized at normal condition for 1~2 hours before measuring.
5.4.9	① No mechanical damage.	① Temperature: -55±2°C
Resistance to	② Impedance change: within ±20%.	② Duration: 1000 ⁺²⁴ hours.
Low		③ The chip shall be stabilized at normal condition for 1~2 hours
Temperature		before measuring.
5.4.10	① No mechanical damage.	① Temperature: 125±2°C
Resistance to	② Impedance change: within ±20%.	② Duration: 1000 ⁺²⁴ hours.
High		③ The chip shall be stabilized at normal condition for 1~2 hours
Temperature		before measuring.
5.4.11	① No mechanical damage.	① Temperature: 60±2°C
Damp Heat	② Impedance change: Within ±20%.	② Humidity: 90% to 95% RH.
(Steady States)		③ Duration: 1000 ⁺²⁴ hours.
		④ The chip shall be stabilized at normal condition for 1~2 hours
		before measuring.
5.4.12	① No visible mechanical damage.	① Temperature: 60±2°C
Loading Under	② Impedance change: within ±20%.	② Humidity: 90% to 95% RH.
Damp Heat		③ Duration: 1000 ⁺²⁴ hours.
		④ Applied current: Rated current.
		5 The chip shall be stabilized at normal condition for 1~2 hours
		before measuring.
5.4.13	1 No visible mechanical damage.	① Temperature: 125±2℃
Loading at High	② Impedance change: within ±20%.	② Duration: 1000 ⁺²⁴ hours.
Temperature		③ Applied current: Rated current.
(Life Test)		④ The chip shall be stabilized at normal condition for 1~2 hours
		before measuring.

6. Packaging, Storage

6.1 Packaging

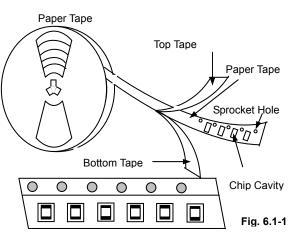
Tape Carrier Packaging:

Packaging code: T

- a. Tape carrier packaging are specified in attached figure Fig.6.1-1~3
- b. Tape carrier packaging quantity please see the following table:

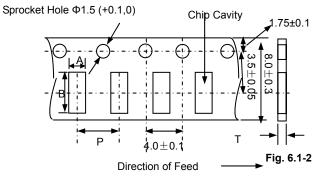
Туре	2012[0805]	
T(mm)	0.85±0.2	
Таре	Paper Tape	
Quantity	4K	

(1) Taping Drawings (Unit: mm)



Remark: The sprocket holes are to the right as the tape is pulled toward the user.

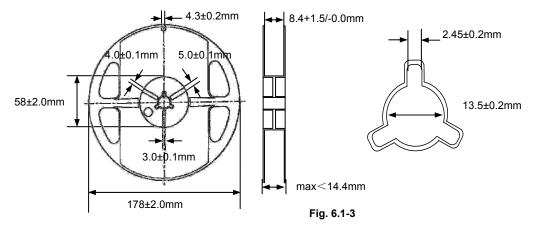
(2) Taping Dimensions (Unit: mm)



Paper Tape

Ī	Туре	А	В	Р	T max
ſ	2012[0805]	1.5±0.2	2.3±0.2	4.0±0.1	1.1

(3) Reel Dimensions (Unit: mm)



6.2 Storage

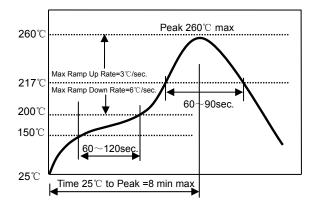
- a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to high humidity. Package must be stored at 40°C or less and 70% RH or less.
- The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H₂S).
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Solderability of the product s with external dimensions as 0603[0201] specified in Clause 5.4.6 shall be guaranteed for 6months from the date of delivery on condition that they are stored at the environment specified in Clause 3. For those parts, which passed more than 6 months shall be checked solder-ability before use.
- e. Solderability of the products, except ones with external dimensions as 0603[0201], specified in Clause 5.4.6 shall be guaranteed for 12 months from the date of delivery on condition that they are stored at the environment specified in Clause 3. For those parts, which passed more than 12 months shall be checked solder-ability before use.

7. Recommended Soldering Technologies

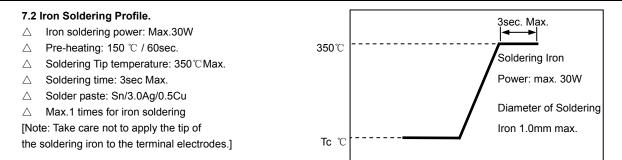
7.1 Re-flowing Profile:

- \triangle Preheat condition: 150 ~200 °C/60~120sec.
- \triangle Allowed time above 217°C: 60~90sec.
- △ Max temp: 260°C
- \triangle Max time at max temp: 10sec.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- \triangle Allowed Reflow time: 2x max

[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]



Sunlord



8. Supplier Information

- a) Supplier:
- Shenzhen Sunlord Electronics Co., Ltd.b) Manufacture:

Shenzhen Sunlord Electronics Co., Ltd.

c) Manufacturing Address:

Sunlord Industrial Park, Dafuyuan Industrial Zone, Guanlan, Shenzhen, China 518110