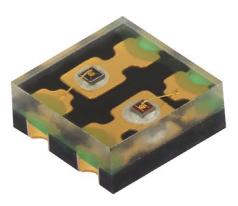
VSMD66694

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Vishay Semiconductors

Dual Color Emitting Diodes, 660 nm and 940 nm



VSMD66694 is a dual color emitting device with 660 nm and

940 nm peak wavelength. The emitters are based on the SurfLightTM technology, providing high radiant power.

FEATURES

- Package type: surface mount
- Package form: square PCB
- Dimensions (L x W x H in mm): 2 x 2 x 0.87
- Peak wavelength: $\lambda_p = 660 \text{ nm}$ and 940 nm
- High reliability
- High radiant power
- Angle of half intensity: $\varphi = \pm 60^{\circ}$
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- Wearables
- · Health monitoring
- Pulse oximetry

PRODUCT SUMMARY COMPONENT COLOR I_e (mW/sr) φ (deg) λ_p (nm) t_r (ns) Red 2.3 660 VSMD66694 ± 60 10 940 IR 1.5

Note

DESCRIPTION

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION

ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VSMD66694	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	square PCB		

Note

• MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	COLOR	VALUE	UNIT		
Reverse voltage		V _R		5	V		
Forward current			Red	70	mA		
		I _F	IR	70			
Peak forward current	$t_p/T = 0.1, t_p = 100 \ \mu s$		Red	140	mA		
Peak forward current		IFM	IR	140			
Current formulated outworkt	t _p = 100 μs		Red	1	А		
Surge forward current		I _{FSM}	IR	1			
Dewer dissinction		Р	Red	161	mW		
Power dissipation		Pv	IR	119			
Junction temperature		Тj		100	°C		
Operating temperature range		T _{amb}		-25 to +85	°C		
Storage temperature range		T _{stg}		-25 to +85	°C		
Soldering temperature	According fig. 10, J-STD-020	T _{sd}		260	°C		
Thermal resistance junction / ambient	J-STD-051	R _{thJA}		390	K/W		

Rev. 1.0, 12-Apr-16

1 For technical questions, contact: <u>emittertechsupport@vishay.com</u> Document Number: 84324

Pb-free



HALOGEN

FREE <u>GREEN</u> (5-2008)

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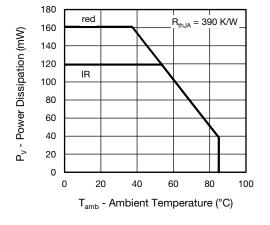


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

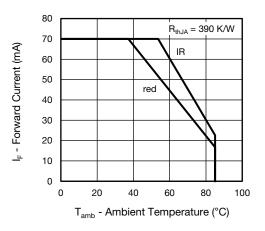
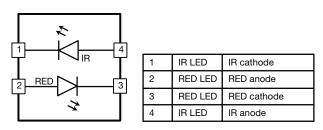


Fig. 2 - Forward Current Limit vs. Ambient Temperature

PARAMETER	TEST CONDITION	SYMBOL	COLOR	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_{\rm F} = 20$ mA, $t_{\rm p} = 20$ ms	V _F	Red	-	2.0	2.3	- v
			IR	-	1.4	1.7	
Temperature coefficient	I _F = 20 mA	TK _{VF}	Red	-	-2.3	-	mV/K
			IR	-	-2.3	-	
Reverse current		I _R	not designed for reverse operation			μA	
Junction capacitance	$V_R = 0 V$, f = 1 MHz, E = 0 mW/cm ²	CJ	Red	-	7	-	- pF
			IR	-	5	-	
Radiant intensity	I _F = 20 mA	1	Red	1.9	2.3	-	mW/sr
		l _e	IR	0.8	1.5	-	
Radiant power	I _F = 20 mA	фе	Red	-	9.5	-	- mW
			IR	-	8.5	-	
Angle of half intensity	I _F = 20 mA	φ		-	± 60	-	deg
Peak wavelength	I _F = 20 mA	λ _p	Red	650	660	670	- nm
			IR	920	940	960	
Spectral bandwidth	I _F = 20 mA	Δλ	Red	-	20	-	nm
			IR	-	40	-	
Temperature coefficient of λ_p	I _F = 20 mA	$TK_{\lambda p}$	Red	-	0.2	-	nm/K
			IR	-	0.3	-	
Rise time	I _F = 20 mA	t _r	Red	-	10	-	ns
			IR	-	10	-	
Fall time	I _F = 20 mA	t _f	Red	-	10	-	ns
			IR	-	10	-	

CIRCUIT BLOCK DIAGRAM



Rev. 1.0, 12-Apr-16

2

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BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

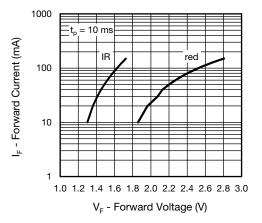


Fig. 3 - Forward Current vs. Forward Voltage

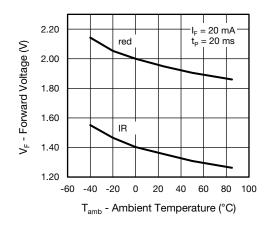


Fig. 4 - Forward Voltage vs. Ambient Temperature

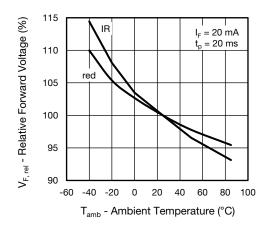


Fig. 5 - Relative Forward Voltage vs. Ambient Temperature

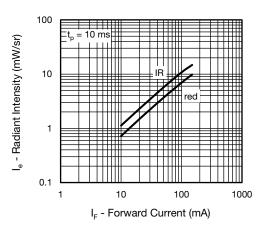


Fig. 6 - Radiant Intensity vs. Forward Current

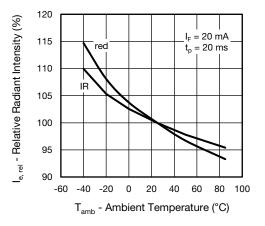


Fig. 7 - Relative Radiant Intensity vs. Ambient Temperature

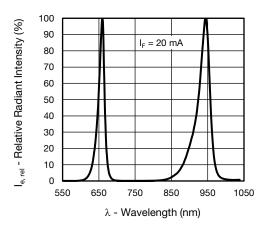


Fig. 8 - Relative Radiant Intensity vs. Wavelength

Rev. 1.0, 12-Apr-16

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DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020:

Moisture sensitivity: level 3

Floor life: 168 h

Conditions: $T_{amb} < 30$ °C, RH < 60 %

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



0

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0°

10°

20°

30°

40°

50°

60'

70'

80°

φ - Angular Displacement

REFLOW SOLDER PROFILE

0.6

0.4 0.2

l_{e, rel} - Relative Radiant Intensity

1.0

0.9

0.8

0.7

948013-1

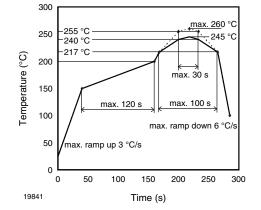
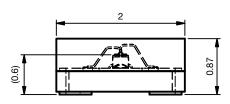
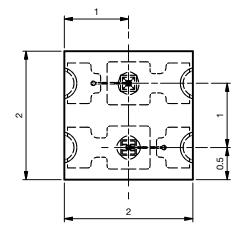


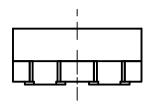
Fig. 10 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020



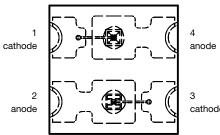
PACKAGE DIMENSIONS in millimeters











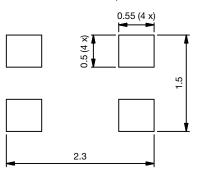


Technical drawings according to DIN specification 0.63 (4 x)

0.5 (4 x)

0.05 (4 x)

Recommended Footprint



Drawing No.: 6.550-5347.01-4 Issue: 1; 19.02.16

1 (2 ×)

Pin 1 marking

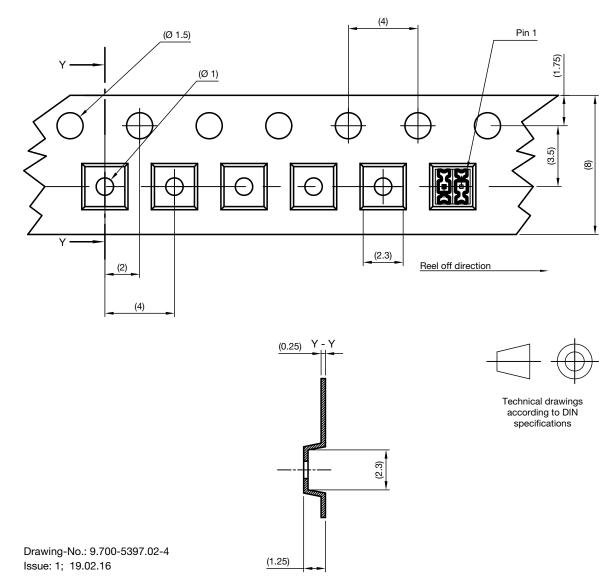
Not indicated tolerances ± 0.1

5





TAPE DIMENSIONS in millimeters

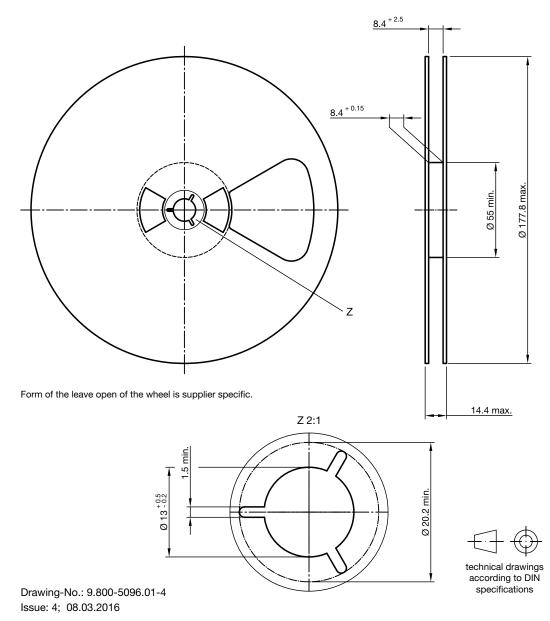


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REEL DIMENSIONS in millimeters



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