TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC4S66F, TC4S66FU

BILATERAL SWITCH

TC4S66F/FU contains one circuit of bidirectional switches. When control input, CONT is set to "H" level, the impedance between input and output of the switch becomes low and when it is set to "L" level, the switch becomes high. This can be applied for switching of analog signals and digital signals.

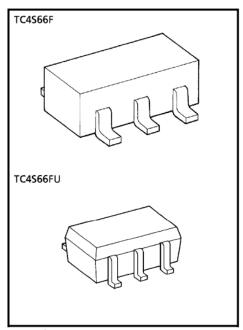
FEATURES

ON-resistance (R_{ON})

300 Ω (Typ.) $V_{DD} - V_{SS} = 5 V$ 110 Ω (Typ.) $V_{DD} - V_{SS} = 10 \text{ V}$ 70 Ω (Typ.) $V_{DD} - V_{SS} = 15 V$

OFF-resistance (ROFF)

 R_{OFF} (Typ.) > $10^9 \Omega$



Weight SSOP5-P-0.95 : 0.016 g (Typ.) SSOP5-P-0.65A : 0.006 g (Typ.)

MAXIMUM RATINGS

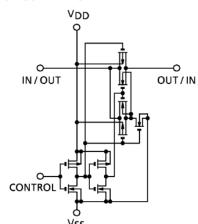
CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V_{DD}	V _{SS} - 0.5~V _{SS} + 20	V
Control Input Voltage	V _C IN	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	٧
Switch I/O Voltage	V _{I/O}	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	٧
Power Dissipation	PD	200	mW
Potential difference across I/O during ON	V _I -V _O	± 0.5	٧
Control Input Current	IC IN	± 10	mA
Operating Temperature Range	T _{opr}	- 40~85	°C
Storage Temperature	T _{stg}	-65~150	°C
Lead Temperature (10 s)	TL	260	°C

TRUTH TABLE

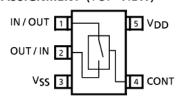
CONTROL	IMPEDANCE BETWEEN IN/OUT-OUT/IN *
Н	$0.5\sim5\times10^{2}\Omega$
Ĺ	> 10 ⁹ Ω

* : See static electrical characteristics.

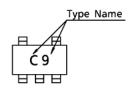
CIRCUIT DIAGRAM



PIN ASSIGNMENT (TOP VIEW)



MARKING



RECOMMENDED OPERATING CONDITIONS ($V_{SS} = 0 V$)

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	V_{DD}	-	3		18	٧
Input/Output Voltage	V _{IN} /V _{OUT}	_	0		V_{DD}	V

STATIC ELECTRICAL CHARACTERISTICS (In case not specifically appointed, $V_{SS} = 0 \text{ V}$)

CVNA					000	2E°C			85°C			
CHARACTERISTIC		SYM-	TEST CONDITION	V_{DD}	– 40°C		25°C			8:	UNIT	
		BOL		(V)	MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	01111
Control In	مامانا بسم				3.5	-	3.5	2.75	_	3.5	_	
Control In	iput nign	V_{IH}	$ I_{IS} = 10 \mu A$	10	7.0	—	7.0	5.50	_	7.0	—	
Voltage				15	11.0	—	11.0	8.25	_	11.0	_	v
Control In	nut Low		I _{IS} = 10 μΑ	5	-	1.5		2.25	1.5	_	1.5	\ \
Voltage	put Low	V_{IL}		10	—	3.0	_	4.5	3.0	—	3.0	
Voltage				15	_	4.0	-	6.75	4.0	_	4.0	
			$0 \le V_{IS} \le V_{DD}$	5	_	800		290	950	_	1200	
On-State I	Resistance	RON	$R_{I} = 10 \text{k}\Omega$	10	—	210	_	120	250	—	300	Ω
			WE = 10 K77	15	_	140	_	85	160	_	200	
Input/Output Leakage Current			V _{IN} = 18 V V _{OUT} = 0 V	18	_	± 100	1	± 0.1	± 100	_	± 1000	
		OFF	V _{IN} = 0 V V _{OUT} = 18 V	18	_	± 100		± 0.1	± 100	_	± 1000	nA
Quiescent	Outesant Davisa			5	_	0.25		0.001	0.25	_	7.5	
Quiescent Device Current		$ V_{DD} = V_{DD}, V_{SS}$	10	—	0.5	_	0.001	0.5	 	15	μΑ	
				15	_	1.0		0.002	1.0	_	30	
Input	H Level	ΊΗ	V _{IH} = 18 V	18	_	0.1	_	10-5	0.1	_	1.0	
Current	L Level	lOL	V _{IL} = 0 V	18	_	- 0.1	_	- 10 ^{- 5}	- 0.1	_	- 1.0	μΑ

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DYNAMIC ELECTRICAL CHARACTERISTICS (Ta = 25°C)

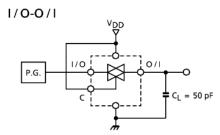
CHARACTERISTIC	SYMBOL	TEST CONDITION	V _{SS} (V)	V _{DD} (V)	MIN.	TYP.	MAX.	UNIT
Propagation Delay Time (IN-OUT)	t _{pLH} t _{pHL}	C _L = 50 pF	0 0 0	5 10 15	_ _ _	15 8 5	40 20 15	
Propagation Delay Time (CONTROL-OUT)	t _{pZL} t _{pZH}	$R_{L} = 1 k\Omega$ $C_{L} = 50 pF$	0 0 0	5 10 15	_ _ _	55 25 20	120 40 30	ns
Propagation Delay Time (CONTROL-OUT)	t _{pLZ} t _{pHZ}	$R_{L} = 1 k\Omega$ $C_{L} = 50 pF$	0 0 0	5 10 15	_ _ _	45 30 25	80 70 60	
Max. Control Input Repetition Rate	fMAX (C)	$R_{L} = 1 k\Omega$ $C_{L} = 50 pF$	0 0 0	5 10 15	_ _ _	10 12 12		MHz
- 3dB Cut Off Frequency	fMAX (I-O)	$R_L = 1 k\Omega$ $C_L = 50 pF (*1)$	- 5	5	_	30	1	
Total Harmonic Distortion	_	$R_{L} = 10 \text{ k}\Omega$ $f = 1 \text{ kHz} \qquad (*2)$	- 5	5	_	0.03	-	%
– 50dB Feedthrough Frequency	_	$R_L = 1 k\Omega$ (*3)	- 5	5	_	600		kHz
Crosstalk (CONTROL-OUT)	_	$R_{IN} = 1 k\Omega$ $R_{OUT} = 10 k\Omega$ $C_L = 15 pF$	0 0 0	5 10 15		200 400 600		mV
Input Capacitance	CIN	Control Input Switch I/O			_	5 10	7.5 —	n.E
Feedthrough Capacitance	C _{IN-OUT}	_			_	0.5		pF

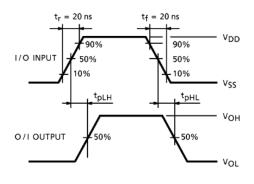
^{*1 :} The frequency at $20log_{10} \frac{V_{OS}}{V_{IS}} = -3 \, dB$ shall be $f_{MAX}(I/O)$ using sine wave of $\pm 2.5 \, V_{p-p}$ for V_{IS} .

*2 : V_{IS} shall be sine wave of $\pm 2.5 \, V$.

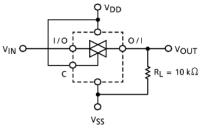
^{*3 :} The frequency at $20\ell og_{10} \frac{V_{OS}}{V_{IS}}$ = 50 dB shall be the feed through using of $\pm 2.5 \, V_{p-p}$.

1. tpLH, tpHL



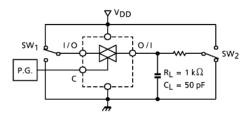


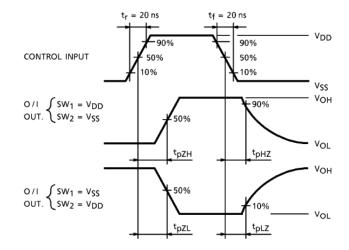
3. RON



2. t_{pZL}, t_{pZH}, t_{pLZ}, t_{pHZ}

CONTROL-O/I

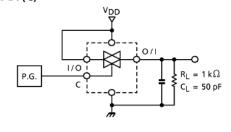


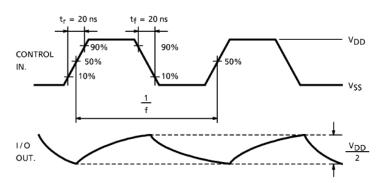


$$\mathsf{R}_{\mathsf{ON}} = \mathsf{10} \times \frac{\langle \mathsf{V}_{\mathsf{IN}} - \mathsf{V}_{\mathsf{OUT}} \rangle}{\mathsf{V}_{\mathsf{OUT}}} (\mathsf{k}\Omega)$$

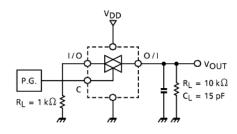
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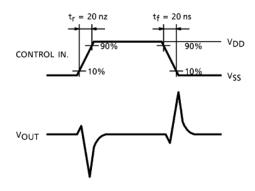
4. fMAX (C)



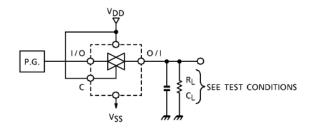


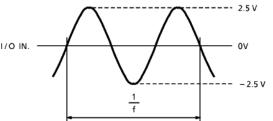
5. CROSSTALK (CONTROL INPUT)





6. TOTAL HARMONIC DISTORTION, $f_{\mbox{MAX}}$ (I/O-O/I), FEEDTHROUGH (SWITCH OFF)



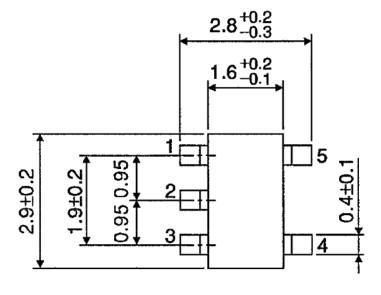


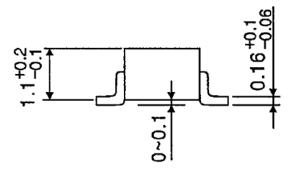
5

PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm





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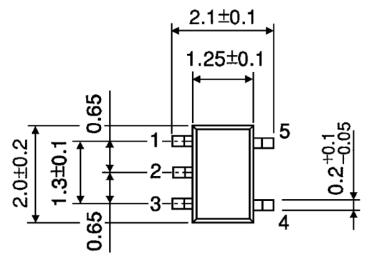
Weight: 0.016 g (Typ.)

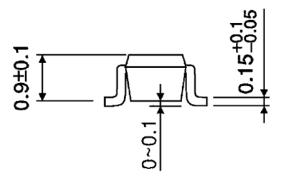
2008-06-03

Unit: mm

PACKAGE DIMENSIONS

SSOP5-P-0.65A





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Weight: 0.006 g (Typ.)

2008-06-03

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20070701-EN GENERAL

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