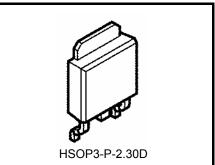
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA58L05F, TA58L06F, TA58L08F, TA58L09F TA58L10F, TA58L12F, TA58L15F

250 mA Low Dropout Voltage Regulator

The TA58L**F Series consists of fixed-positive-output, low-dropout regulators with an output current of 250 mA (max) that utilize PNP transistors for the output stage. Low dropout voltage and standby current make the TA58L**F Series suitable for applications requiring low power consumption.



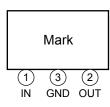
Features

- Maximum output current ÷ 250 mA
- Output voltage
- Output voltage accuracy
- : $V_{OUT} \pm 3\%$ (@T₁ = 25°C)

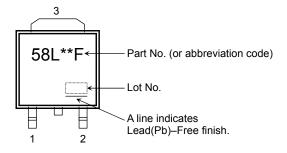
: 5/ 6/ 8/ 9/ 10/ 12/ 15 V

- Low-dropout voltage
- Protection function
- : $VOUT \pm 3\%$ (@1j = 25°C) : 0.4 V (Max) (@IOUT = 200 mA)
- : Overcurrent protection / overheating protection /
- Reverse connection of power supply / 60 V load dump : Surface-mount New PW-Mold
- Pin Assignment

Package type



Marking



Note 1: The "**" in each product name is replaced with the output voltage of each product.

The product(s) in this document ("Product") contain functions intended to protect the Product from temporary small overloads such as minor short-term overcurrent, overvoltage, or overheating. The protective functions do not necessarily protect Product under all circumstances. When incorporating Product into your system, please design the system (1) to avoid such overloads upon the Product, and (2) to shut down or otherwise relieve the Product of such overload conditions immediately upon occurrence. For details, please refer to the notes appearing below in this document and other documents referenced in this document.

Weight : 0.36 g (Typ.)

Pin Description

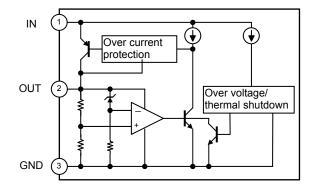
Pin No.	Symbol	Description
1	IN	Input terminal. Connected by capacitor (CIN) to GND.
3	GND	Ground terminal
2	OUT	Output terminal. Connected by capacitor (C _{OUT}) to GND.

How to Order

Product No.		Package	Package Type and Capacity
TA58L**F(TE16L1,NQ	(Note2)	New PW-Mold: Surface-mount	Tape (2000 pcs/reel)

Note 2: The "**" in each product number is replaced with the output voltage of each product.

Block Diagram



Absolute Maximum Rating (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
Input voltage	DC	V _{IN (DC)}	29	V
input voltage	Pulse	V _{IN (Pulse)}	60(τ=200ms)	V
Output current	Output current		250	mA
Operating tempera	ture	T _{opr}	-40 to 105	°C
Junction temperatu	ıre	Tj	150	°C
Storage temperatu	re	T _{stg} –55 to 150		°C
Power dissipation	Ta = 25°C	Po	1	W
	Tc= 25°C	PD	10	vv

Note 3: Do not apply current and voltage (including reverse polarity) to any pin that is not specified.

Note 4: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, junction to ambient	R _{th (j−a)}	125	°C/W
Thermal resistance, junction to case	R _{th (j−c)}	12.5	°C/W

Protection Function (Reference)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Thermal shutdown	T _{SD}	V _{IN} = 14 V (05 to 06F)/ 16 V (08 to 10F)/ 18 V (12F)/ 20 V (15F)		170	_	°C
Peak circuit current	I PEAK		_	600	_	mA
Short circuit current	I _{SC}		_	330	_	mA
Overvoltage protection	V _{IN}	$T_j = 25^{\circ}C$	29	33	_	V

Note 5: Ensure that the devices operate within the limits of the maximum rating when in actual use.

Note 6: When the input voltage exceeds 29 V, the overvoltage protection circuit is activated to turn off the output voltage.

TA58L05F Electrical Characteristics (unless otherwise specified, T_j = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{IN} = 14 V, I _{OUT} = 10 mA	4.85	5.00	5.15	
	V _{OUT}	5.35 V \leq V $_{IN}$ \leq 26 V, I_{OUT} = 10 mA, -40°C \leq Ta \leq 105°C	4.8	5.0	5.2	V
	Deciline	9 V \leq V _{IN} \leq 16 V, I _{OUT} = 10 mA		1	10	mV
Line regulation	Reg∙line	5.35 V \leq V _{IN} \leq 26 V, I _{OUT} = 10 mA	_	2	15	IIIV
Load regulation	Reg·load	$V_{IN} = 14~V,~10~mA \leq I_{OUT} \leq 250~mA$	_	10	30	mV
Quiescent current	Ι _Β	$6 \text{ V} \leq V_{IN} \leq 26 \text{ V}, \text{ I}_{OUT} = 0 \text{ A}$	_	0.45	1.00	
		$6 \text{ V} \leq \text{V}_{IN} \leq 26 \text{ V}, \text{ I}_{OUT} = 250 \text{ mA}$	_	25	50	mA
Description	\/-	I _{OUT} = 50 mA		0.08	0.20	v
Dropout voltage	VD	I _{OUT} = 200 mA	—	0.22	0.40	

TA58L06F Electrical Characteristics (unless otherwise specified, T_j = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{IN} = 14 V, I _{OUT} = 10 mA	5.82	6.00	6.18	
	V _{OUT}	$\begin{array}{l} 6.35 \text{ V} \leq \text{V}_{IN} \leq 26 \text{ V}, \text{ I}_{OUT} = 10 \text{ mA}, \\ -40^{\circ}\text{C} \leq \text{Ta} \leq 105^{\circ}\text{C} \end{array}$	5.76	6.00	6.24	V
	Dogulino	10 V \leq V _{IN} \leq 17 V, I _{OUT} = 10 mA	_	1	10	mV
Line regulation	Reg∙line	6.35 V \leq V _{IN} \leq 26 V, I _{OUT} = 10 mA	_	2	15	IIIV
Load regulation	Reg·load	$V_{IN} = 14~V,~10~mA \leq I_{OUT} \leq 250~mA$	_	10	30	mV
	1-	7 V \leq V _{IN} \leq 26 V, I _{OUT} = 0 A	_	0.5	1.0	mA
Quiescent current	Ι _Β	7 V \leq V _{IN} \leq 26 V, I _{OUT} = 250 mA	_	25	50	ma
Description	\/-	I _{OUT} = 50 mA		0.08	0.20	V
Dropout voltage	VD	I _{OUT} = 200 mA	_	0.22	0.40	V

TA58L08F Electrical Characteristics (unless otherwise specified, T_j = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{IN} = 16 V, I _{OUT} = 10mA	7.76	8.00	8.24	
	V _{OUT}	8.35 V \leq VIN \leq 26 V, I_OUT = 10 mA, -40°C \leq Ta \leq 105°C	7.68	8.00	8.32	V
Line regulation	Reg·line	$12~V \leq V_{IN} \leq 19~V,~I_{OUT}$ = 10 mA	_	1	10	mV
	Reguline	8.35 V \leq V _{IN} \leq 26 V, I _{OUT} = 10 mA	_	2	15	IIIV
Load regulation	Reg·load	V_{IN} = 16 V, 10 mA $\leq I_{OUT} \leq$ 250 mA	_	10	40	mV
	1-	9 V \leq V _{IN} \leq 26 V, I _{OUT} = 0 A	_	0.55	1.00	mA
Quiescent current	Ι _Β	9 V \leq V _{IN} \leq 26 V, I _{OUT} = 250 mA	_	25	50	ШA
Dreneutueltere	\/-	I _{OUT} = 50 mA	_	0.08	0.20	v
Dropout voltage	VD	I _{OUT} = 200 mA	_	0.22	0.40	

TA58L09F Electrical Characteristics (unless otherwise specified, T_j = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V _{IN} = 16 V, I _{OUT} = 10 mA	8.73	9.00	9.27	
Output voltage	Vout	9.35 V \leq V $_{IN}$ \leq 26 V, I $_{OUT}$ = 10 mA, -40°C \leq Ta \leq 105°C	8.64	9.00	9.36	V
Line of the first	Reg·line	$13~V \leq V_{IN} \leq 20~V,~I_{OUT} = 10~mA$	_	1	12	mV
Line regulation	Reguline	9.35 V \leq V _{IN} \leq 26 V, I _{OUT} = 10 mA	_	2	20	IIIV
Load regulation	Reg·load	V_{IN} = 16 V, 10 mA $\leq I_{OUT}$ \leq 250 mA	_	12	40	mV
Quiessant surrant	1_	$10~V \leq V_{IN} \leq 26~V,~I_{OUT} = 0~A$	_	0.6	1.0	m 4
Quiescent current	Ι _Β	$10~V \leq V_{IN} \leq 26~V,~I_{OUT} = 250~mA$	_	25	50	mA
Deservationality	\/-	I _{OUT} = 50 mA		0.08	0.20	V
Dropout voltage	VD	I _{OUT} = 200 mA		0.22	0.40	V

TA58L10F Electrical Characteristics (unless otherwise specified, T_j = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V _{IN} = 16 V, I _{OUT} = 10 mA	9.7	10.0	10.3	
Output voltage	V _{OUT}	10.35 V \leq V _{IN} \leq 26 V, I _{OUT} = 10 mA, -40°C \leq Ta \leq 105°C	9.6	10.0	10.4	V
	Deciline	$14~V \leq V_{IN} \leq 21~V,~I_{OUT} = 10~mA$	_	1	12	m) (
Line regulation	Reg∙line	$10.35 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 10 \text{ mA}$	_	2	20	mV
Load regulation	Reg∙load	V_{IN} = 16 V, 10 mA $\leq I_{OUT} \leq$ 250 mA	_	12	40	mV
Quiescent current	1-	$11~V \leq V_{IN} \leq 26~V,~I_{OUT} = 0~A$	_	0.6	1.2	m 4
Quiescent current	Ι _Β	$11~V \leq V_{IN} \leq 26~V,~I_{OUT} = 250~mA$	_	25	50	mA
Dropout voltage	\/_	I _{OUT} = 50 mA		0.08	0.20	V
	VD	I _{OUT} = 200 mA		0.22	0.40	V

TA58L12F Electrical Characteristics (unless otherwise specified, T_j = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V _{IN} = 18 V, I _{OUT} = 10 mA	11.64	12.00	12.36	
Output voltage	V _{OUT}	12.35 V \leq V $_{IN}$ \leq 26 V, I_{OUT} = 10 mA, -40°C \leq Ta \leq 105°C	11.52	12.00	12.48	V
Line of the first	Reg·line	16 V \leq V $_{IN}$ \leq 23 V, I $_{OUT}$ = 10 mA		1	12	mV
Line regulation		$12.35 \text{ V} \le V_{IN} \le 26 \text{ V}, I_{OUT} = 10 \text{ mA}$	_	2	20	IIIV
Load regulation	Reg·load	V_{IN} = 18 V, 10 mA $\leq I_{OUT}$ \leq 250 mA	_	20	50	mV
	1-	$13~V \leq V_{IN} \leq 26~V,~I_{OUT} = 0~A$	_	0.65	1.20	mA
Quiescent current	Ι _Β	$13~V \leq V_{IN} \leq 26~V,~I_{OUT} = 250~mA$	_	25	50	ША
Description	\/-	I _{OUT} = 50 mA		0.08	0.20	V
Dropout voltage	VD	I _{OUT} = 200 mA	_	0.22	0.40	V

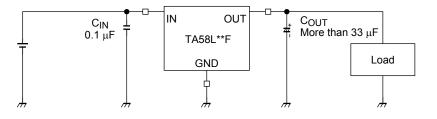
TA58L15F Electrical Characteristics (unless otherwise specified, $T_j = 25^{\circ}C$)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
		V _{IN} = 20 V, I _{OUT} = 10 mA	14.55	15.00	15.45	
Output voltage	V _{OUT}	15.35 V \leq V _{IN} \leq 26 V, I _{OUT} = 10 mA, -40°C \leq Ta \leq 105°C	14.4	15.0	15.6	V
	Deguline	19 V \leq V $_{IN}$ \leq 26 V, I_{OUT} = 10 mA	_	1	12	mV
Line regulation	Reg∙line	$15.35 \text{ V} \le \text{V}_{IN} \le 26 \text{ V}, \text{ I}_{OUT} = 10 \text{ mA}$	_	2	20	mv
Load regulation	Reg·load	$V_{IN} = 20 \text{ V}, 10 \text{ mA} \le I_{OUT} \le 250 \text{ mA}$	_	20	60	mV
Quieseent current	1_	16 V \leq V $_{IN}$ \leq 26 V, I_{OUT} = 0 A	_	0.75	1.40	m 1
Quiescent current	Ι _Β	$16~V \leq V_{IN} \leq 26~V,~I_{OUT} = 250~mA$	_	25	50	mA
	<i>\\</i> _	I _{OUT} = 50 mA		0.08	0.20	V
Dropout voltage	VD	I _{OUT} = 200 mA	_	0.22	0.40	V

Electrical Characteristics Common to All Products

• Tj = 25 °C in the measurement conditions of each item is a regulation for where the standard condition when a pulse test is carried out, and any drift in the electrical characteristic due to a rise in the junction temperature of the chip may be disregarded.

Standard Application Circuit



• Place C_{IN} as close as possible to the input terminal and GND. Place C_{OUT} as close as possible to the output terminal and GND. Although capacitor C_{OUT} acts to smooth the dc output voltage during suspension of output oscillation or load change, it might cause output oscillation in a cold environment due to increased capacitor ESR. It is therefore recommended to use a capacitor with small variations temperature sensitivity. The IC may oscillate due to external conditions (output current, temperature, or the type of the capacitor used). The type of capacitor required must be determined by the actual application circuit in which the IC is used.

Usage Precautions

• Low voltage

Do not apply voltage to the Product that is lower than the minimum operating voltage, or the Product's protective functions will not operate properly and the Product may be permanently damaged.

Overcurrent Protection

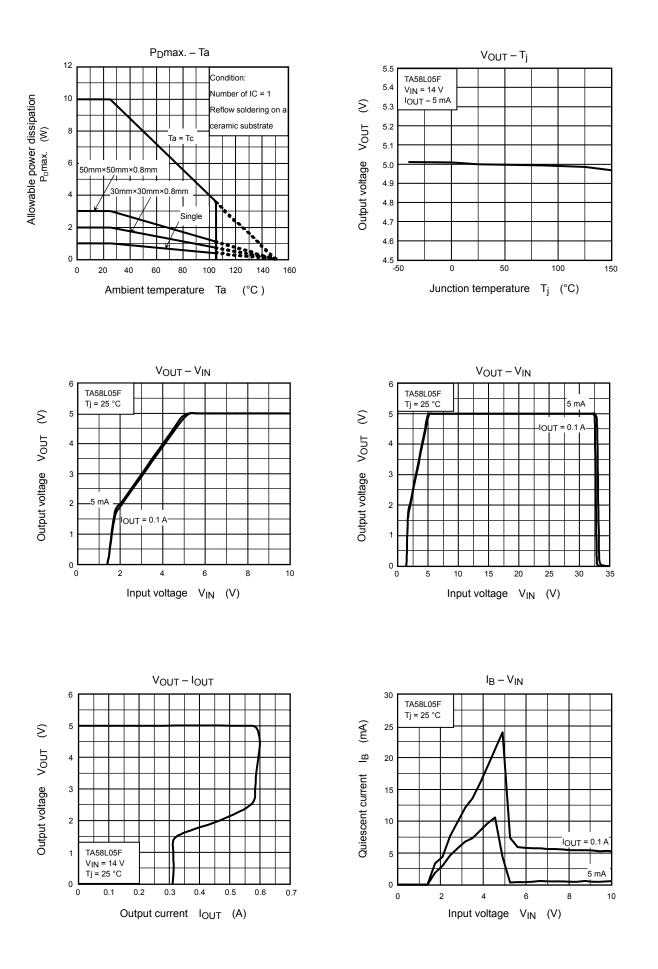
The overcurrent protection circuits in the Product are designed to temporarily protect Product from minor overcurrent of brief duration. When the overcurrent protective function in the Product activates, immediately cease application of overcurrent to Product. Improper usage of Product, such as application of current to Product exceeding the absolute maximum ratings, could cause the overcurrent protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

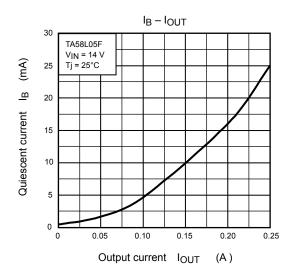
• Overheating Protection

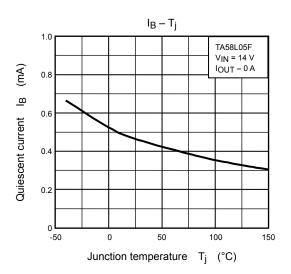
The thermal shutdown circuits in the Product are designed to temporarily protect Product from minor overheating of brief duration. When the overheating protective function in the Product activates, immediately correct the overheating situation. Improper usage of Product, such as the application of heat to Product exceeding the absolute maximum ratings, could cause the overheating protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

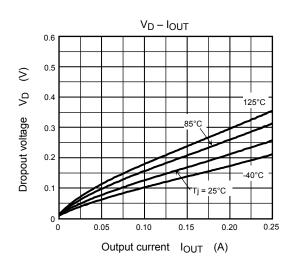
Overvoltage Protection

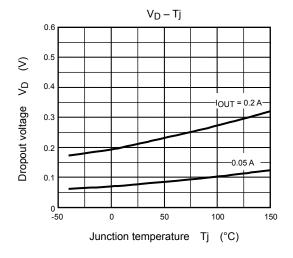
The overvoltage protection circuits in the Product are designed to temporarily protect Product from minor overvoltage of brief duration. When the overvoltage protective function in the Product activates, immediately cease application of overvoltage to Product. Improper usage of Product, such as application of voltage to Product exceeding the absolute maximum ratings, could cause the overvoltage protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

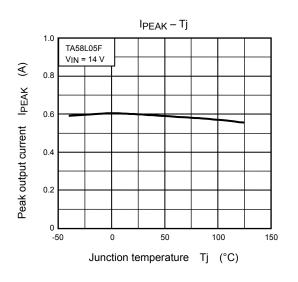


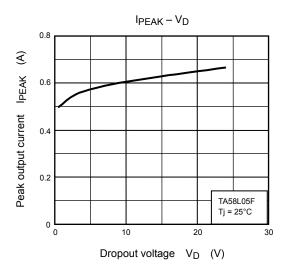










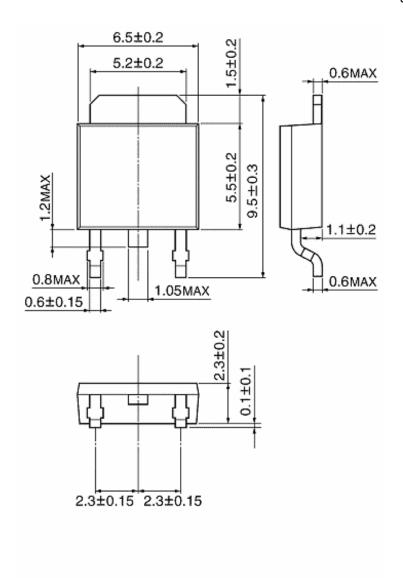


TOSHIBA

Package Dimensions

HSOP-3-P-2.30D

Unit: mm



Weight: 0.36 g (Typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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