



60V INPUT, 5V 15mA REGULATOR TRANSISTOR

Description

The ZXTR2105F monolithically integrates a transistor, zener diode and resistor to function as a linear regulator. The device regulates with a 5V nominal output at 15mA. It is designed for use in high-voltage applications where standard linear regulators cannot be used. This function is fully integrated into a SOT23 package, minimizing PCB area and reducing the number of components when compared with a multi-chip discrete solution.

Applications

Supply voltage regulation for:

- 12V to 5V Rails
- 24V to 5V Rails
- Other Customized Input Rails

Features

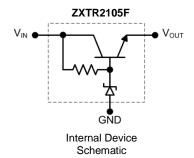
- Series Linear Regulator Using Emitter-Follower Stage
- Input Voltage = 7V to 60V (For regulated output Voltage)
- Output Voltage = 5V ± 5%
- Fully Integrated into a SOT23 Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (ZXTR2105FQ)

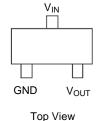
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic "Green" Molding Compound;
 UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)



Top View





Pin-Out

Pin Name	Pin Function
V_{IN}	Input Supply
GND	Power Ground
V _{OUT}	Voltage Output

May 2017

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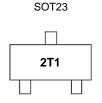
Ordering Information (Note 5)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXTR2105F-7	AEC-Q101	2T1	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.htmlfor more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



2T1 = Product Type Marking Code



Absolute Maximum Ratings (Voltage relative to GND, @TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Input Voltage	V _{IN}	-0.3 to 60	V
Continuous Input and Output Current	I _{IN,} I _{OUT}	320	mA
Peak Pulsed Input and Output Current	I _{IM} , I _{OM}	2	Α
Maximum Voltage Applied to V _{OUT}	V _{OUT(MAX)}	Smaller of V _{IN} +5V or 10V	V

Maximum Current at V_{IN} = 12V (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Continuous Output Current	(Note 7)	I _{OUT}	89	mA	
Pulsed Output Current	(Note 8)		2,000	mΛ	
Pulsed Output Current	(Note 9)	ІОМ	890	mA mA	

Thermal Characteristics

Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 5)	В	625	mW	
Power Dissipation	(Note 6)	P _D	500	ITIVV	
Thermal Resistance, Junction to Ambient	(Note 5)	0	200		
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	250		
Thermal Resistance, Junction to Lead (Note 10)		$R_{ hetaJL}$	197	°C/W	
Thermal Resistance, Junction to Case	(Note 10)	$R_{ heta JC}$	17		
Maximum Operating Junction and Storage Temperature Range		$T_{J_1}T_{STG}$	-65 to +150	°C	

ESD Ratings (Note 11)

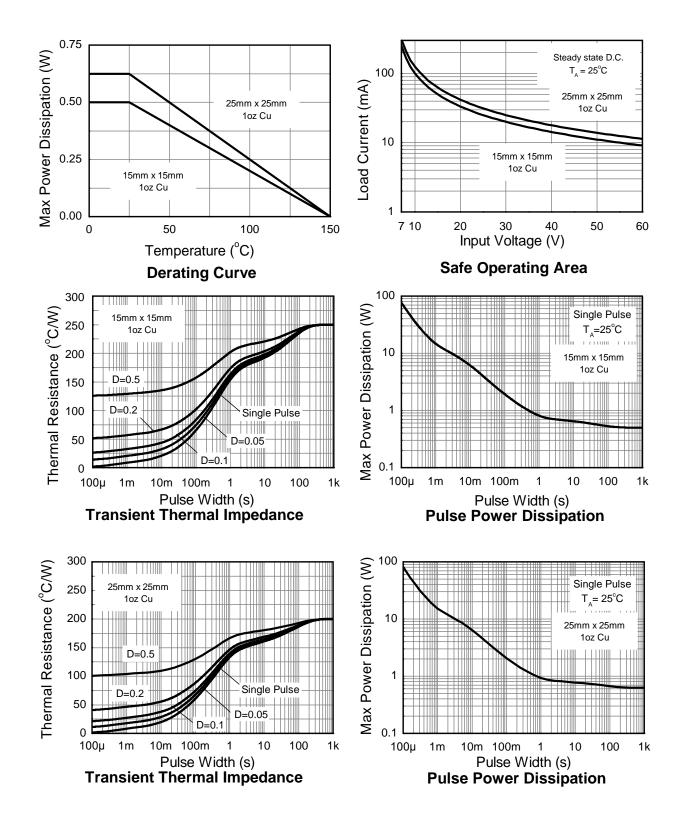
Characteristics	Symbols	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge – Machine Model	ESD MM	400	V	С

Notes:

- 5. For a device mounted with the V_{IN} lead on 25mm x 25mm 1oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in steady-state.
- 6. Same as Note 5, except mounted on 15mm x 15mm 1oz copper.
- 7. Same as Note 5, whilst operating at V_{IN}=12V. Refer to Safe Operating Area for other Input Voltages.
- 8. Same as Note 5, except measured with a single pulse width = 100μ s and $V_{IN}=12V$.
- 9. Same as Note 5, except measured with a single pulse width = 10ms and $\ensuremath{V_{\text{IN}}}\xspace=12\ensuremath{V}\xspace$.
- 10. $R_{\theta JL}$ = Thermal resistance from junction to solder-point (at the end of the V_{IN} lead). $R_{\theta JC}$ = Thermal resistance from junction to the top of case.
- 11. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information





Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Output Voltage (Note 12)	Vout	4.75	5.0	5.25	V	V _{IN} = 12V, I _{OUT} = 15mA
		1	33	220		$V_{IN} = 10V$ to 15V, $I_{OUT} = 15$ mA
Line Regulation (Notes 12 & 13)	ΔV_{OUT}	1	400	700	mV	$V_{IN} = 7V$ to 60V, $I_{OUT} = 15mA$
		_	145	400		V _{IN} = 10V to 60V, I _{OUT} = 15mA
Temperature Coefficient	Δ\/α=/ΔΤ		3.52		mV/°C	$T_J = -40^{\circ}\text{C to } +150^{\circ}\text{C}$
Temperature Coemicient	ΔV _{OUT} /ΔT	_	3.32		IIIV/ C	$V_{IN} = 12V$, $I_{OUT} = 15mA$
Load Regulation (Notes 12 & 14)	A\/	_	-20	-130	mV	$I_{OUT} = 10$ mA to 20mA, $V_{IN} = 12$ V
Load Regulation (Notes 12 & 14)	ΔV_{OUT}	_	-166	-300	IIIV	$I_{OUT} = 0.1$ mA to 50mA, $V_{IN} = 12$ V
Minimum Value of Input Voltage Required to Maintain Line Regulation	V _{IN(MIN)}	7	ı	_	V	_
Quiescent Current	1-	1	450	800	μA	$V_{IN} = 12V$, $I_{OUT} = 10\mu A$
Quiescent Gunent	IQ	1	4,000	6,700	μΑ	$V_{IN} = 60V$, $I_{OUT} = 10\mu A$
Power Supply Rejection Ratio	47/ /47/		46		dB	C _{OUT} = 100nF, I _{OUT} = 15mA,
rower Supply Rejection Ratio	$\Delta V_{IN} \Delta V_{OUT}$		40		ub	$V_{OUT} = 5V$, $V_{IN} = 7V$ to $60V$, $f = 100Hz$

Notes: 12. Measured Under Pulsed Conditions; Pulse Width ≤ 300µs. Duty cycle ≤ 2%.

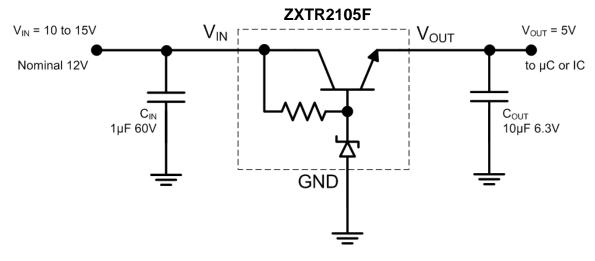
13. Line Regulation $\Delta V_{OUT} = V_{OUT}(@V_{IN} = 15V) - V_{OUT}(@V_{IN} = 10V)$

 $\Delta V_{OUT} = V_{OUT}(@V_{IN} = 60V) - V_{OUT}(@V_{IN} = 7V)$

 $\Delta V_{OUT} = V_{OUT}(@V_{IN} = 60V) - V_{OUT}(@V_{IN} = 10V)$ 14. Load Regulation $\Delta V_{OUT} = V_{OUT}(@I_{OUT} = 20mA) - V_{OUT}(@I_{OUT} = 10mA)$

 $\Delta V_{OUT} = V_{OUT} (@I_{OUT} = 50 \text{mA}) - V_{OUT} (@I_{OUT} = 0.1 \text{mA})$

Typical Application Circuit



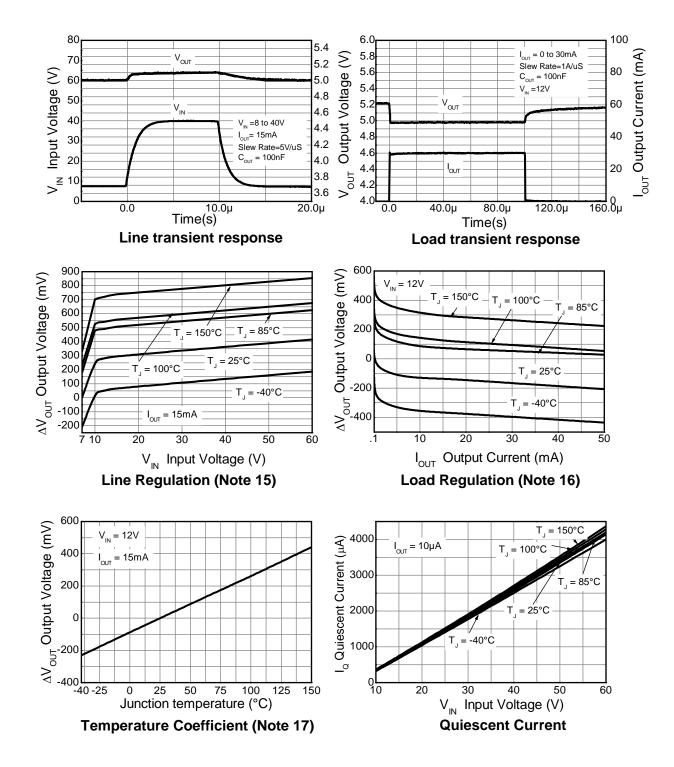
Example of a 5V regulated supply from a nominal 12V for powering a Controller IC.

Pin Functions

Pin Name	Pin Function	Notes
V _{IN}	Input Supply	Input voltage can vary from -0.3V to 60V with respect to GND; for V_{OUT} regulated then $7V \le V_{IN} \le 60V$. It is recommended to connect a 1µF capacitor to GND.
GND	Power Ground	This pin should be tied to the system ground.
		Outputs a regulated 5V when $7V \le V_{IN} \le 60V$. When $V_{IN} < 7V$, then V_{OUT} maximum = $V_{IN} - 1V$.
V _{OUT}	Voltage Output	The pin can be pulled high to a maximum of +10V with respect to GND, or +5V with respect to V_{IN} , whichever is lower. It is recommended to connect a $10\mu F$ capacitor to GND and a minimum of $10\mu A$ to be drawn from V_{OUT} to maintain regulation.



Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)



Notes: 15. Line Regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@V_{IN} = 7V, I_{OUT} = 15mA, T_J = +25^{\circ}C)$.

16. Load Regulation $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@V_{IN} = 12V, I_{OUT} = 0.1 mA, T_J = +25 °C)$.

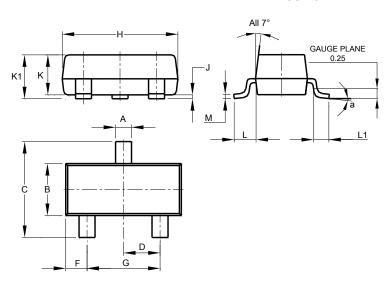
17. Temperature Coefficient $\Delta V_{OUT} = V_{OUT} - V_{OUT} (@V_{IN} = 12V, I_{OUT} = 15mA, T_{J} = +25^{\circ}C)$.



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

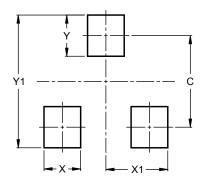


SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Η	2.80	3.00	2.90		
J	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
L	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
М	0.085	0.150	0.110		
а	0°	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	29



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