

PDTA114ET-Q

PNP resistor-equipped transistor; R1 = 10 kΩ, R2 = 10 kΩ25 February 2022Product data sheet

## 1. General description

PNP Resistor-Equipped Transistor (RET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTC114ET-Q

## 2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- · Qualified according to AEC-Q101 and recommended for use in automotive applications

### 3. Applications

- Digital application in automotive and industrial segments
- Cost-saving alternative for BC847-Q series in digital applications
- Controlling IC inputs
- Switching loads

## 4. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	-50	V
lo	output current			-	-	-100	mA
R1	bias resistor 1 (input)		[1]	7	10	13	kΩ
R2/R1	bias resistor ratio		[1]	0.8	1	1.2	

[1] See "Section 11: Test information" for resistor calculation and test conditions.

# nexperia

## 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	3	c
2	GND	ground (emitter)		
3	0	output (collector)		R2 E sym003

## 6. Ordering information

### Table 3. Ordering information

Type number	Package	ackage				
	Name	Description	Version			
PDTA114ET-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

## 7. Marking

### Table 4. Marking codes

Type number	Marking code[1]
PDTA114ET-Q	%03

[1] % = placeholder for manufacturing site code

PDTA114ET-Q

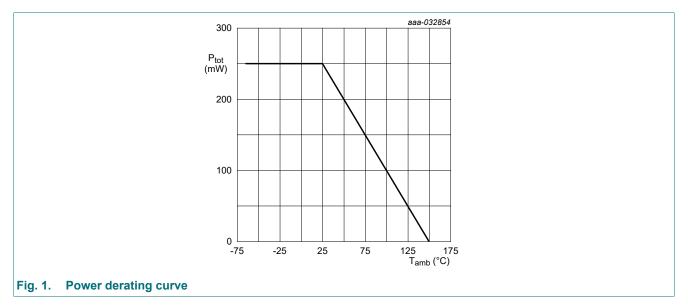
## 8. Limiting values

### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-50	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-10	V
VI	input voltage	positive		-	40	V
		negative		-	-10	V
I <sub>O</sub>	output current			-	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

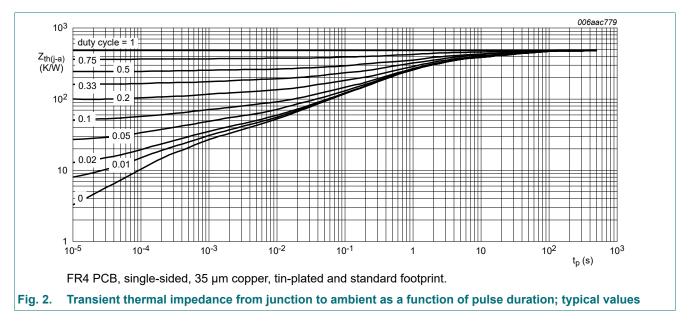
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 µm copper, tin-plated and standard footprint.



## 9. Thermal characteristics

Table 6. Therma	al characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-u)	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 µm copper, tin-plated and standard footprint.

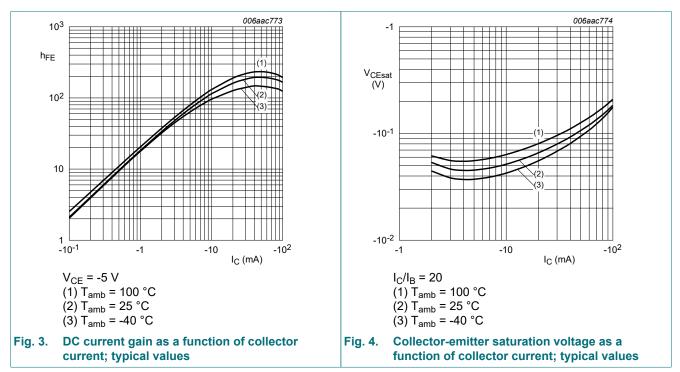


## **10. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C		50	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	$I_{C} = 2 \text{ mA}; I_{B} = 0 \text{ A}; T_{amb} = 25 \text{ °C}$		50	-	-	V
I <sub>CBO</sub>	collector-base cut-off current	$V_{CB} = -50 \text{ V}; \text{ I}_{\text{E}} = 0 \text{ A}; \text{ T}_{\text{amb}} = 25 \text{ °C}$		-	-	-100	nA
I <sub>CEO</sub>	collector-emitter cut-off	V <sub>CE</sub> = -30 V; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-1	μA
	current	V <sub>CE</sub> = -30 V; I <sub>B</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C		-	-	-400	μA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -5 mA; T <sub>amb</sub> = 25 °C		30	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{C}$ = -10 mA; $I_{B}$ = -0.5 mA; $T_{amb}$ = 25 °C		-	-	-150	mV
V <sub>I(off)</sub>	off-state input voltage	$V_{CE}$ = -5 V; I <sub>C</sub> = -100 µA; T <sub>amb</sub> = 25 °C		-	-1.1	-0.8	V
V <sub>I(on)</sub>	on-state input voltage	$V_{CE}$ = -0.3 V; I <sub>C</sub> = -10 mA; T <sub>amb</sub> = 25 °C		-2.5	-1.8	-	V
R1	bias resistor 1 (input)		[1]	7	10	13	kΩ
R2/R1	bias resistor ratio		[1]	0.8	1	1.2	
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C		-	-	3	pF
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	[2]	-	180	-	MHz

[1] See "Section 11: Test information" for resistor calculation and test conditions.

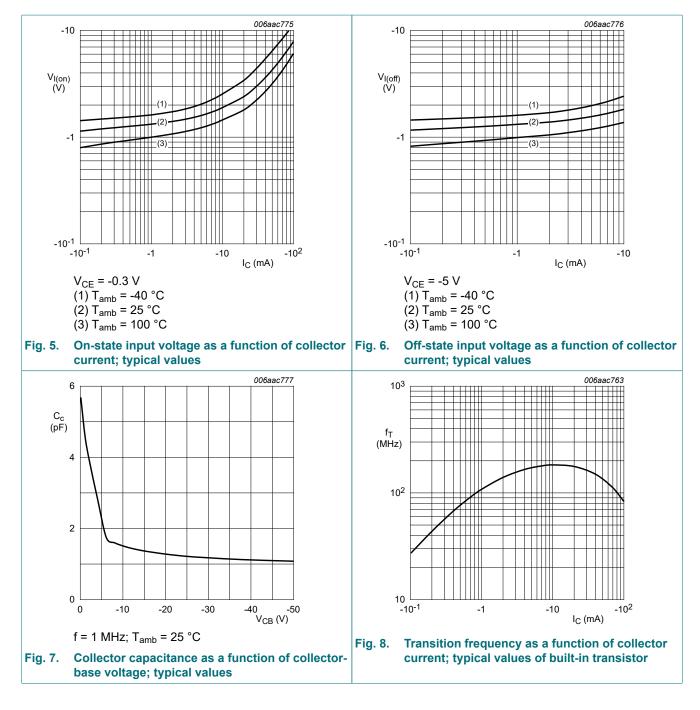
[2] Characteristics of built-in transistor.



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### PNP resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 10 k $\Omega$



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## **11. Test information**

### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

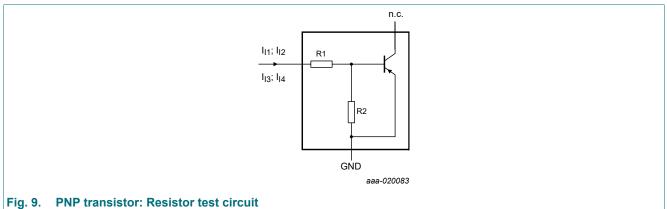
### **Resistor calculation**

• Calculation of bias resistor 1 (R1)

$$R1 = \frac{V(I_{12}) - V(I_{11})}{I_{12} - I_{11}}$$

Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I14) - V(I13)}{R1 \cdot (I14 - I13)} - 1$$

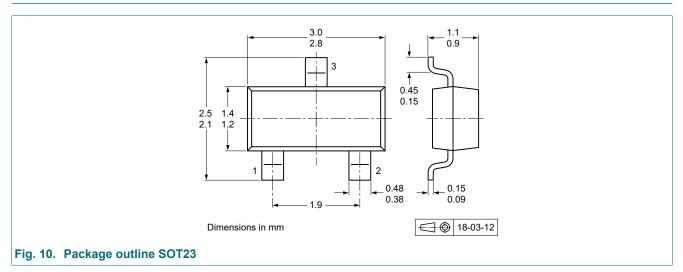


### **Resistor test conditions**

### Table 8. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions				
			I <sub>I1</sub>	I <sub>12</sub>	I <sub>13</sub>	I <sub>14</sub>	
PDTA114ET-Q	10	10	-350 µA	-450 µA	350 µA	450 µA	

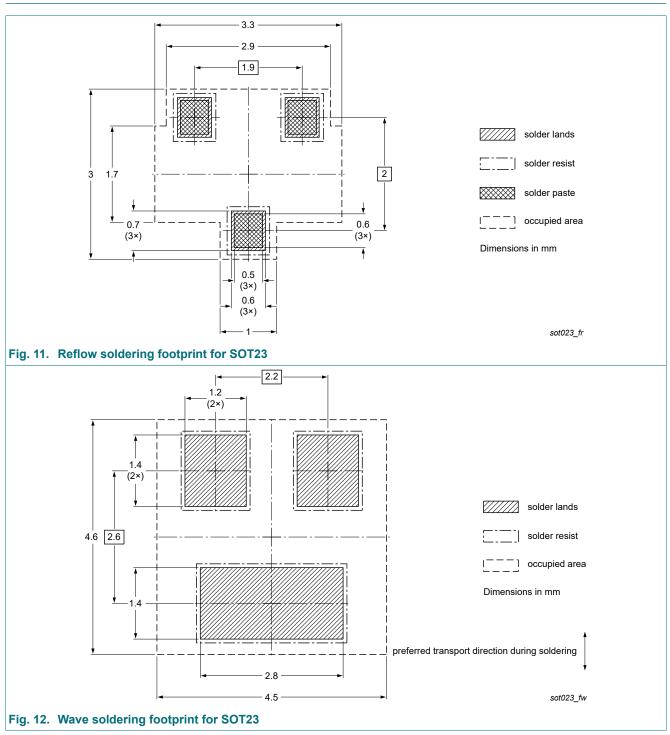
## 12. Package outline



## PDTA114ET-Q

### PNP resistor-equipped transistor; R1 = 10 k $\Omega$ , R2 = 10 k $\Omega$

## 13. Soldering



**Product data sheet** 

## 14. Revision history

Table 9. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PDTA114ET-Q v.1	20220225	Product data sheet	-	-	

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## 15. Legal information

### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
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