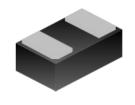


PESDUC2FD5VBA

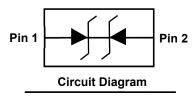
Bi-directional 5V Low Capacitance ESD Protector

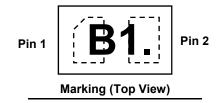
Description

The PESDUC2FD5VBA protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, low operating voltage. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.



DFN1006-2L(Bottom View)





Feature

- 80W peak pulse power per line (t_P = 8/20µs)
- DFN1006-2L package
- Replacement for MLV(0402)
- Bidirectional configurations
- Response time is typically < 1ns</p>
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to IEC61000-4-2(ESD) ±25KV(air), ±25KV(contact); IEC61000-4-4 (EFT) 40A (5/50ns)

Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

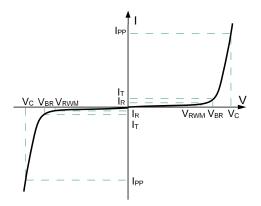
Mechanical Characteristics

- Mounting position: Any
- Qualified max reflow temperature:260°C
- > Device meets MSL 1 requirements
- DFN1006-2L without plating

PESDUC2FD5VBA

Electronics Parameter

Symbol	Parameter		
VRWM	Peak Reverse Working Voltage		
IR	Reverse Leakage Current @ V _{RWM}		
VBR	Breakdown Voltage @ I⊤		
IT	Test Current		
IPP	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		
IF	Forward Current		
VF	Forward Voltage @ I⊧		



Electrical characteristics per line@25 $^{\circ}$ C (unless otherwise specified)

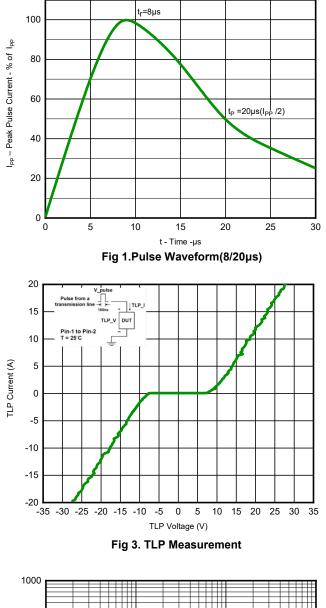
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	VRWM				5	V
Breakdown Voltage	V _{BR}	lt = 1mA 5.6			9.0	V
Reverse Leakage Current	I _R	V _{RWM} = 5V T=25°C			1.0	μA
Clamping Voltage	VcL	I _{PP} =16A t _p =100ns		24		V
Clamping Voltage	Vc	IPP=1.0A		10	13	V
Clamping Voltage	Vc	I _{PP} =4.5A		18	22	V
Junction Capacitance	Cj	V _R =0V f = 1MHz		0.25	0.45	pF

Absolute maximum rating@25℃

Rating	Symbol	Value	Units
Peak Pulse Power (t _p =8/20µs)	P _{pp}	80	W
Operating Temperature	TJ	-55 to 150	°C
Storage Temperature	Tstg	-55 to 150	°C

PESDUC2FD5VBA

Typical Characteristics



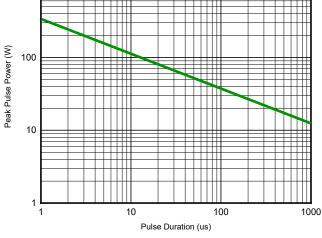
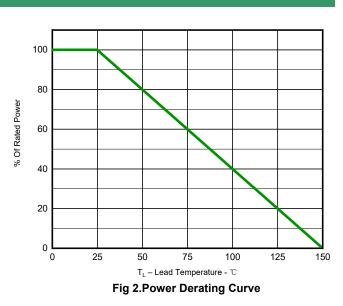


Fig 5. Non Repetitive Peak Pulse Power vs. Pulse time



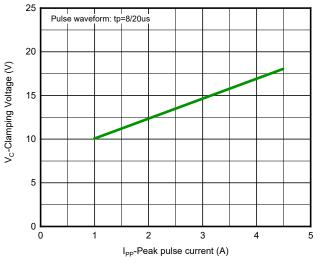


Fig 4. Clamping voltage vs. Peak pulse current

PESDUC2FD5VBA

(IEC61000-4-2 -8KV contact)

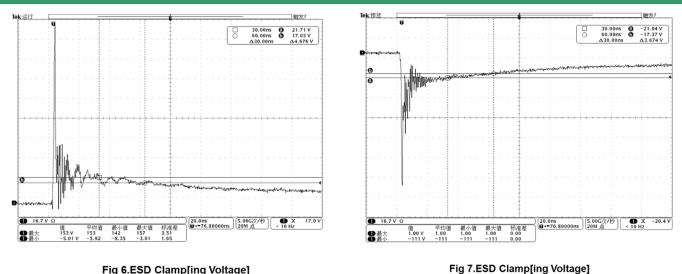
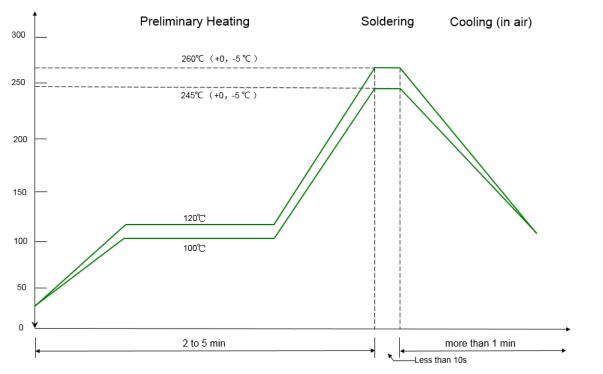


Fig 6.ESD Clamp[ing Voltage] (IEC61000-4-2 +8KV contact)

Solder Reflow Recommendation



RRemark: Pb free for 260°C; Pb for 245°C.

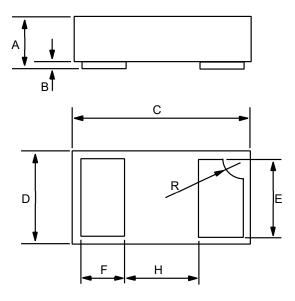
PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

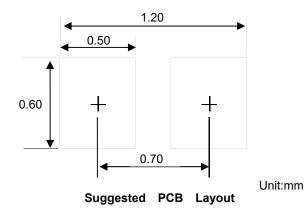
- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

PESDUC2FD5VBA

Product dimension (DFN1006-2L)



Dim	Inc	hes	Millimeters		
	MIN	MAX	MIN	МАХ	
А	0.013	0.020	0.34	0.50	
В	0.000	0.002	0.00	0.05	
С	0.037	0.043	0.95	1.080	
D	0.022	0.027	0.55	0.680	
E	0.016	0.024	0.40	0.60	
F	0.008	0.012	0.20	0.30	
н	0.015Typ.		0.40Тур.		
R	0.001	0.005	0.05	0.15	



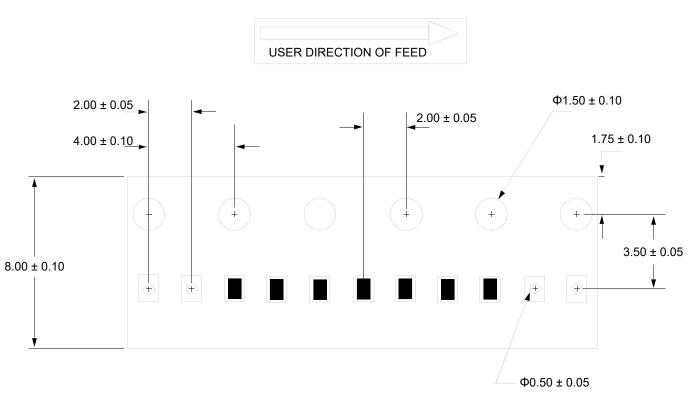
Ordering information

Device	Package	Reel	Shipping
PESDUC2FD5VBA	DFN1006-2L (Pb-Free)	7"	10000 / Tape & Reel

PESDUC2FD5VBA

ESD Protector

Load with information



Unit: mm

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