**New Product** 

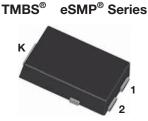


V15P45

Vishay General Semiconductor

# High Current Density Surface Mount Trench MOS Barrier Schottky Rectifier

Ultra Low  $V_F = 0.31$  V at  $I_F = 5$  A



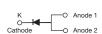
15 A

45 V

210 A

0.42 V

150 °C



**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub>

V<sub>RRM</sub>

I<sub>FSM</sub>

 $V_F$  at  $I_F = 15 A$ 

T<sub>.1</sub> max.

TO-277A (SMPC)

# FEATURES

- Very low profile typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

### TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

### **MECHANICAL DATA**

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V15P45	UNIT	
Device marking code		V1545		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	45	V	
Maximum DC forward current	I <sub>F</sub> <sup>(1)</sup>	15	Α	
	I <sub>F</sub> <sup>(2)</sup>	4.8		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	210	А	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 40 to + 150	°C	

Notes

<sup>(1)</sup> Mounted on 30 mm x 30 mm pad areas aluminum PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area

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COMPLIANT

HALOGEN

FREE



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V15P45

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.40	-	V	
	I <sub>F</sub> = 7.5 A			0.45	-		
	I <sub>F</sub> = 15 A			0.49	0.58		
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.31	-		
	I <sub>F</sub> = 7.5 A			0.34	-		
	I <sub>F</sub> = 15 A			0.42	0.51		
Reverse current	$T_{A} = 25 \text{ °C}$	$V_{R} = 45 \text{ V} \qquad \frac{T_{A} = 25 \text{ °C}}{T_{A} = 125 \text{ °C}}$	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	1500	μA
	$v_{\rm R} = 45 v$		IR (/	15	50	mA	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL V15P45 U		UNIT	
Turnical thermal registeres	R <sub>0JA</sub> <sup>(1)</sup>	75	°C/W	
Typical thermal resistance	R <sub>0JM</sub> <sup>(2)</sup>	4		

### Notes

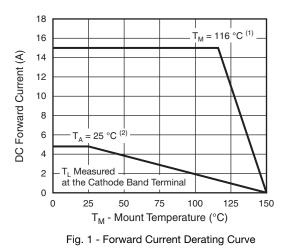
 $^{(1)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

<sup>(2)</sup> Mounted on 30 mm x 30 mm aluminum PCB; thermal resistance  $R_{\theta JM}$  - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V15P45-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel	
V15P45-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel	

# **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)



Notes

- $^{(1)}$  Mounted on 30 mm x 30 mm aluminum PCB; T<sub>M</sub> measured at the terminal of cathode band (R<sub>0JM</sub> = 4 °C/W)
- $^{(2)}$  Free air, mounted on recommended copper pad area  $(R_{\theta JA}=75~^{\circ}C/W)$

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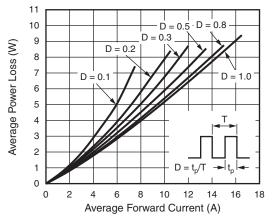


Fig. 2 - Forward Power Loss Characteristics Per Diode

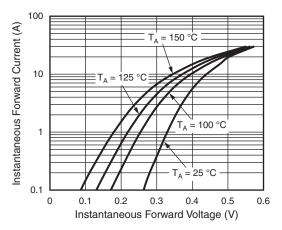


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

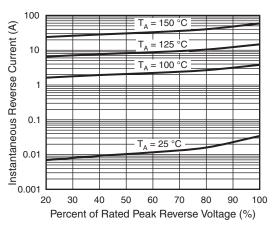


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

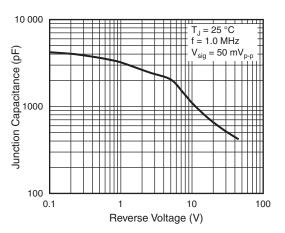


Fig. 5 - Typical Junction Capacitance

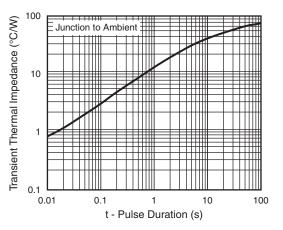


Fig. 6 - Typical Transient Thermal Impedance Per Diode

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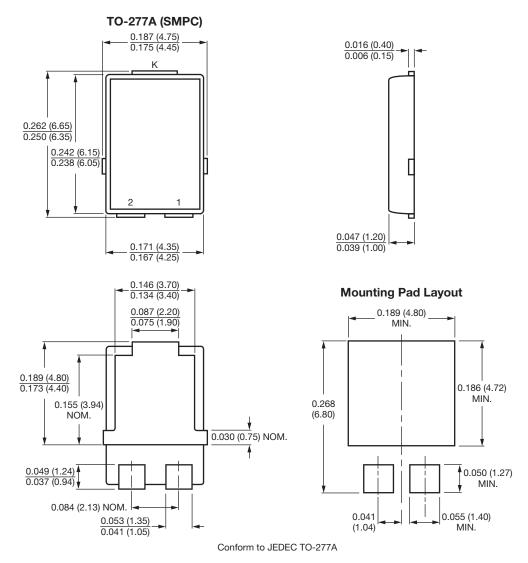
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# **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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