

KBL4005G thru KBL404G

KBL Package

Single Phase Glass Passivated Silicon Bridge Rectifier

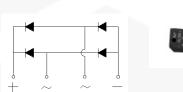
 $V_{\text{RRM}} = 50 \text{ V} - 400 \text{ V}$ $I_{\text{O}} = 4 \text{ A}$

Features

- · Ideal for printed circuit board
- Reliable low cost construction utilizing molded plastic technique
- High surge current capability
- Silver plated copper leads
- Types from 50 V to 400 V V_{RRM}
- Not ESD Sensitive

Mechanical Data

Case: Molded plastic Terminals: Plated terminals, solderable per MIL-STD-202F, Method 208 Polarity: Marked on body Weight: 0.167 ounce, 5 grams Mounting position: Any





REACH

Maximum ratings at T_i = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions	KBL4005G	KBL401G	KBL402G	KBL404G	Unit
Repetitive peak reverse vo	oltage V _{RRM}		50	100	200	400	V
RMS reverse voltage	V _{RMS}		35	70	140	280	V
DC blocking voltage	V _{DC}		50	100	200	400	V
Operating temperature	Tj		-50 to 150	-50 to 150	-50 to 150	-50 to 150	°C
Storage temperature	T _{stg}		-50 to 150	-50 to 150	-50 to 150	-50 to 150	°C

Electrical characteristics at Tj = 25 °C, unless otherwise specified

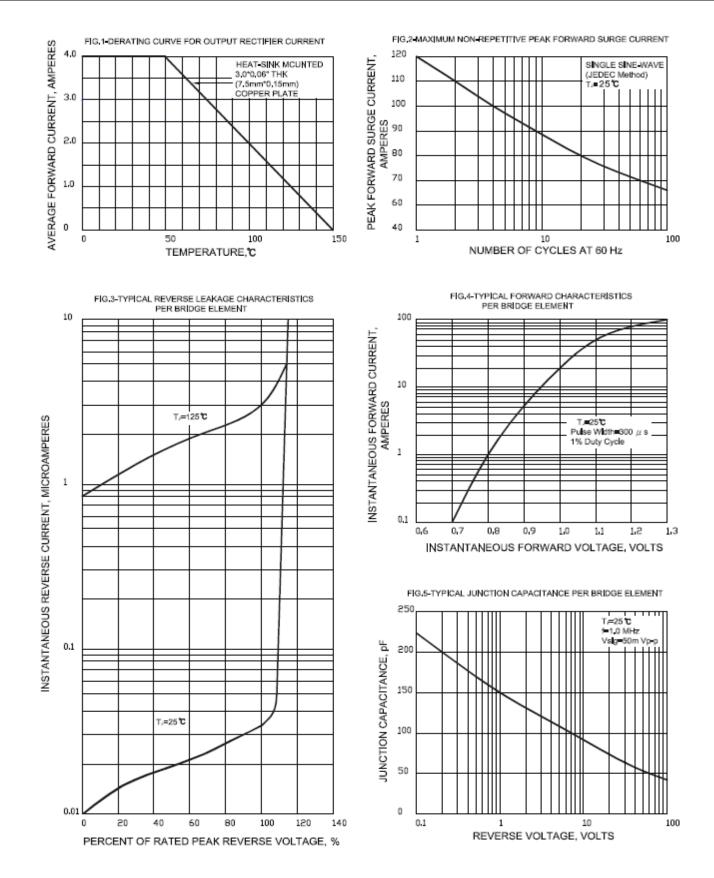
Single phase, half sine wave, 60 Hz, resistive or inductive load

For capacitive load derate current by 20%

Parameter	Symbol	Conditions	KBL4005G	KBL401G	KBL402G	KBL404G	Unit
Maximum average forward rectified current	I _O	T _a = 50 °C	4	4	4	4	А
Peak forward surge current	I _{FSM}	single sine-wave	120	120	120	120	А
Maximum instantaneous forward voltage per leg	V _F	I _F = 4 A	1.1	1.1	1.1	1.1	V
Maximum reverse current at rated DC blocking voltage per leg	I _R	T _a = 25 °C	5	5	5	5	μΑ
		T _a = 125 °C	100	100	100	100	



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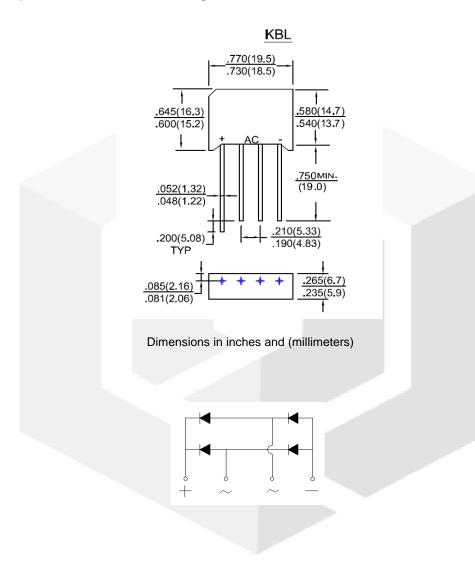


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Package dimensions and terminal configuration

Product is marked with part number and terminal configuration.



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GeneSiC Semiconductor: KBL404G