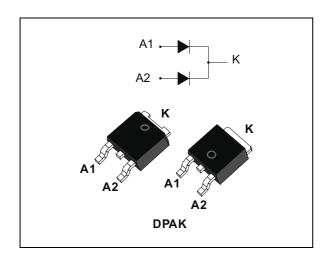
## STTH802C



## High efficiency ultrafast diode

Datasheet - production data



### **Features**

- Suited for SMPS
- Low losses
- Low forward and reverse recovery time
- · High surge current capability
- High junction temperature
- ECOPACK<sup>®</sup>2 compliant component for DPAK on demand

### **Description**

Dual center tap rectifier suited for switched mode power supply and high frequency DC to DC converters.

Packaged in DPAK, this device is intended for use in low voltage, high frequency inverters, freewheeling and polarity protection applications.

**Table 1. Device summary** 

Symbol	Value
I <sub>F(AV)</sub>	2 X 4 A
$V_{RRM}$	200 V
T <sub>j</sub> (max)	175 °C
V <sub>F</sub> (typ.)	0.81 V
t <sub>rr</sub> (typ.)	13 ns

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### 1 Characteristics

Table 2. Absolute ratings (limiting values at  $T_j = 25$  °C per diode, unless otherwise specified)

Symbol		Value	Unit			
$V_{RRM}$	Repetitive peak reverse voltage	200	V			
I <sub>F(RMS)</sub>	Forward rms current			10	Α	
	Average forward current,	T <sub>c</sub> = 155 °C	= 155 °C Per diode		А	
'F(AV)	$\delta = 0.5$ , square wave		Per device	8		
I <sub>FSM</sub>	Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}$				Α	
T <sub>stg</sub>	Storage temperature range				°C	
T <sub>j</sub>	Maximum operating junction temperature				°C	

**Table 3. Thermal resistances** 

Symbol	Parameter	Max. value	Unit	
D	Junction to case	Per diode	4	
R <sub>th(j-c)</sub>	Junction to case	Total	2.5	°C/W
R <sub>th(c)</sub>	Coupling		1.0	

When the diodes 1 and 2 are used simultaneously:

 $\Delta T_{j}(diode 1) = P(diode1) \times R_{th(j-c)}(Per diode) + P(diode 2) \times R_{th(c)}$ 

Table 4. Static electrical characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
I <sub>R</sub> <sup>(1)</sup>	Reverse leakage current	T <sub>j</sub> = 25 °C	\/- <b>-</b> \/	-		4	μA
Reverse leakage current	T <sub>j</sub> = 125 °C	$V_R = V_{RRM}$	-	2	40	μΑ	
	T <sub>j</sub> = 25 °C	-		1.10			
V <sub>E</sub> <sup>(2)</sup>	Forward voltage drop	$T_j = 125  ^{\circ}\text{C}$ $I_F = 4  \text{A}$	-	0.81	0.95	V	
VF`	$ T_j = 25 \text{ °C} $ $ T_j = 125 \text{ °C} $ $ T_j = 125 \text{ °C} $	I _ Q A	-		1.25	V	
		IF	-	0.95	1.10		

- 1. Pulse test:  $t_p = 5 \text{ ms}$ ,  $\delta < 2\%$
- 2. Pulse test:  $t_p$  = 380  $\mu$ s,  $\delta$  < 2%

To evaluate the conduction losses, use the following equation:

$$P = 0.80 \text{ x I}_{F(AV)} + 0.037 \text{ x I}_{F}^{2}_{(RMS)}$$

STTH802C Characteristics

Table 5. Dynamic characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
t <sub>rr</sub>	Reverse recovery time		$I_F = 0.5 A$ $I_{RR} = 0.25 A$ $I_R = 1 A$	-	13	20	ns
t <sub>fr</sub>	Forward recovery time	T <sub>j</sub> = 25 °C	I <sub>F</sub> = 4 A dI <sub>F</sub> /dt = 100 A/μs V <sub>FR</sub> = 1.1 x V <sub>Fmax</sub>	-	50		ns
V <sub>FP</sub>	Forward recovery voltage		$I_F = 4A$ $dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	2.4		٧

Characteristics STTH802C

Figure 1. Average forward power dissipation versus average forward current (per diode)

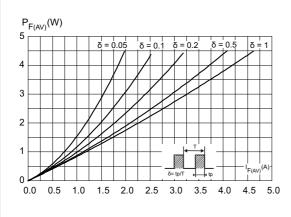


Figure 2. Forward voltage drop versus forward current (per diode)

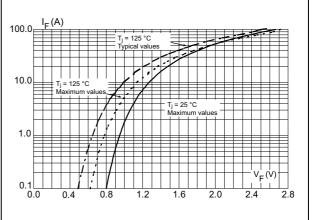


Figure 3. Relative variation of thermal impedance, junction to case, versus pulse duration

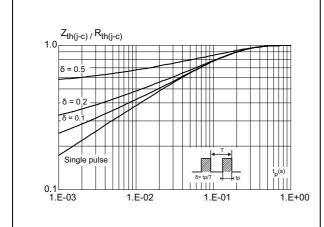


Figure 4. Junction capacitance versus reverse applied voltage (typical values, per diode)

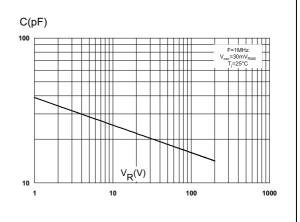


Figure 5. Reverse recovery charges versus dl<sub>F</sub>/dt (typical values, per diode)

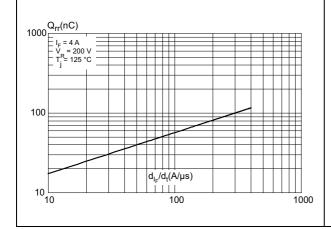
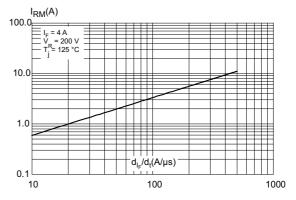


Figure 6. Peak reverse recovery current versus dl<sub>F</sub>/dt (typical values, per diode)



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-T<sub>i</sub>(°C)

150

125

Figure 7. Dynamic parameters versus junction temperature (reference: T<sub>j</sub> = 125 °C)

1.4
1.2
V<sub>R</sub> = 200 V
1.0
0.8
0.6
0.4

75

100

ambient, versus copper surface under tab

Rth(j-a)(°C/W)

100
90
Epoxy printed board FR4, copper thickness = 35 µm
DPAK

70
40
30
20
10
0
5
10
15
20
25
30
35
40

Figure 8. Thermal resistance, junction to



0.2

0.0

25

**Package Information** STTH802C

#### 2 **Package Information**

- Epoxy meets UL94,V0
- Cooling method: by conduction (C)

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

#### 2.1 **DPAK** package information

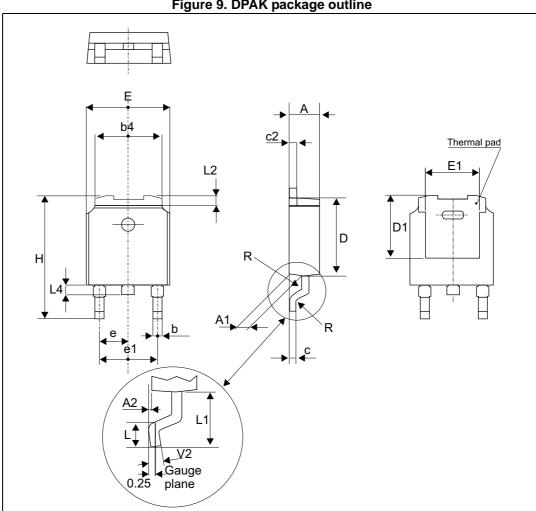


Figure 9. DPAK package outline

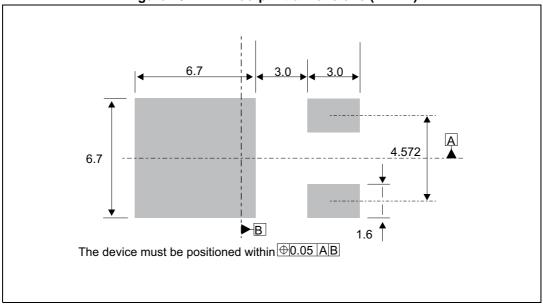
Note:

This package drawing may slightly differ from the physical package. However, all the specified dimensions are guaranteed.

Table 6. DPAK package mechanical data

				Dimensions		
Ref.		Millimeters		Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
А	2.18		2.40	0.085		0.094
A1	0.90		1.10	0.035		0.043
A2	0.03		0.23	0.001		0.009
b	0.64		0.90	0.025		0.035
b4	4.95		5.46	0.194		0.214
С	0.46		0.61	0.018		0.024
c2	0.46		0.60	0.018		0.023
D	5.97		6.22	0.235		0.244
D1	4.95		5.60	0.194		0.220
E	6.35		6.73	0.250		0.264
E1	4.32		5.50	0.170		0.216
е		2.28			0.090	
e1	4.40		4.70	0.173		0.185
Н	9.35		10.40	0.368		0.409
L	1.00		1.78	0.039		0.070
L2			1.27			0.050
L4	0.60		1.02	0.023		0.040
V2	-8°		+8°	-8°		8°

Figure 10. DPAK footprint dimensions (in mm)



Ordering Information STTH802C

# **3 Ordering Information**

**Table 7. Ordering information** 

Order code	Marking	Package	Weight	Base qty	Delivery mode
STTH802CB-TR	STTH8 02CB	DPAK	0.3 g	2500	Tape and reel

## 4 Revision history

**Table 8. Document revision history** 

Date	Revision	Description of Changes
26-Jun-2012	1	First release.
04-Nov-2014	2	Removed TO-220AB and TO-220FPAB package information. Reformatted to current standard.
02-Nov-2016	3	Updated DPAK package information and reformatted to current standard.

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