1. General description

Maximum Efficiency General Application (MEGA) Schottky barrier rectifier, encapsulated in a CFP15 (SOT1289) power and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 10 A
- Reverse voltage: V_R ≤ 100 V
- Low leakage current due to high Schottky barrier technology
- Low forward voltage
- High power capability due to clip-bonding technology and heat sink
- High temperature T_i ≤ 175 °C
- Small and thin SMD power plastic package, typical height 0.78 mm
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- Automotive LED lighting
- High efficiency DC-to-DC conversion
- Switch mode power supply
- · Reverse polarity protection
- Low power consumption application

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; $T_{amb} \le 150$ °C; square wave	-	-	10	Α
V_R	reverse voltage	T _j = 25 °C	-	-	100	V
V _F	forward voltage	$I_F = 10 \text{ A; } t_p \le 300 \mu\text{s; } \delta \le 0.02;$ $T_j = 25 ^{\circ}\text{C}$	-	770	850	mV
I _R	reverse current	$V_R = 100 \text{ V}; t_p \le 3 \text{ ms}; \delta \le 0.03;$ $T_j = 25 ^{\circ}\text{C}$	-	0.2	0.8	μA



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	А	anode		r □ □ A
2	А	anode	3	A aaa-009063
3	K	cathode	2 CFP15 (SOT1289)	

6. Ordering information

Table 3. Ordering information

Type number	Package	ackage						
	Name	Description	Version					
PMEG100V100ELPD	CFP15	plastic, thermal enhanced ultra thin SMD package; 3 leads; body: 5.8 x 4.3 x 0.78 mm	SOT1289					

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG100V100ELPD	100V L10E

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	100	V
l _F	forward current	δ = 1; T _{sp} ≤ 145 °C		-	14	Α
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; $T_{amb} \le 150$ °C; square wave		-	10	Α
	non-repetitive peak	t _p = 8 ms; square wave; T _{j(init)} = 25 °C		-	170	Α
	forward current	t_p = 8.3 ms; single half sine wave; $T_{j(init)}$ = 25 °C		-	210	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.66	W
			[2]	-	2.15	W
				-	3.75	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [3] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient		[1] [2]	-	-	90	K/W
			[1] [3]	-	-	70	K/W
			[1] [4]	-	-	40	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		<u>[5]</u>	-	-	3	K/W

^[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

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^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

^[4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

^[5] Soldering point of cathode tab.

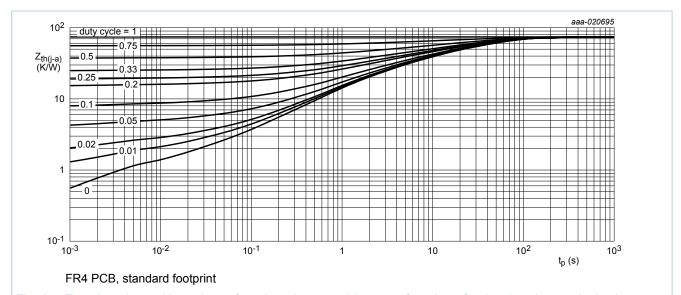


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

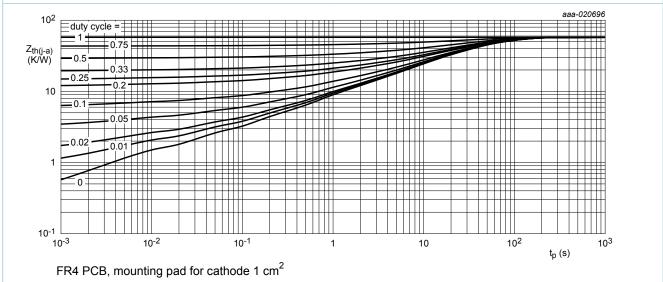
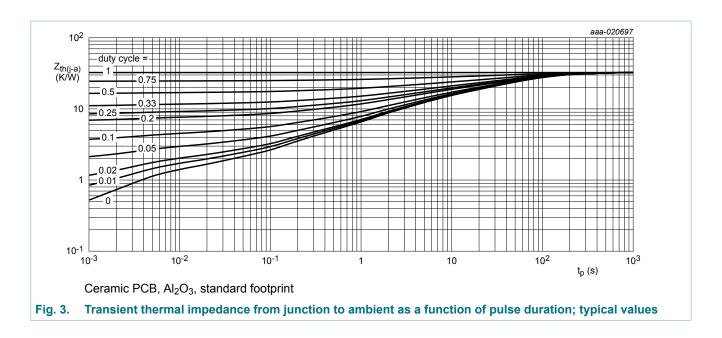


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

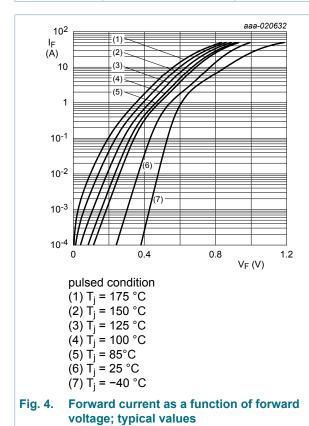


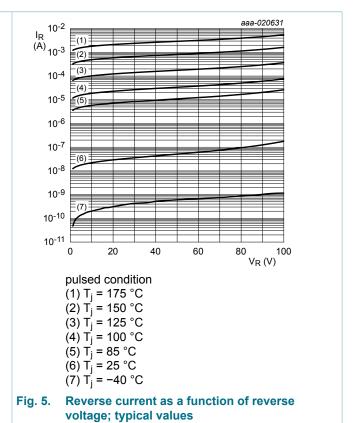
10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	$I_R = 1 \text{ mA}; t_p \le 1.2 \text{ ms}; \delta \le 0.12;$ pulsed; $T_j = 25 \text{ °C}$	100	-	-	V
V _F	forward voltage	$I_F = 0.1 \text{ A}; t_p \le 300 \mu\text{s}; \delta \le 0.02;$ $T_j = 25 ^{\circ}\text{C}$	-	440	-	mV
		I_F = 1 A; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C	-	545	650	mV
		I_F = 2 A; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C	-	610	710	mV
		I_F = 4 A; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C	-	685	-	mV
		I_F = 5 A; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C	-	700	790	mV
		I_F = 6 A; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C	-	720	-	mV
		I_F = 8 A; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 25 °C	-	745	-	mV
		I_F = 10 A; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 25 °C	-	770	850	mV
		I_F = 10 A; $t_p \le 300$ μs; $δ \le 0.02$; T_j = -40 °C	-	870	960	mV
		I_F = 5 A; $t_p \le 300 \text{ μs}$; $\delta \le 0.02$; T_j = 125 °C	-	570	-	mV
		I_F = 10 A; $t_p \le 300$ μs; $δ \le 0.02$; T_j = 125 °C	-	635	730	mV

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _R	reverse current	$V_R = 60 \text{ V}; t_p \le 3 \text{ ms}; \delta \le 0.03;$ $T_j = 25 \text{ °C}$	-	0.06	-	μΑ
		$V_R = 80 \text{ V}; t_p \le 3 \text{ ms}; \delta \le 0.03;$ $T_j = 25 \text{ °C}$	-	0.09	-	μΑ
		$V_R = 100 \text{ V}; t_p \le 3 \text{ ms}; \delta \le 0.03;$ $T_j = 25 \text{ °C}$	-	0.2	0.8	μΑ
		$V_R = 100 \text{ V}; t_p \le 3 \text{ ms}; \delta \le 0.03;$ $T_j = 125 \text{ °C}$	-	0.38	2.5	mA
		$V_R = 60 \text{ V; } t_p \le 3 \text{ ms; } \delta \le 0.03;$ $T_j = 150 ^{\circ}\text{C}$	-	0.92	3.5	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	365	-	pF
		V _R = 4 V; f = 1 MHz; T _j = 25 °C	-	215	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	135	-	pF
t _{rr}	reverse recovery time	$I_F = 0.5 \text{ A}$; $I_R = 0.5 \text{ A}$; $I_{R(meas)} = 0.1 \text{ A}$; $I_{j} = 25 \text{ °C}$	-	14	-	ns
V_{FRM}	peak forward recovery voltage	$I_F = 0.5 \text{ A}; \text{ d}I_F/\text{d}t = 20 \text{ A/}\mu\text{s}; T_j = 25 ^{\circ}\text{C}$	-	555	-	mV





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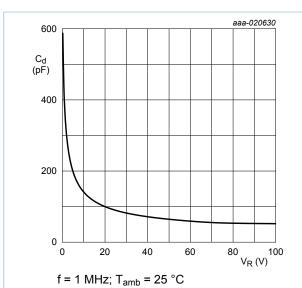
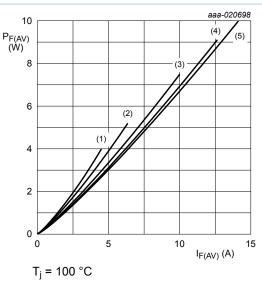
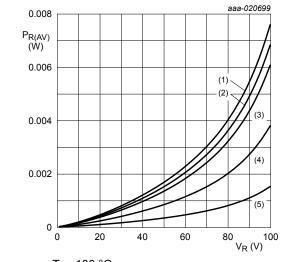


Fig. 6. Diode capacitance as a function of reverse voltage; typical values



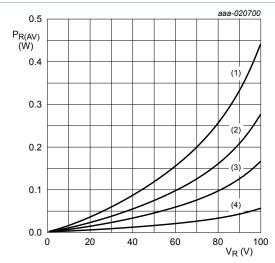
 $T_j = 100 \,^{\circ}\text{C}$ (1) $\delta = 0.1$ (2) $\delta = 0.2$ (3) $\delta = 0.5$ (4) $\delta = 0.8$ (5) $\delta = 1$

Fig. 7. Average forward power dissipation as a function of average forward current; typical values



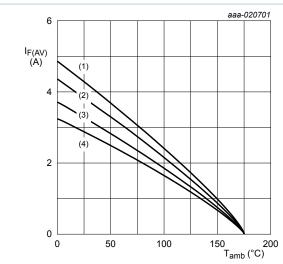
 $T_j = 100 \,^{\circ}\text{C}$ $(1) \, \delta = 1$ $(2) \, \delta = 0.9$ $(3) \, \delta = 0.8$ $(4) \, \delta = 0.5$ $(5) \, \delta = 0.2$

Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values



 $T_j = 175 \,^{\circ}\text{C}$ (1) $\delta = 1$ (2) $\delta = 0.5$ (3) $\delta = 0.2$ (4) $\delta = 0.1$

Fig. 9. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

 $T_i = 175 \,{}^{\circ}\text{C}$

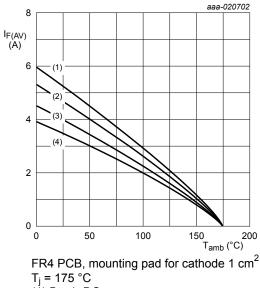
 $(1) \delta = 1; DC$

(2) $\delta = 0.5$; f = 20 kHz

(3) δ = 0.2; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



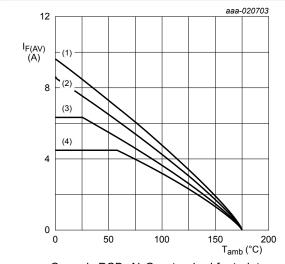
 $(1) \delta = 1; DC$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al₂O₃, standard footprint

T_i = 175 °C

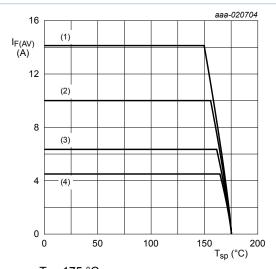
 $(1) \delta = 1 (DC)$

(2) δ = 0.5; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 12. Average forward current as a function of ambient temperature; typical values



 $T_i = 175 \, ^{\circ}C$

 $(1) \delta = 1 (DC)$

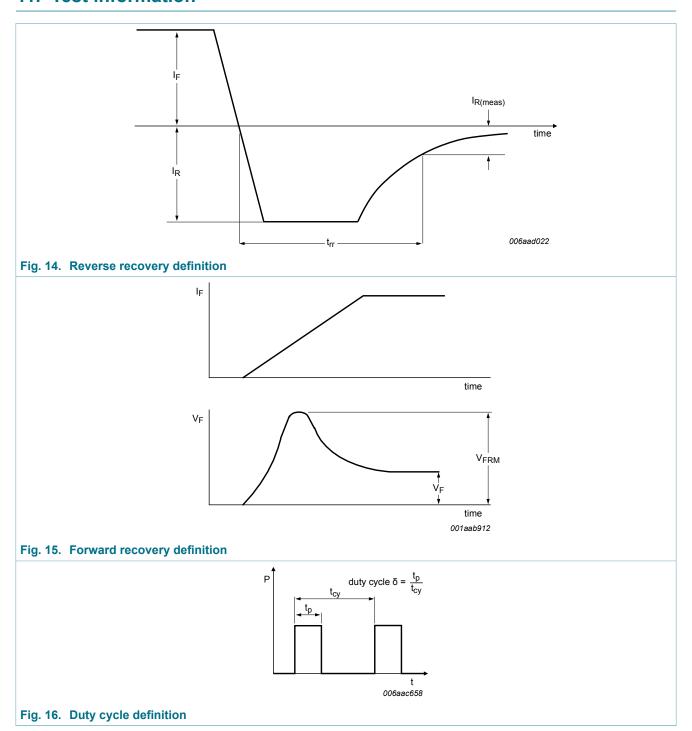
(2) δ = 0.5; f = 20 kHz

(3) δ = 0.2; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 13. Average forward current as a function of solder point temperature; typical values

11. Test information



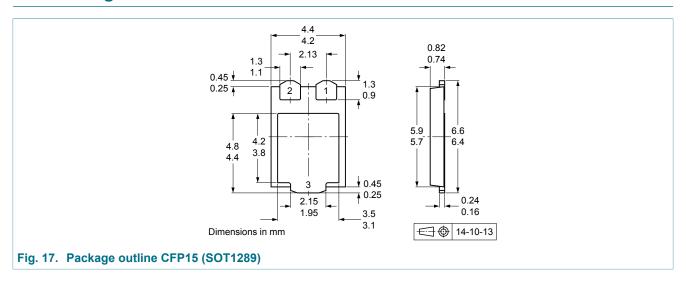
The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

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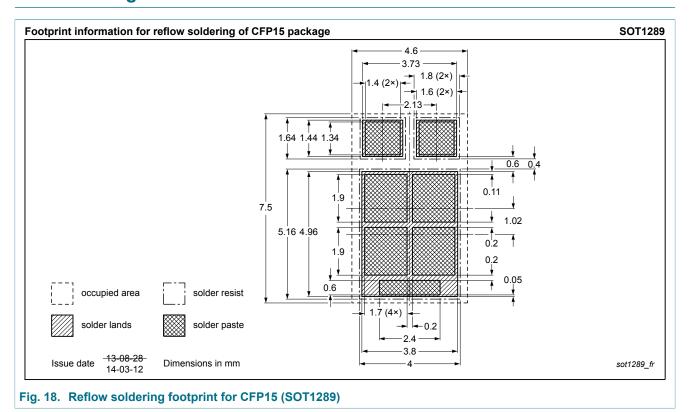
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

Table 6. Nevision history								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMEG100V100ELPD v.4	20180405	Product data sheet	-	PMEG100V100ELPD v.3				
Modifications:	I _{FSM} parameter	added (sine wave)						
PMEG100V100ELPD v.3	20161004	Product data sheet	-	PMEG100V100ELPD v.2				
PMEG100V100ELPD v.2	20160203	Preliminary data sheet	-	PMEG100V100ELPD v.1				
PMEG100V100ELPD v.1	20151117			-				

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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PMEG100V100ELPD

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