GD30MPS12J
1200V 30A SiC Schottky MPS™ Diode

GeneSiC™
SEMICONDUCTOR

Silicon Carbide Schottky Diode

|		
V_{RRM}	=	1200 V
$I_{F\,(T_C = 150°C)}$	=	30 A
Q_C	=	97 nC

Features

- Gen4 Thin Chip Technology for Low V_F
- Superior Figure of Merit Q_C*V_F
- 100% Avalanche (UIL) Tested
- Enhanced Surge Current Withstand Capability
- Temperature Independent Fast Switching
- Low Thermal Resistance
- Positive Temperature Coefficient of V_F
- High dV/dt Ruggedness

Package

Case (K)

GD30MPS12J

NC   A

TO-263-7

Case

K   A

RoHS

REACH
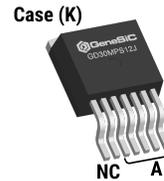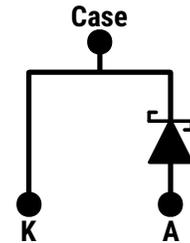
Advantages

- Improved System Efficiency
- High System Reliability
- Optimal Price Performance
- Reduced Cooling Requirements
- Increased System Power Density
- Zero Reverse Recovery Current
- Easy to Parallel without Thermal Runaway
- Enables Extremely Fast Switching

Applications

- Power Factor Correction (PFC)
- Electric Vehicles and Battery Chargers
- Solar Inverters
- High Frequency Converters
- Switched Mode Power Supply (SMPS)
- Motor Drives
- Anti-Parallel / Free-Wheeling Diode
- Induction Heating & Welding

Absolute Maximum Ratings (At T_C = 25°C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Repetitive Peak Reverse Voltage	V_{RRM}		1200	V	
Continuous Forward Current	I_F	T_C = 100°C, D = 1	59	A	Fig. 4
		T_C = 135°C, D = 1	41		
		T_C = 150°C, D = 1	30		
Non-Repetitive Peak Forward Surge Current, Half Sine Wave	$I_{F,SM}$	T_C = 25°C, t_P = 10 ms	240	A	
		T_C = 150°C, t_P = 10 ms	192		
Repetitive Peak Forward Surge Current, Half Sine Wave	$I_{F,RM}$	T_C = 25°C, t_P = 10 ms	144	A	
		T_C = 150°C, t_P = 10 ms	100		
Non-Repetitive Peak Forward Surge Current	$I_{F,MAX}$	T_C = 25°C, t_P = 10 µs	1200	A	
i²t Value	$\int i^2 dt$	T_C = 25°C, t_P = 10 ms	288	A²s	
Non-Repetitive Avalanche Energy	E_{AS}	L = 0.6 mH, I_{AS} = 30 A	271	mJ	
Diode Ruggedness	dV/dt	V_R = 0 ~ 960 V	200	V/ns	
Power Dissipation	P_{TOT}	T_C = 25°C	350	W	Fig. 3
Operating and Storage Temperature	T_j , T_{stg}		-55 to 175	°C	

Electrical Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
Diode Forward Voltage	V_F	I_F = 30 A, T_j = 25°C		1.5	1.8	V	Fig. 1
		I_F = 30 A, T_j = 175°C		1.9			
Reverse Current	I_R	V_R = 1200 V, T_j = 25°C		2	20	µA	Fig. 2
		V_R = 1200 V, T_j = 175°C		20			
Total Capacitive Charge	Q_C	V_R = 400 V		67		nC	Fig. 7
		V_R = 800 V		97			
Switching Time	t_S	$I_F \leq I_{F,MAX}$ dI_F/dt = 200 A/µs V_R = 400 V		< 10		ns	
		V_R = 800 V					
Total Capacitance	C	V_R = 1 V, f = 1MHz		1101		pF	Fig. 6
		V_R = 800 V, f = 1MHz		64			

Thermal/Package Characteristics

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit	Note
Thermal Resistance, Junction - Case	R_{thJC}			0.43		°C/W	Fig. 9
Weight	W_T			1.45		g	

GeneSiC™ SEMICONDUCTOR

Figure 1: Typical Forward Characteristics

Forward Current, I_F (A) vs Forward Voltage, V_F (V), with curves at 175°C, 150°C, 125°C, 75°C, 25°C
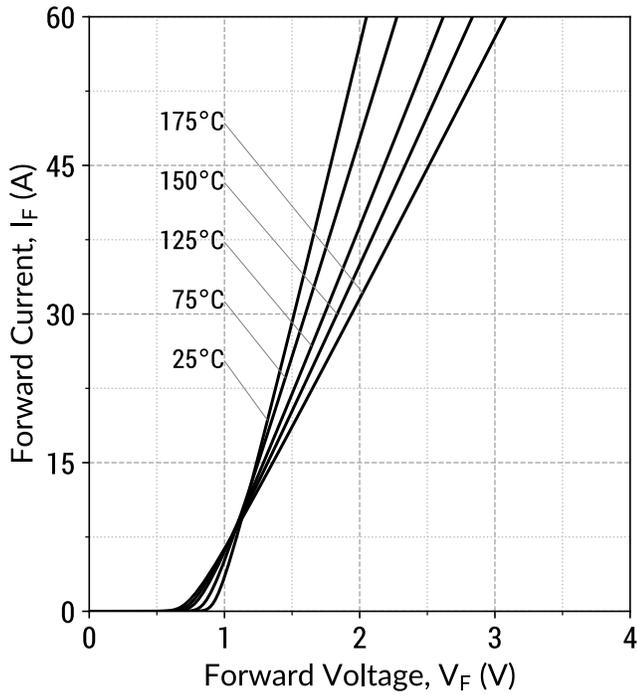
$I_F = f(V_F, T_j); t_P = 250\ \mu s$

Figure 2: Typical Reverse Characteristics

Reverse Current, I_R (A) vs Reverse Voltage, V_R (V), with curves at 175°C, 150°C, 125°C, 75°C, 25°C
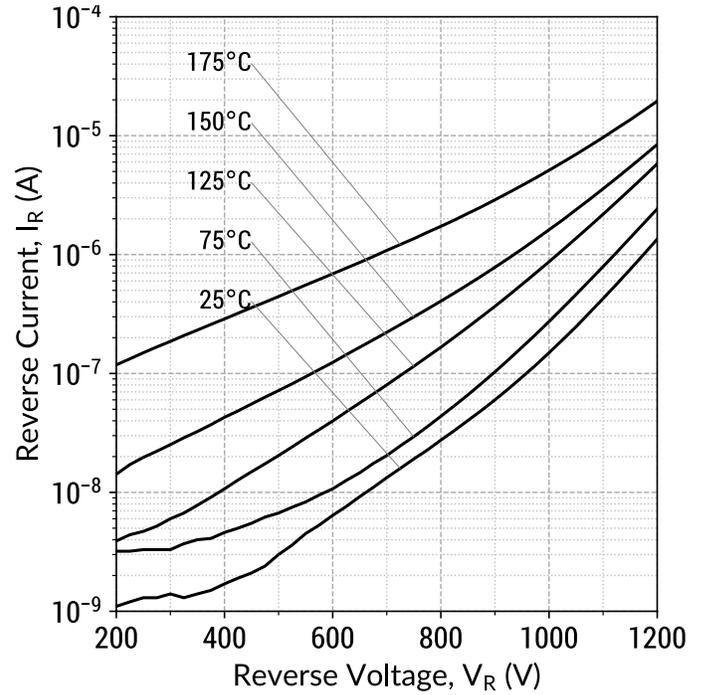
$I_R = f(V_R, T_j)$

Figure 3: Power Derating Curves

Power Dissipated (W) vs Case Temperature, T_C (°C)
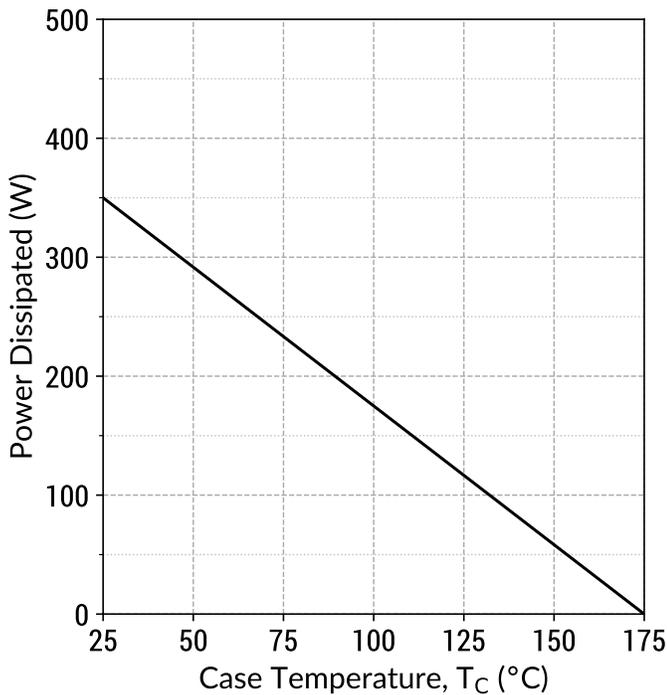
$P_{TOT} = f(T_C); T_j = 175°C$

Figure 4: Current Derating Curves (Typical V_F)

Current Rating (A) vs Case Temperature, T_C (°C), with curves D = 0.1, D = 0.2, D = 0.3, D = 0.5, D = 0.7, D = 1
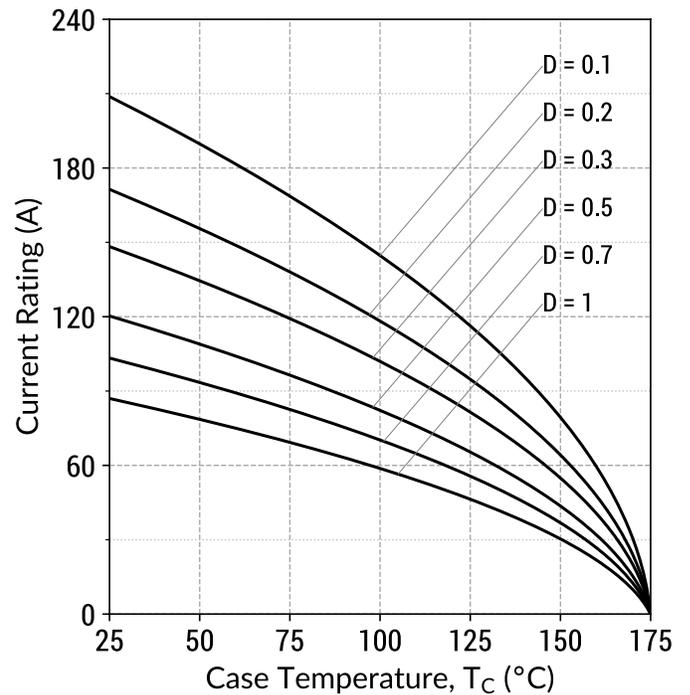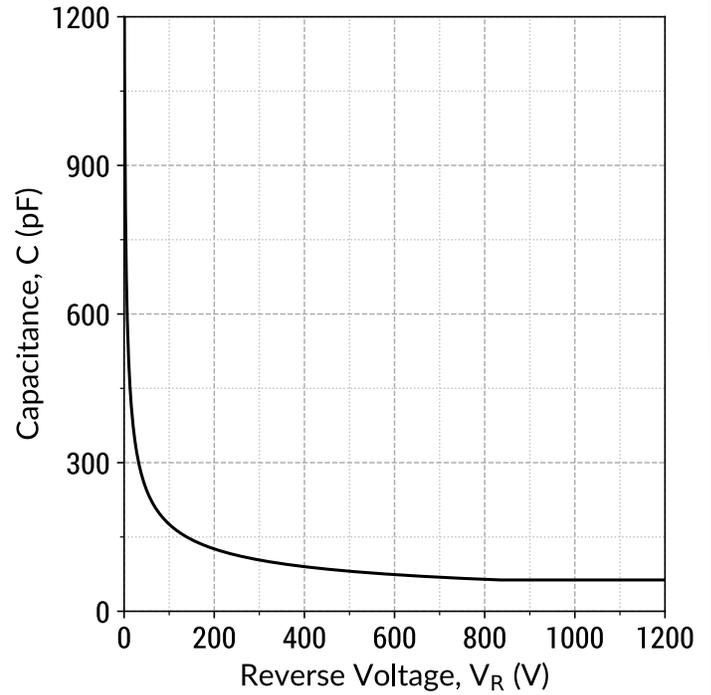
$I_F = f(T_C); D = t_P/T; T_j \le 175°C; f_{SW} > 10kHz$

Figure 5: Current Derating Curves (Maximum V_F)
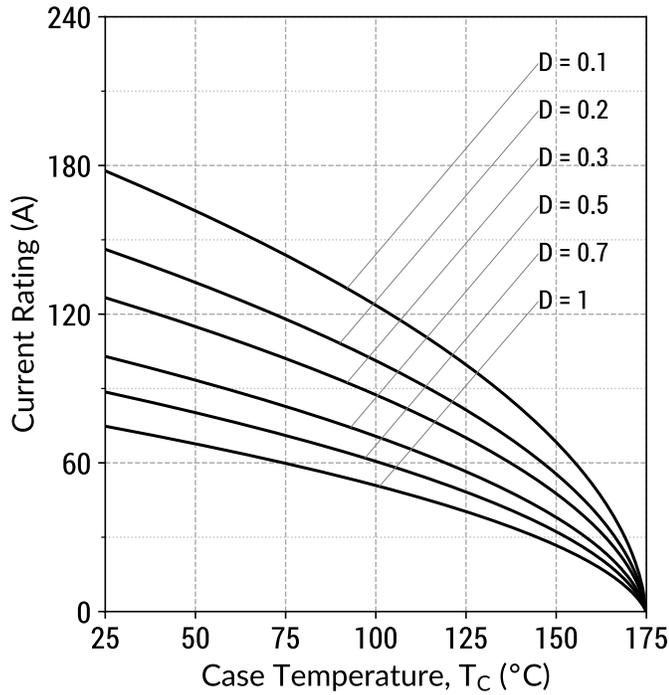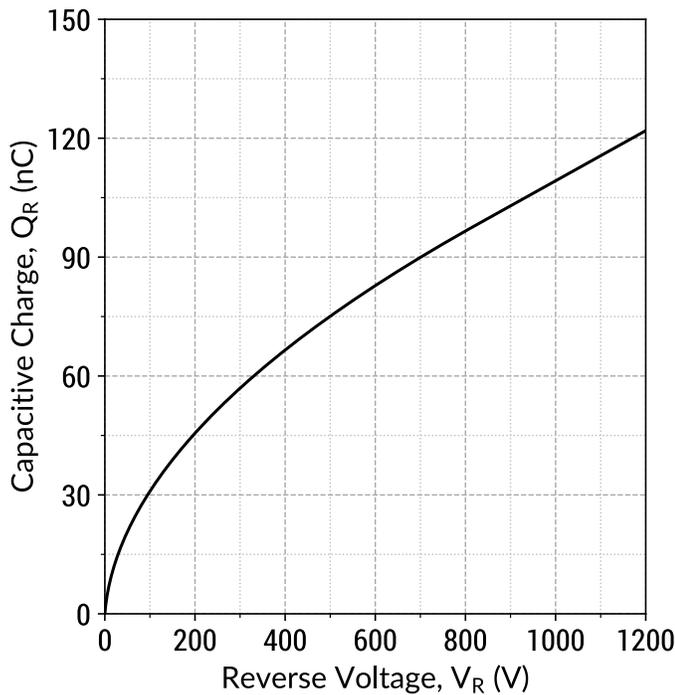

$I_F = f(T_C); D = t_P/T; T_j \leq 175°C; f_{SW} > 10kHz$

Figure 6: Typical Junction Capacitance vs Reverse Voltage Characteristics
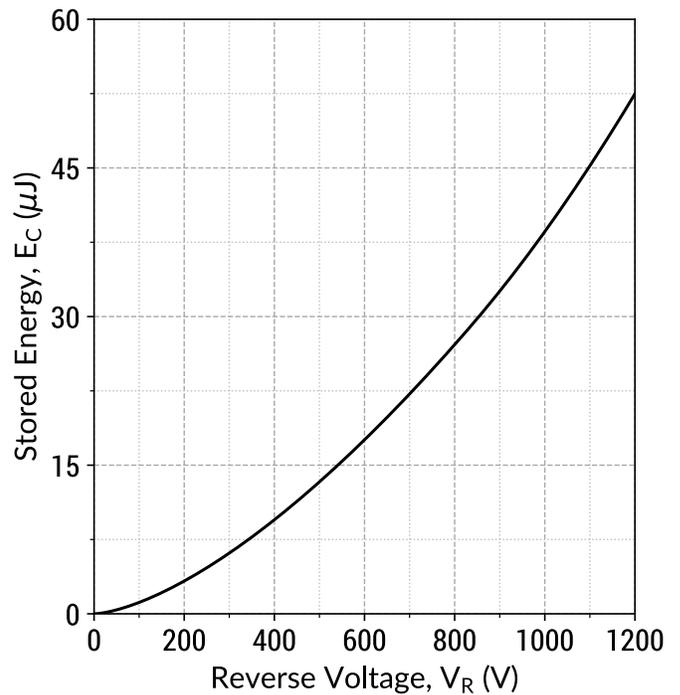

$C = f(V_R); f = 1MHz$

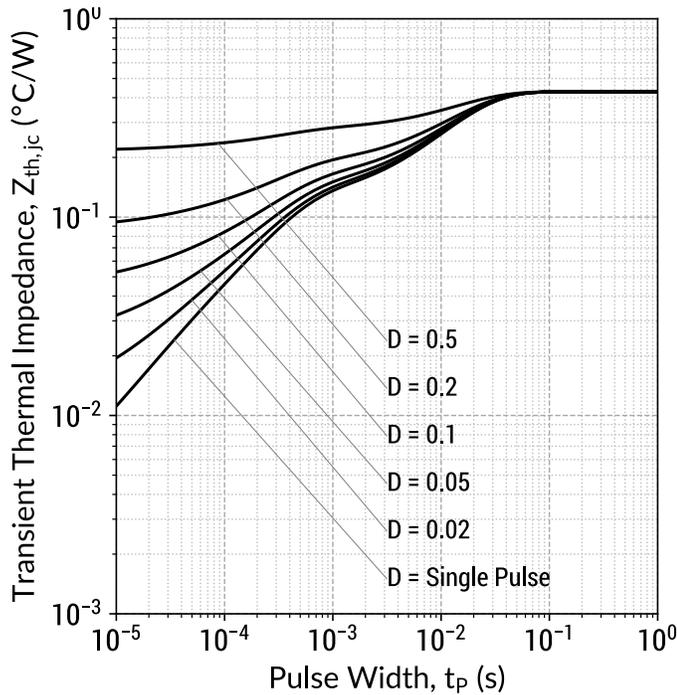## Figure 7: Typical Capacitive Charge vs Reverse Voltage Characteristics

$Q_C = f(V_R); f = 1MHz$

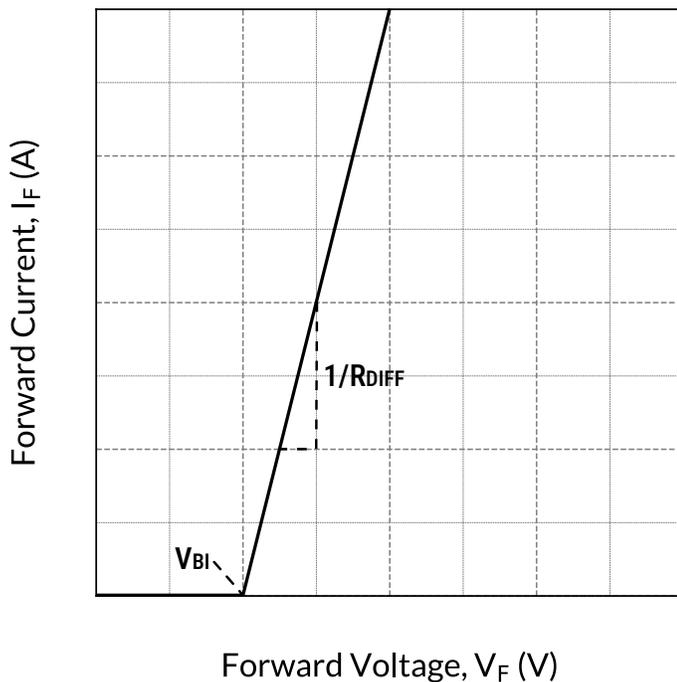## Figure 8: Typical Capacitive Energy vs Reverse Voltage Characteristics

$E_C = f(V_R); f = 1MHz$

Figure 9: Transient Thermal Impedance

Transient Thermal Impedance, $Z_{th,jc}$ (°C/W) vs Pulse Width, t_P (s)

D = 0.5
D = 0.2
D = 0.1
D = 0.05
D = 0.02
D = Single Pulse

$Z_{th,jc} = f(t_P,D); D = t_P/T$

Figure 10: Forward Curve Model

Forward Current, I_F (A) vs Forward Voltage, V_F (V)

$1/R_{DIFF}$

V_{BI}

$I_F = f(V_F,T_j)$

Forward Curve Model Equation:

$I_F = (V_F - V_{BI})/R_{DIFF}$ (A)

Built-In Voltage (V_{BI}):

$V_{BI}(T_j) = m \times T_j + n$ (V)

m = -0.00119 (V/°C)

n = 1.01 (V)

Differential Resistance (R_{DIFF}):

$R_{DIFF}(T_j) = a \times T_j^2 + b \times T_j + c$ (Ω)

a = 3.97e-07 (Ω/°C^2)

b = 5.5e-05 (Ω/°C)
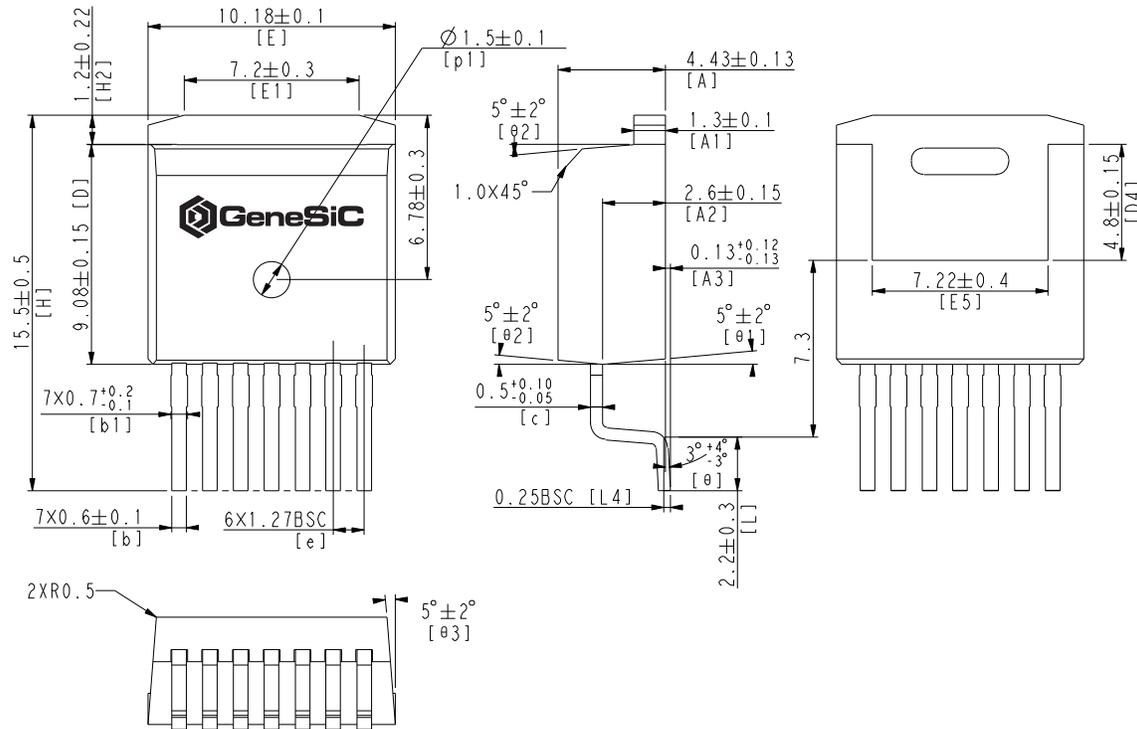
c = 0.0163 (Ω)

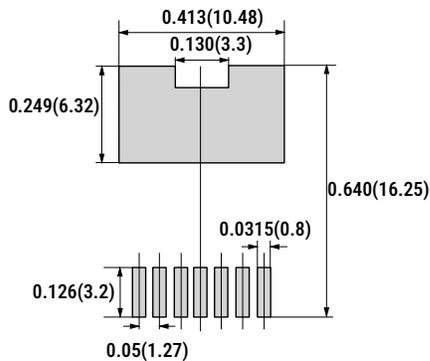### Forward Power Loss Equation:

$P_{LOSS} = V_{BI}(T_j) \times I_{AVG} + R_{DIFF}(T_j) \times I_{RMS}^2$

Package Dimensions

TO-263-7 Package Outline
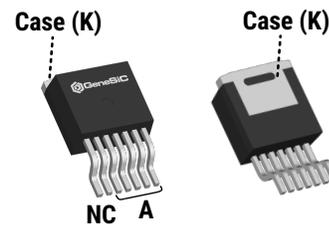

Recommended Solder Pad Layout

Package View

Case (K) Case (K)

NC A

NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.

Compliance

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

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Related Links

- SPICE Models: https://www.genesicsemi.com/sic-schottky-mps/GD30MPS12J/GD30MPS12J_SPICE.zip
- PLECS Models: https://www.genesicsemi.com/sic-schottky-mps/GD30MPS12J/GD30MPS12J_PLECS.zip
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- Evaluation Boards: https://www.genesicsemi.com/technical-support
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- Compliance: https://www.genesicsemi.com/compliance
- Quality Manual: https://www.genesicsemi.com/quality

Revision History

- Rev 21/Jul: Initial Release

www.genesicsemi.com/sic-schottky-mps/

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