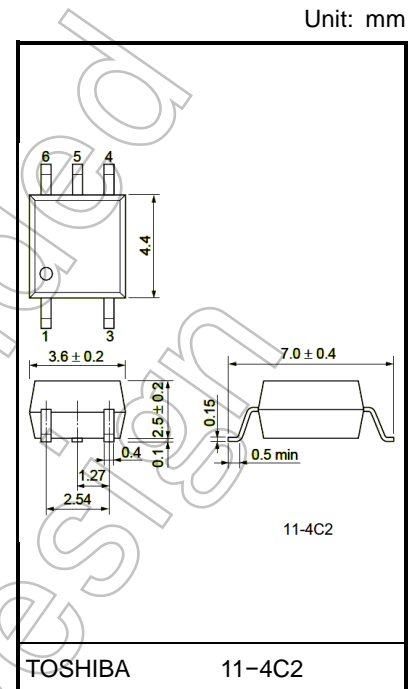


TLP148G

Office Machine
Household Use Equipment
Solid State Relay
Switching Power Supply

The TOSHIBA mini-flat coupler TLP148G is a small outline coupler, suitable for surface mount assembly.
The TLP148G consists of a photo thyristor, optically coupled to an infrared emitting diode.

- Peak off-state voltage: 400 V (min)
- Trigger LED current: 10 mA (max)
- On-state current: 150 mA (max)
- Isolation voltage: 2500 Vrms (min)
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A
File No.E67349



Weight: 0.09 g (typ.)

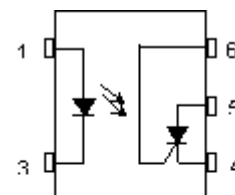
Trigger LED current

Classification*	Trigger LED current (mA)		Marking of classification
	V _{AK} =6V, R _{GK} =27kΩ, Ta=25°C		
	Min	Max	
(IFT7)	—	7	T7
Standard	—	10	T7, blank

*Example: “(IFT7)”; “TLP148G(IFT7)”

(Note) When specifying the application type name for certification testing, be sure to use the standard product type name, e.g. TLP148G(IFT7); TLP148G

Pin Connections



- 1 : Anode
- 3 : Cathode
- 4 : Cathode
- 5 : Anode.
- 6 : Gate

Start of commercial production
2009-06

● **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit
LED	Forward current	IF	50	mA
	Forward current derating (Ta ≥ 53°C)	ΔIF/°C	-0.7	mA / °C
	Peak forward current (100 μs pulse, 100 pps)	IFP	1	A
	Reverse voltage	VR	5	V
	Diode power dissipation	PD	100	mW
	Diode power dissipation derating (Ta ≥ 53°C)	ΔPD/°C	-1.4	mW/°C
Detector	Peak forward voltage(RGK = 27kΩ)	VDRM	400	V
	Peak reverse voltage(RGK = 27kΩ)	VRRM	400	V
	On-state current	IT(RMS)	150	mA
	On-state current derating (Ta ≥ 25°C)	ΔIT / °C	-2.0	mA / °C
	Peak on-state current (100 μs pulse, 120 pps)	ITP	3	A
	Peak one cycle surge current	ITSM	2	A
	Peak reverse gate voltage	VGM	5	V
	Output power dissipation	PO	150	mW
	Output power dissipation derating (Ta ≥ 25°C)	ΔPO/°C	-1.5	mW / °C
Operating temperature range		Topr	-40 to 100	°C
Storage temperature range		Tstg	-55 to 125	°C
Lead soldering temperature (10 s)		Tsol	260	°C
Isolation voltage (AC, 60 s, R.H. ≤ 60 %) (Note 1)		BVS	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

(Note 1) Device considered a two terminal device: pins 1 and 3 shorted together and pins 4, 5 and 6 shorted together.

Recommended Operating Conditions

Characteristics	Symbol	Min	Typ.	Max	Unit
Supply voltage	VAC	—	—	120	Vac
Forward current	IF	15	—	25	mA
Operating temperature	Topr	-25	—	85	°C
Gate to cathode resistance	RGK	—	27	33	kΩ
Gate to cathode capacitance	CGK	—	0.01	0.1	μF

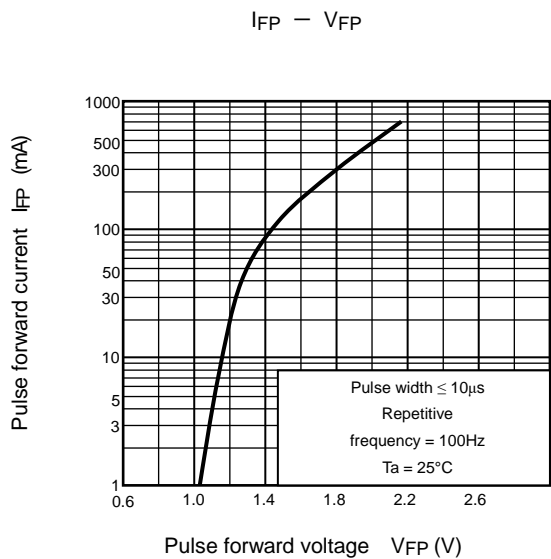
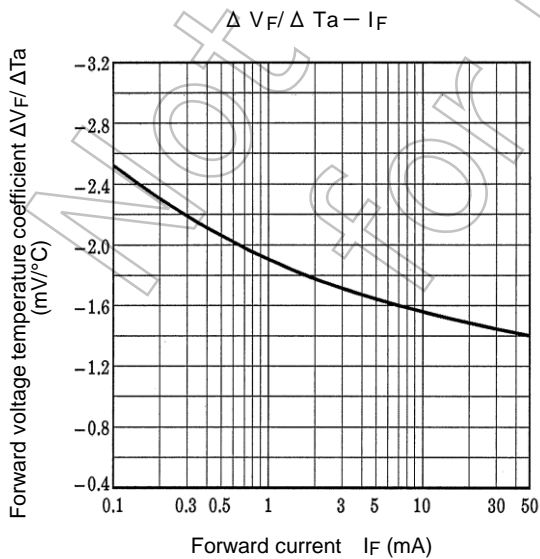
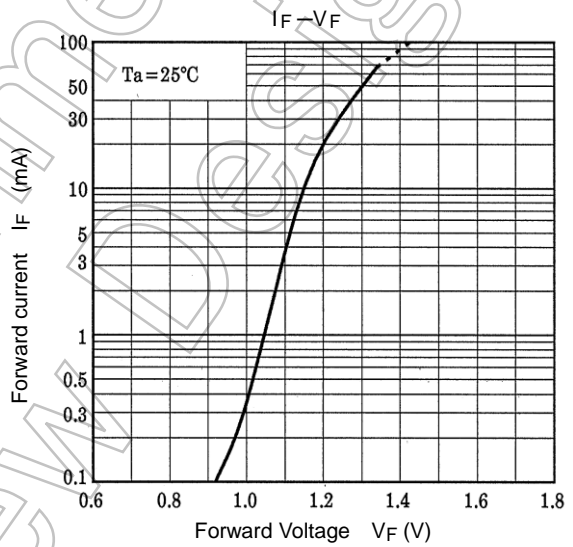
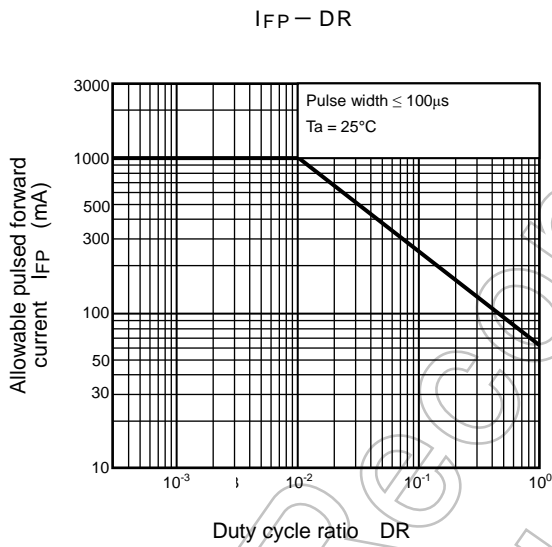
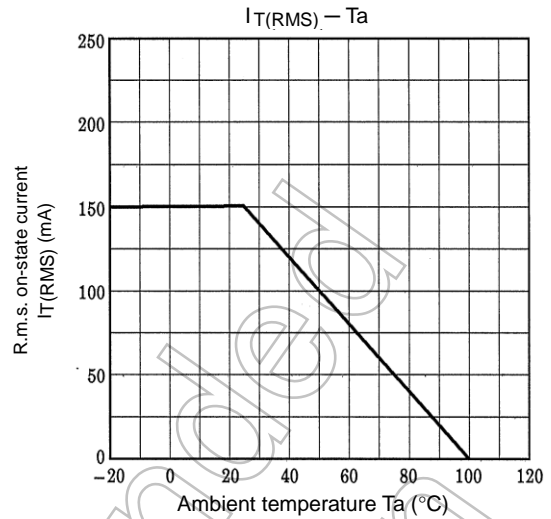
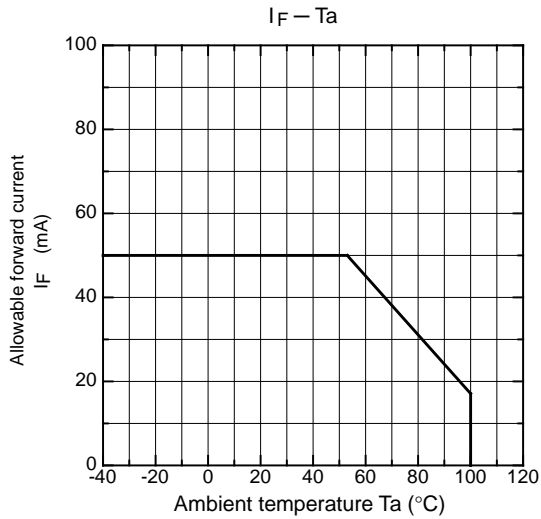
Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

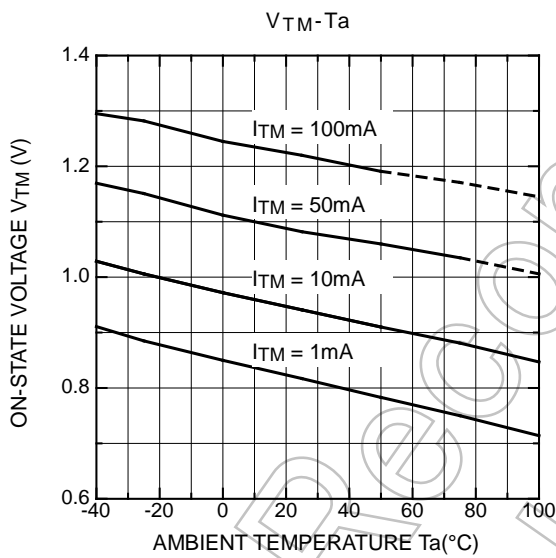
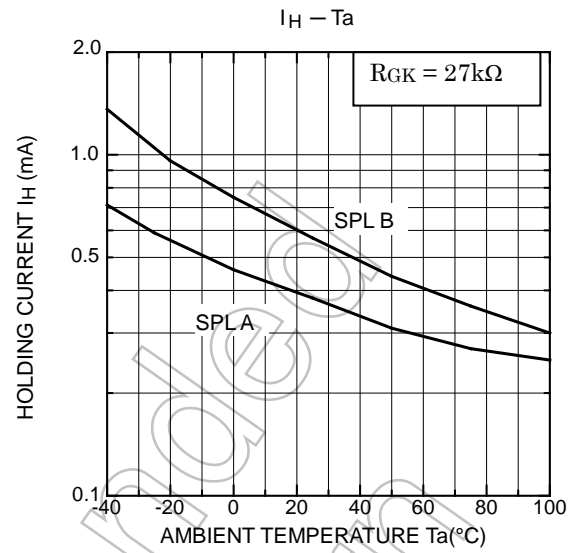
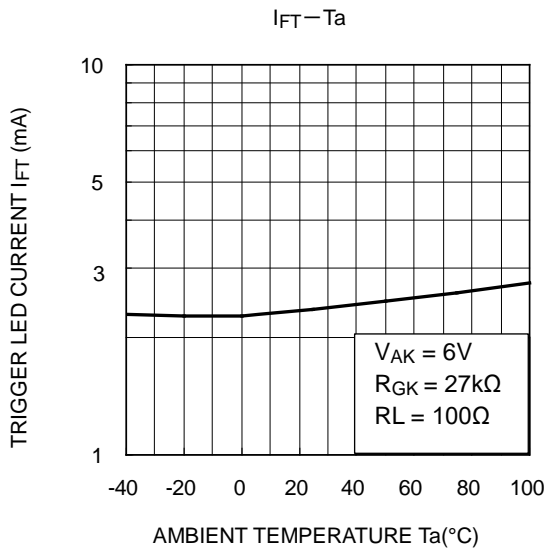
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V_F	$I_F = 10 \text{ mA}$	1.0	1.15	1.3	V
	Reverse current	I_R	$V_R = 5 \text{ V}$	—	—	10	μA
	Capacitance	C_T	$V_F = 0 \text{ V}, f = 1 \text{ MHz}$	—	30	—	pF
Detector	Off-state current	I_{DRM}	$V_{AK} = 400 \text{ V}, R_{GK} = 27 \text{ k}\Omega$	—	—	5	μA
	Reverse current	I_{RRM}	$V_{KA} = 400 \text{ V}, R_{GK} = 27 \text{ k}\Omega$	—	—	5	μA
	On-state voltage	V_{TM}	$I_{TM} = 100 \text{ mA}, I_F = 10 \text{ mA}$	—	1.25	1.45	V
	Holding current	I_H	$R_{GK} = 27 \text{ k}\Omega$	—	—	1	mA
	Off-state dv / dt	dv/dt	$V_{AK} = 280 \text{ V}, R_{GK} = 27 \text{ k}\Omega$	15	—	—	V / μs
	Capacitance	C_j	$V = 0 \text{ V}, f = 1 \text{ MHz}$	Anode to gate	—	5	—
	Gate to cathode			—	500	—	

Coupled Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Trigger LED current	I_{FT}	$V_{AK} = 6 \text{ V}, R_{GK} = 27 \text{ k}\Omega$	—	3	10	mA
Turn-on time	t_{ON}	$I_F = 30 \text{ mA}, V_{AA} = 50 \text{ V}, R_{GK} = 27 \text{ k}\Omega$	—	10	—	μs
Capacitance (input to output)	C_S	$V_S = 0 \text{ V}, f = 1 \text{ MHz}$	—	0.8	—	pF
Isolation resistance	R_S	$V_S = 500 \text{ V}, R.H. \leq 60 \%$	5×10^{10}	10^{14}	—	Ω
Isolation voltage	BV_S	AC, 60 s	2500	—	—	Vrms



NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



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