

# **High Performance Schottky Rectifier, 400 A**



PRODUCT SUMMARY					
I <sub>F(AV)</sub>	400 A				
V <sub>R</sub>	100 V				
Package	TO-244				
Circuit	Two diodes common cathode				

### **FEATURES**

- 175 °C T<sub>J</sub> operation
- · Center tap module
- · Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

### **DESCRIPTION**

The VS-403CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	400	A			
V <sub>RRM</sub>		100	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	25 500	A			
V <sub>F</sub>	200 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.69	V			
TJ	Range	-55 to 175	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-403CNQ100PbF	UNITS			
Maximum DC reverse voltage	$V_{R}$	100	V			
Maximum working peak reverse voltage	$V_{RWM}$	100	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS		
Maximum average forward	per leg		50.0% - 1 1 1 1 1 1 1 1		50 % d d a d d T = 444 %		
current See fig. 5	per device	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 141 °C, rectangular waveform		400	- A	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7			5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	25 500		
		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	3300		
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 13 A, L = 0.2 mH		15	mJ	
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1	Α	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS VAI			UNITS
		200 A	T <sub>J</sub> = 25 °C	0.84	V
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	400 A	1J=25 C	1.07	
See fig. 1		200 A	T T mayimum	0.69	
		400 A	$T_J = T_J$ maximum	0.82	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	6	mA
See fig. 2		T <sub>J</sub> = 125 °C	v <sub>R</sub> = nateu v <sub>R</sub>	80	IIIA
Maximum junction capacitance per leg	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		5500	pF
Typical series inductance per leg	L <sub>S</sub>	From top of terminal hole to mounting plane 5.0		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V		V/µs	

### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>	-55	-	175	°C	
Thermal resistance, junction to case per leg		-	-	0.19		
Thermal resistance, junction to case per module	- R <sub>thJC</sub>	-	-	0.095	°C/W	
Thermal resistance, case to heatsink	R <sub>thCS</sub>	-	0.10	-		
Wester		-	68	-	g	
Weight		-	2.4	-	OZ.	
Mounting torque		35.4 (4)		53.1 (6)		
Mounting torque center hole		30 (3.4)		40 (4.6)	lbf ⋅ in (N ⋅ m)	
Terminal torque		30 (3.4)	-	44.2 (5)	(1.7 11)	
Vertical pull		-	-	80	llef in	
2" lever pull		-	-	35	– lbf · in	

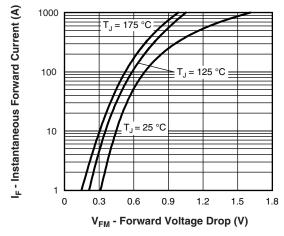


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

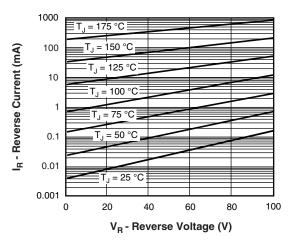


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

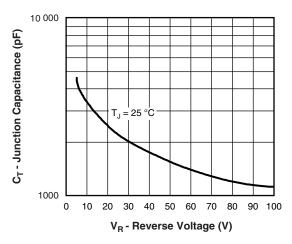


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

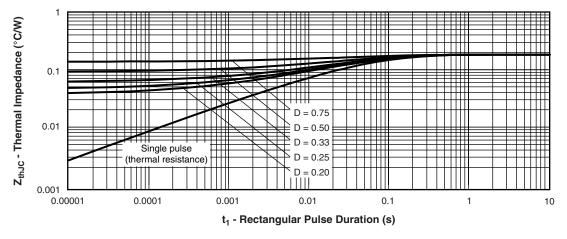


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

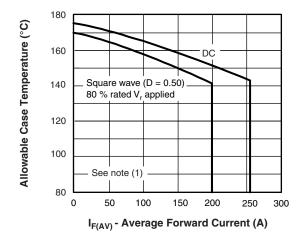


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

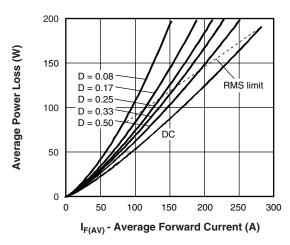
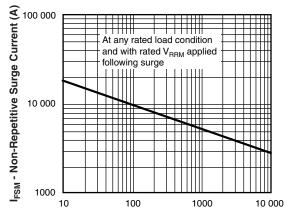


Fig. 6 - Forward Power Loss Characteristics (Per Leg)



t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

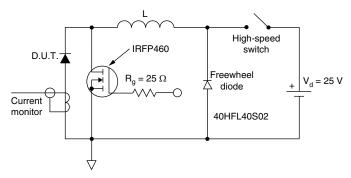


Fig. 8 - Unclamped Inductive Test Circuit

### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$ 

### **ORDERING INFORMATION TABLE**

Device code	vs-	40	3	С	N	Q	100	PbF
	1	2	3	4	5	6	7	8
	1 - 2 - 3 - 4 -	Average current rating (x 10) Product silicon identification						
	5 -							
	6 -	- Q = Schottky rectifier diode						
	7 -	Volt	Voltage rating (100 = 100 V)					
	8 -	- Lead (Pb)-free						

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			



### **TO-244**

### **DIMENSIONS** in millimeters (inches)









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