



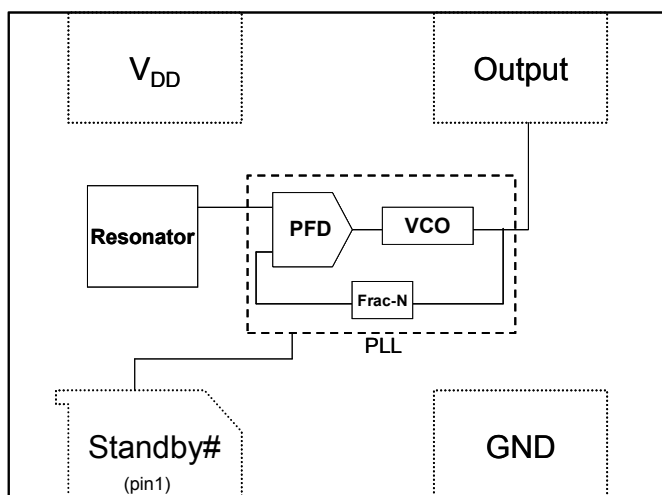
## General Description

The DSC1003 is a silicon MEMS based CMOS oscillator offering excellent jitter and stability performance over a wide range of supply voltages and temperatures. The device operates from 1 to 150MHz with supply voltages between 1.8 to 3.3 Volts and extended temperatures from -40°C to 105°C. The DSC1003 has the same functionality and performance as the DSC1001 but with greater output drive ( $C_L < 25\text{pf}$ ).

The DSC1003 incorporates an all silicon resonator that is extremely robust and nearly immune to stress related fractures, common to crystal based oscillators. Without sacrificing the performance and stability required of today's systems, a crystal-less design allows for a higher level of reliability, making the DSC1003 ideal for rugged, industrial, and portable applications where stress, shock, and vibration can damage quartz crystal based systems.

Available in industry standard packages, the DSC1003 can be "dropped-in" to the same PCB footprint as standard crystal oscillators.

## Block Diagram



## Features

- Frequency Range: 1 to 150MHz
- Exceptional Stability over Temperature
  - $\pm 10$  PPM,  $\pm 25$  PPM,  $\pm 50$  PPM
- Operating voltage
  - 1.7 to 3.6V
- Operating Temperature Range
  - Ext. Industrial -40°C to 105°C
  - Industrial -40°C to 85°C
  - Ext. Commercial -20°C to 70°C
  - Commercial 0°C to 70°C
- Low Operating and Standby Current
  - 6mA Operating (40MHz)
  - 15uA Standby
- Ultra Miniature Footprint
  - 2.5 x 2.0 x 0.85 mm
  - 3.2 x 2.5 x 0.85 mm
  - 5.0 x 3.2 x 0.85 mm
  - 7.0 x 5.0 x 0.85 mm
- Excellent Shock and Vibration Resistance
- Lead Free, RoHS & Reach SVHC Compliant

## Benefits

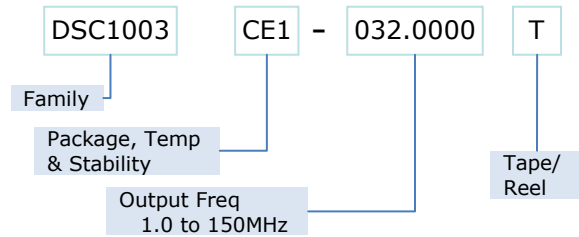
- Pin for pin "drop in" replacement for industry standard oscillators
- Semiconductor level reliability, significantly higher than quartz
- Short mass production lead-times
- Longer Battery Life / Reduced Power
- Compact Plastic package
- Cost Effective

## Applications

- Mobile Applications
- Consumer Electronics
- Portable Electronics
- CCD Clock for VTR Cameras
- Low Profile Applications
- Industrial

## Absolute Maximum Ratings<sup>1</sup>

Item	Min.	Max	Unit	Condition
<b>Input Voltage</b>	-0.3	VDD+0.3	V	
<b>Junction Temp</b>	-	+150	°C	
<b>Storage Temp</b>	-55	+150	°C	
<b>Soldering Temp</b>	-	+260	°C	40 sec max.
<b>ESD</b>	-		V	
<b>HBM</b>		4000		
<b>MM</b>		200		
<b>CDM</b>		1500		



\* See Ordering Information for details

## Ordering Code Recommended Operating Conditions

Parameter	Symbol	Range
<b>Supply Voltage</b>	V <sub>DD</sub>	1.7 – 3.6V
<b>Output Load</b>	Z <sub>L</sub>	R>10KΩ, C≤25pF
<b>Operating Temperature</b>		
<b>Option 1</b>		-40 to +105 °C
<b>Option 2</b>	T	-40 to +85 °C
<b>Option 3</b>		-20 to +70 °C
<b>Option 4</b>		0 to +70 °C

## Specifications (V<sub>DD</sub> = 1.8 to 3.3v) T<sub>A</sub>=85°C unless otherwise specified

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Frequency</b>	f <sub>0</sub>	Single Frequency	1		150	MHz
<b>Frequency Tolerance</b>	Δf	Includes frequency variations due to initial tolerance, temperature and power supply voltage			±10,±25,±50	ppm
<b>Aging</b>	Δf	1 year @25°C			±5	ppm
<b>Supply Current, standby</b>	I <sub>DD</sub>	T=25°C			15	uA
<b>Output Startup Time<sup>2</sup></b>	t <sub>SU</sub>	T=25°C		1.0	1.3	ms
<b>Output Disable Time</b>	t <sub>DA</sub>			20	100	ns
<b>Output Duty Cycle</b>	SYM		45		55	%
<b>Input Logic Levels</b>						
<b>Input logic high</b>	V <sub>IH</sub>		0.75*V <sub>DD</sub>		-	Volts
<b>Input logic low</b>	V <sub>IL</sub>		-		0.25* V <sub>DD</sub>	

### Notes:

1. Absolute maximum ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated beyond these limits.
2. t<sub>SU</sub> is time to stable output frequency after V<sub>DD</sub> is applied. t<sub>SU</sub> and t<sub>EN</sub> (after EN is asserted) are identical values.
3. Measured over 50k clock cycles.

### VDD = 1.8v

Parameter	Symbol	Condition		Min	Typ	Max	Unit
Supply Current, no load	$I_{DD}$	$C_L=0p$ $R_L=\infty$ $T=25^\circ C$	1MHz 27MHz 70MHz 150MHz		5.7 6.4 7.7 10.0	6.0 6.8 8.0 11.0	mA
Output Logic Levels Output logic high Output logic low	$V_{OH}$ $V_{OL}$		-6mA 6mA	$0.8*V_{DD}$ -		- $0.2*V_{DD}$	Volts
Output Transition time Rise Time Fall Time	$t_R$ $t_F$		$C_L=25pF$ ; $T=25^\circ C$ 20%/80%* $V_{DD}$		1.5 1.2	3 3	ns
Output Transition time Rise Time Fall Time	$t_R$ $t_F$		$C_L=25pF$ ; $T=25^\circ C$ 10%/90%* $V_{DD}$		2.6 1.9	4 4	ns
Period Jitter	$J_p$		$F = 100MHz^3$		10	15	ps rms

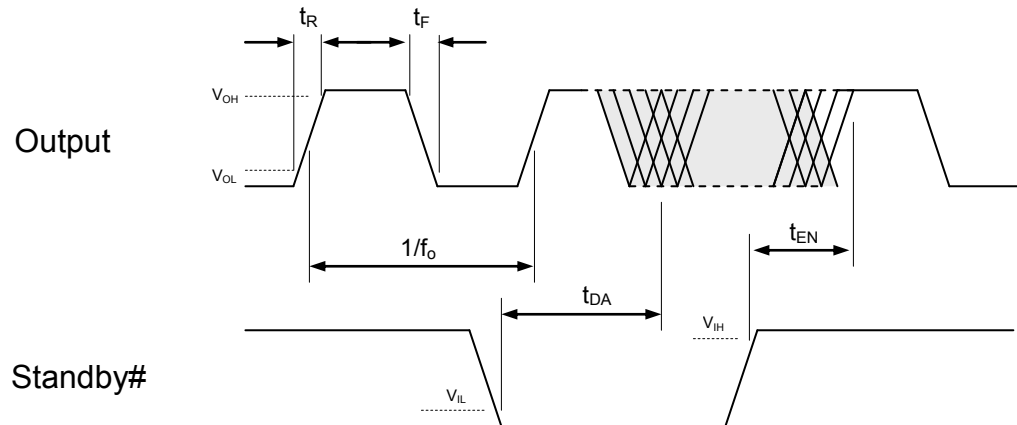
### VDD = 2.5v

Parameter	Symbol	Condition		Min	Typ	Max	Unit
Supply Current, no load	$I_{DD}$	$C_L=0p$ $R_L=\infty$ $T=25^\circ C$	1MHz 27MHz 70MHz 150MHz		5.7 6.7 8.4 11.4	6.0 7.1 8.8 12.7	mA
Output Logic Levels Output logic high Output logic low	$V_{OH}$ $V_{OL}$		-6mA 6mA	$0.8*V_{DD}$ -		- $0.2*V_{DD}$	Volts
Output Transition time Rise Time Fall Time	$t_R$ $t_F$		$C_L=25pF$ ; $T=25^\circ C$ 20%/80%* $V_{DD}$		1.1 0.9	2 2	ns
Output Transition time Rise Time Fall Time	$t_R$ $t_F$		$C_L=25pF$ ; $T=25^\circ C$ 10%/90%* $V_{DD}$		1.9 1.5	3.5 3	ns
Period Jitter	$J_p$		$F = 100MHz^3$		5	10	ps rms

### VDD = 3.3v

Parameter	Symbol	Condition		Min.	Typ.	Max.	Unit
Supply Current, no load	$I_{DD}$	$C_L=0p$ $R_L=\infty$ $T=25^\circ C$	1MHz 27MHz 70MHz 150MHz		5.7 7.0 9.1 13.1	6.0 7.4 9.6 15.0	mA
Output Logic Levels Output logic high Output logic low	$V_{OH}$ $V_{OL}$		-6mA 6mA	$0.9*V_{DD}$ -		- $0.1*V_{DD}$	Volts
Output Transition time Rise Time Fall Time	$t_R$ $t_F$		$C_L=25pF$ ; $T=25^\circ C$ 20%/80%* $V_{DD}$		1.1 0.9	2 2	ns
Output Transition time Rise Time Fall Time	$t_R$ $t_F$		$C_L=25pF$ ; $T=25^\circ C$ 10%/90%* $V_{DD}$		1.5 1.5	3 3	ns
Period Jitter	$J_p$		$F = 100MHz^3$		5	10	ps rms

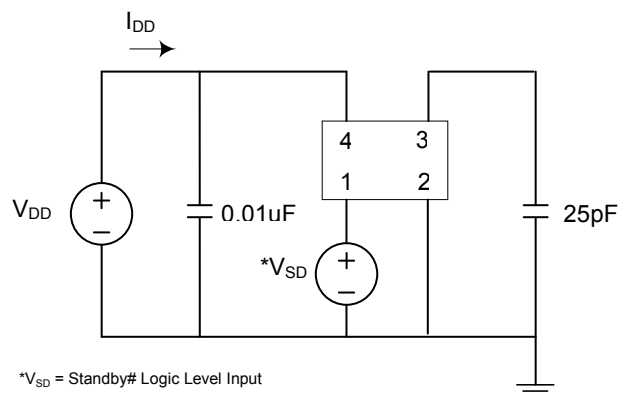
## Output Waveform



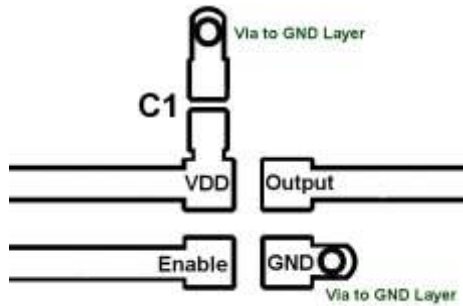
## Standby Function

Standby# (pin 1)	Output (pin 3)
Hi Level	Output ON
Open (no connect)	Output ON
Low Level	High Impedance

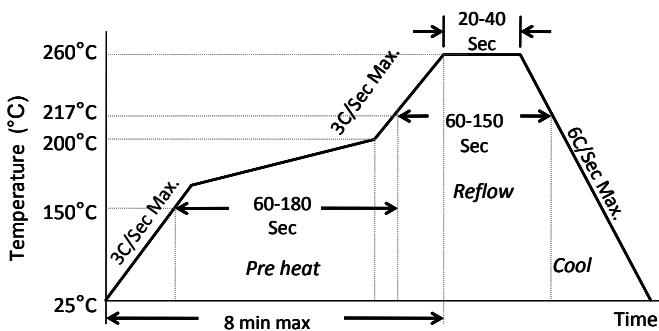
## Test Circuit



### Board Layout (recommended)



### Solder Reflow Profile



MSL 1 @ 260°C refer to JSTD-020C	
Ramp-Up Rate (200°C to Peak Temp)	3°C/Sec Max.
Preheat Time 150°C to 200°C	60-180 Sec
Time maintained above 217°C	60-150 Sec
Peak Temperature	255-260°C
Time within 5°C of actual Peak	20-40 Sec
Ramp-Down Rate	6°C/Sec Max.
Time 25°C to Peak Temperature	8 min Max.

### Package Dimensions

#### 7.0 x 5.0 mm Plastic Package

#### External Dimensions

7.0±0.10 [0.276±0.004]

5.0±0.10 [0.197±0.004]

2.6 [0.102]

3.5 [0.138]

2.2 [0.087]

1.4 [0.055]

0.2 [0.008]

1.2 [0.047]

0.85±0.05 [0.033±0.002]

#### Recommended Land Pattern\*

5.08 [0.200]

2.6 [0.102]

0.2 [0.008]

1.4 [0.055]

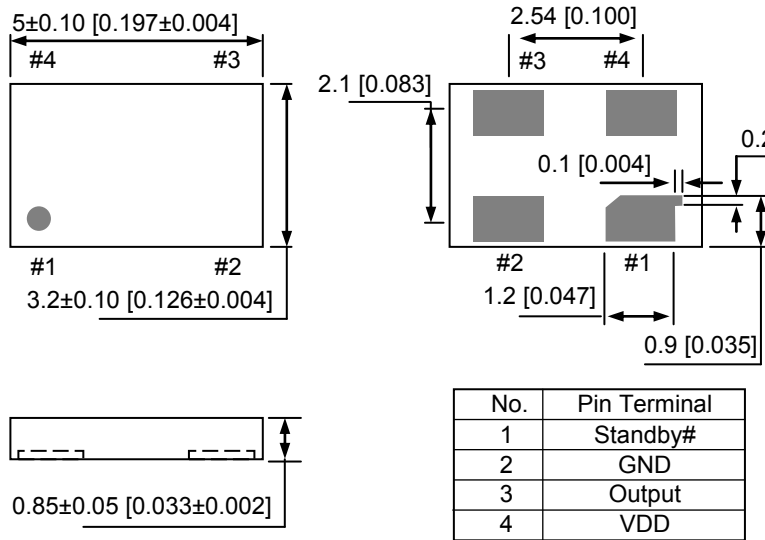
No.	Pin Terminal
1	Standby#
2	GND
3	Output
4	VDD

units: mm [inch]

\*Note: The center pad is not connected internally and should be left unconnected or tied to GND.

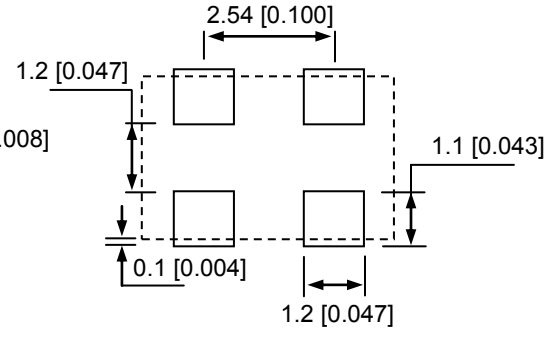
### 5.0 x 3.2 mm Plastic Package

#### External Dimensions



No.	Pin Terminal
1	Standby#
2	GND
3	Output
4	VDD

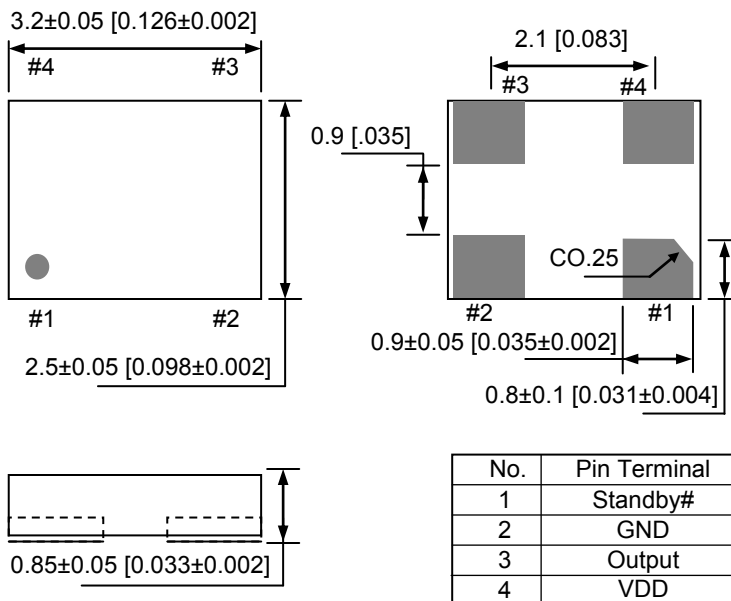
#### Recommended Land Pattern



units: mm [inch]

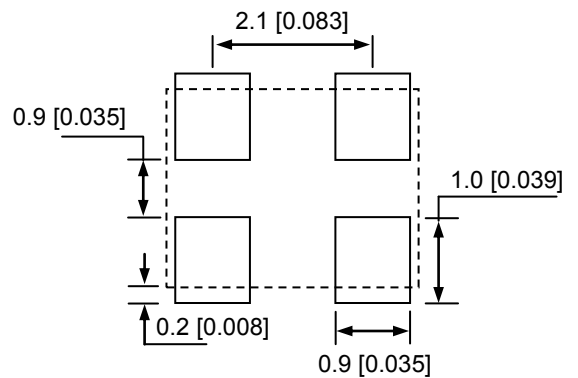
### 3.2 x 2.5 mm Plastic Package

#### External Dimensions



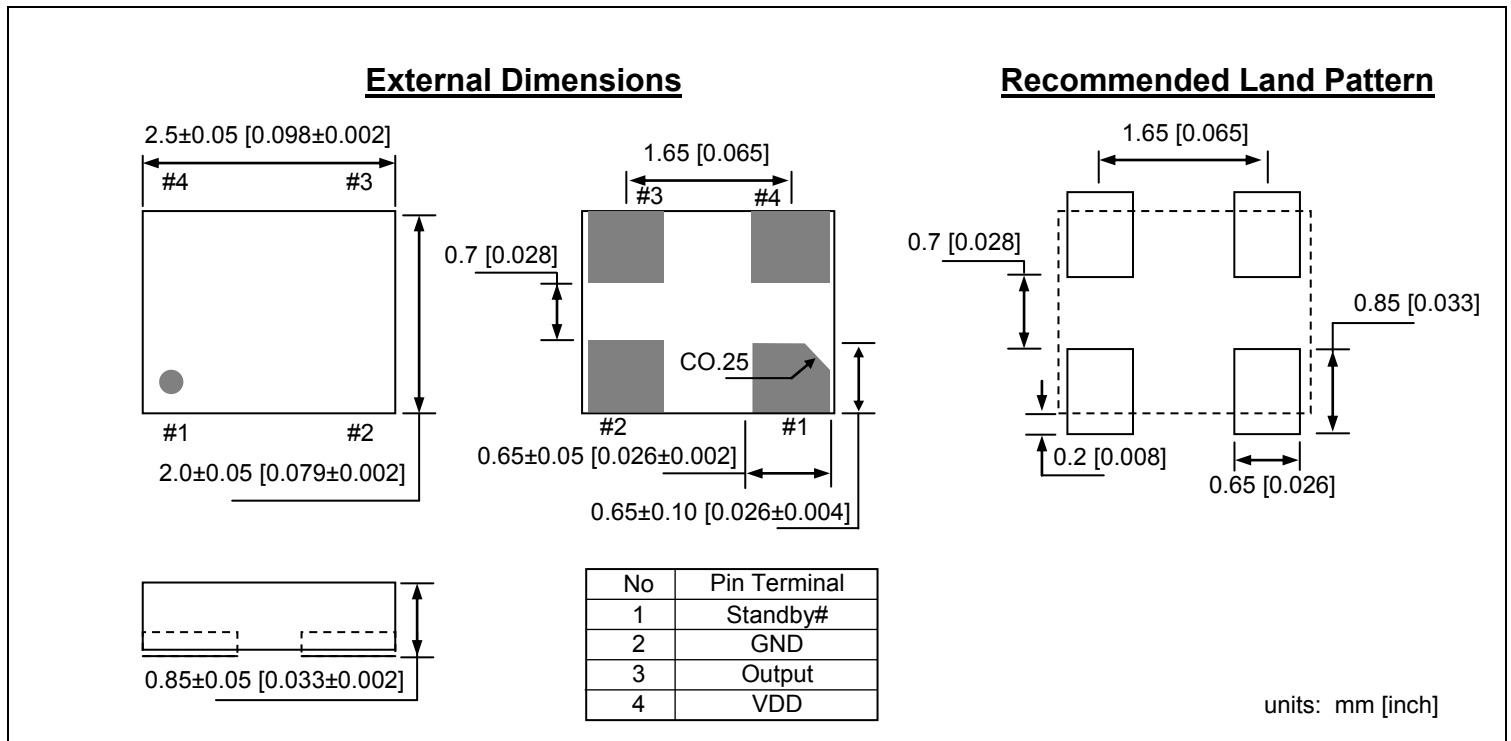
No.	Pin Terminal
1	Standby#
2	GND
3	Output
4	VDD

#### Recommended Land Pattern



units: mm [inch]

## 2.5 x 2.0 mm Plastic Package



## Ordering Information

### DSC1003 PTS – xxx.xxxx T

#### PART NUMBERING GUIDE

Package (Plastic QFN)	Temperature	Stability	Frequency	Packing Option
<b>P=A:</b> 7.0x5.0mm <b>P=B:</b> 5.0x3.2mm <b>P=C:</b> 3.2x2.5mm <b>P=D:</b> 2.5x2.0mm	<b>T=C:</b> 0° ~ +70° C <b>T=E:</b> -20° ~ +70° C <b>T=I:</b> -40° ~ +85° C <b>T=L:</b> -40° ~ +105° C	<b>S=1:</b> ±50ppm <b>S=2:</b> ±25ppm <b>S=5:</b> ±10ppm	<b>XXX.XXXX</b>	<b>Blank:</b> Tubes <b>T:</b> Tape & Reel

Example: DSC1003CE1-123.0000T

The example part number above is a 123.0000MHz oscillator in Plastic 3.2x2.5mm package, with ±50ppm stability over an operating temperature of -20 to +70°C, shipped in Tape and Reel.

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