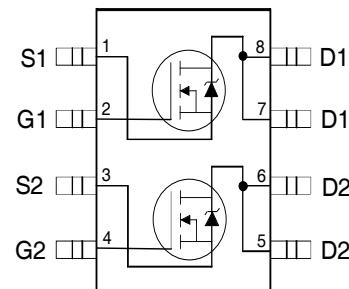


## General Description

The IRF7311TR is the highest performance trench 2N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications. They meet the RoHS and Product requirement with full function reliability approved.



SOP-8

## General Features

$V_{DS} = 30V$   $I_D = 9A$

$R_{DS(ON)} < 13m\Omega$  @  $V_{GS}=10V$

$R_{DS(ON)} < 18m\Omega$  @  $V_{GS}=4.5V$

## Application

Battery protection

Load switch

Uninterruptible power supply

## Absolute Maximum Ratings ( $T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	9	A
$I_D @ T_c=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	8.2	A
$I_D @ T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	6.5	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	5.6	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	30	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	15	mJ
$I_{AS}$	Avalanche Current	22	A
$P_D @ T_c=25^\circ C$	Total Power Dissipation <sup>4</sup>	1.6	W
$P_D @ T_A=70^\circ C$	Total Power Dissipation <sup>4</sup>	1.0	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	75	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	4.8	$^\circ C/W$

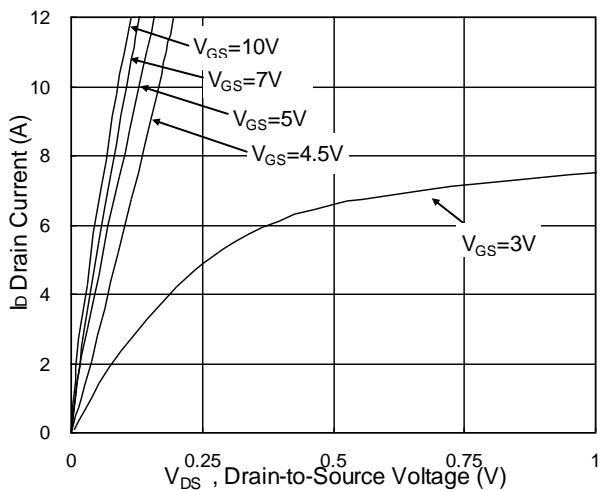
**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	30			V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BVDSS Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$		0.023		$\text{V}/^\circ\text{C}$
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}$ , $I_D=15\text{A}$			13	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=10\text{A}$			18	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	1.0		2.5	V
$\Delta V_{\text{GS}(\text{th})}$	$V_{\text{GS}(\text{th})}$ Temperature Coefficient			-5.08		$\text{mV}/^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=24\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$			1	$\text{uA}$
		$V_{\text{DS}}=24\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$			5	
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$			$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=5\text{V}$ , $I_D=15\text{A}$		32		S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$		1.7		$\Omega$
$Q_g$	Total Gate Charge (4.5V)	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=4.5\text{V}$ , $I_D=12\text{A}$		5.3		nC
$Q_{\text{gs}}$	Gate-Source Charge			0.78		
$Q_{\text{gd}}$	Gate-Drain Charge			2.2		
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=15\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_g=1.5\text{ }\square$		6.4		ns
$T_r$	Rise Time			39		
$T_{\text{d(off)}}$	Turn-Off Delay Time		$I_D=20\text{A}$	21		
$T_f$	Fall Time			4.7		
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$		580		pF
$C_{\text{oss}}$	Output Capacitance			97		
$C_{\text{rss}}$	Reverse Transfer Capacitance			39		
$I_s$	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0\text{V}$ , Force Current			37	A
$I_{\text{SM}}$	Pulsed Source Current <sup>2,5</sup>				75	A
$V_{\text{SD}}$	Diode Forward Voltage <sup>2</sup>	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$			1	V

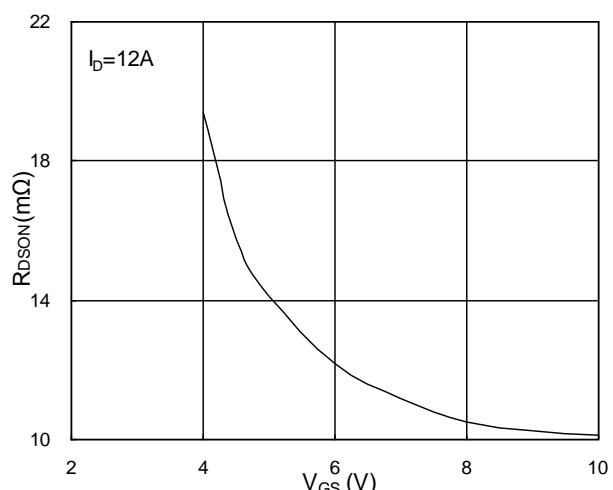
Note :

- 1 .The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$
- 3 .The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=25\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $L=0.1\text{mH}$ , $I_{\text{AS}}=22\text{A}$
- 4.The power dissipation is limited by  $175^\circ\text{C}$  junction temperature
- 5 .The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

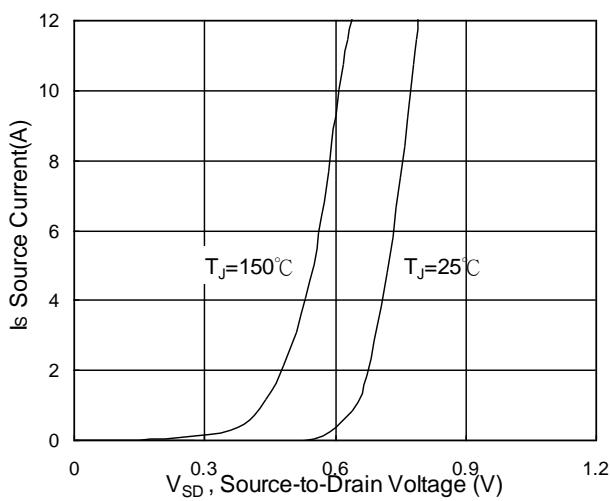
### Typical Characteristics



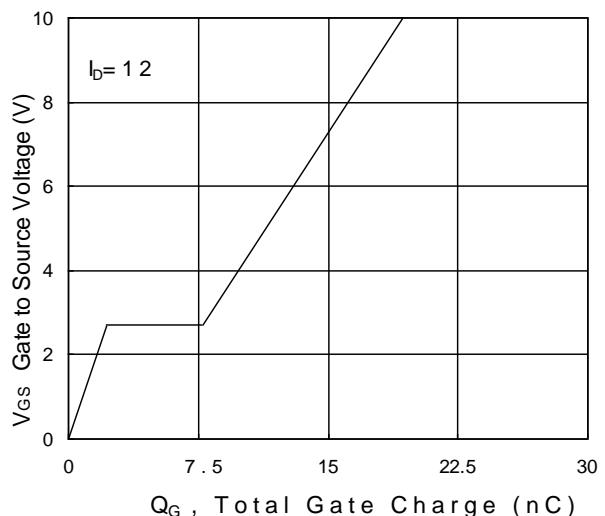
**Fig.1 Typical Output Characteristics**



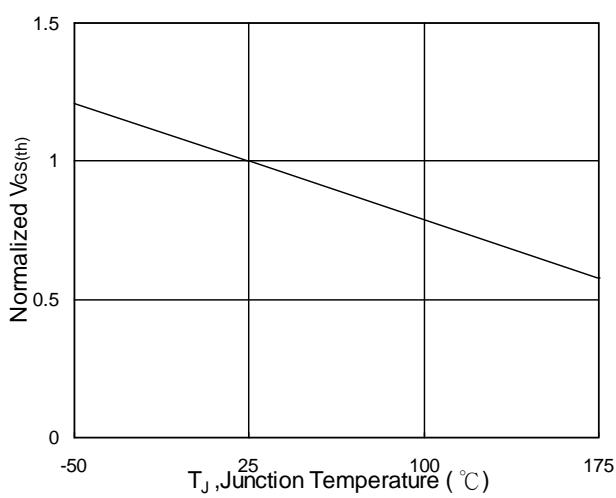
**Fig.2 On-Resistance vs. G-S Voltage**



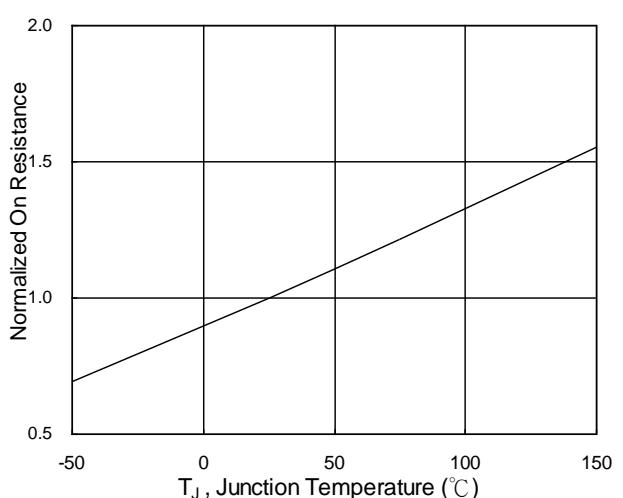
**Fig.3 Forward Characteristics of Reverse**



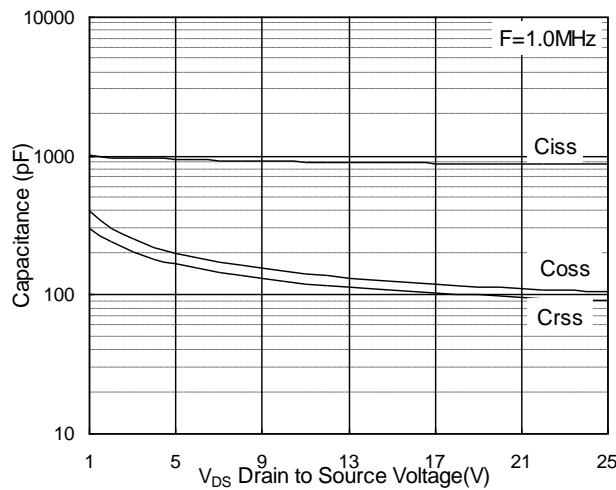
**Fig.4 Gate-charge Characteristics**



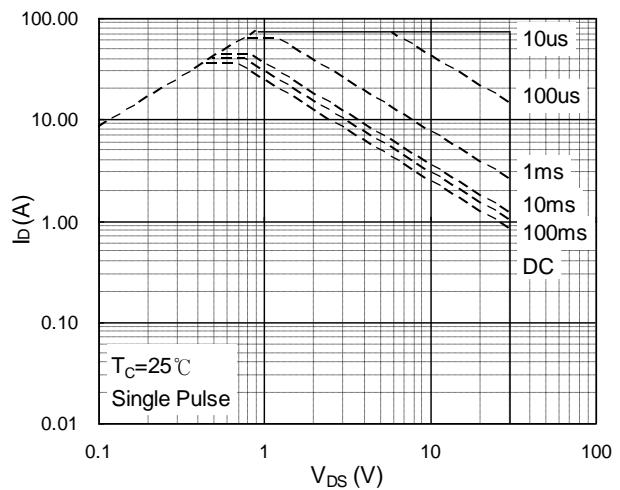
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



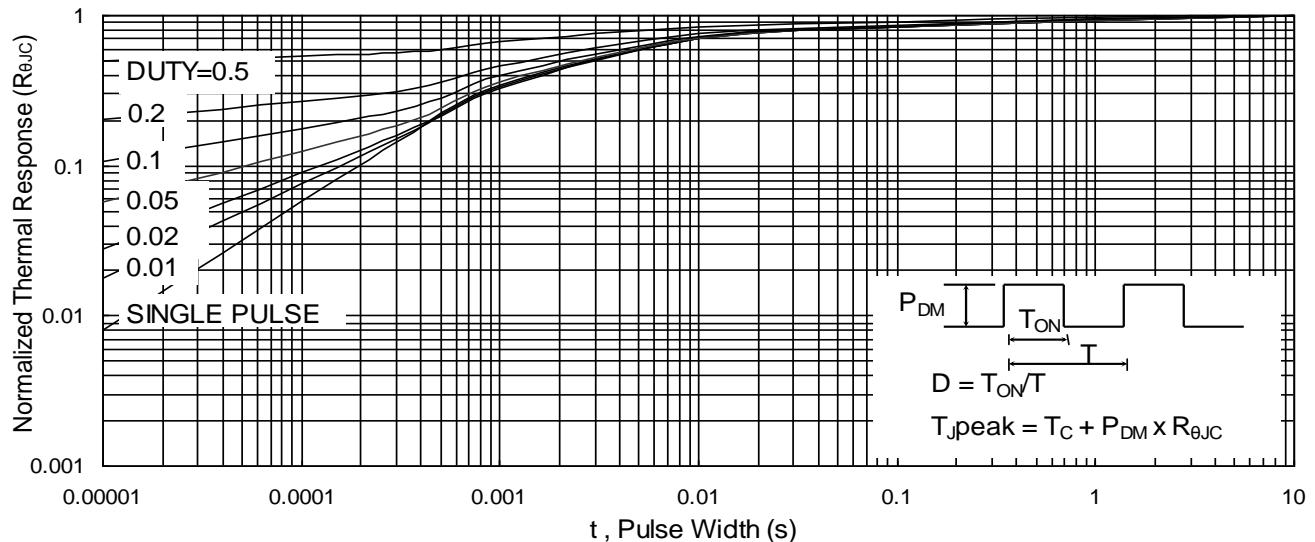
**Fig.6 Normalized  $R_{DSON}$  vs.  $T_J$**



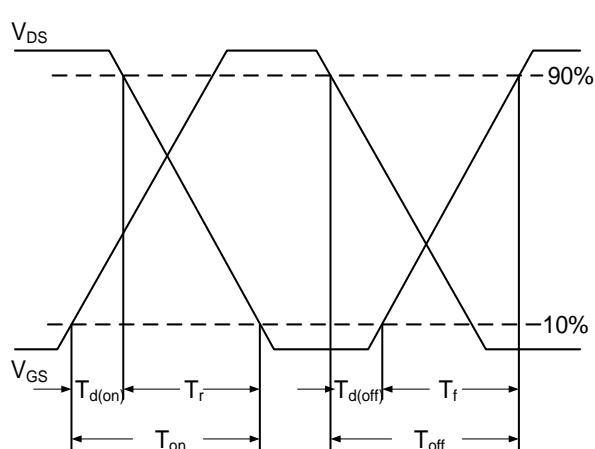
**Fig.7 Capacitance**



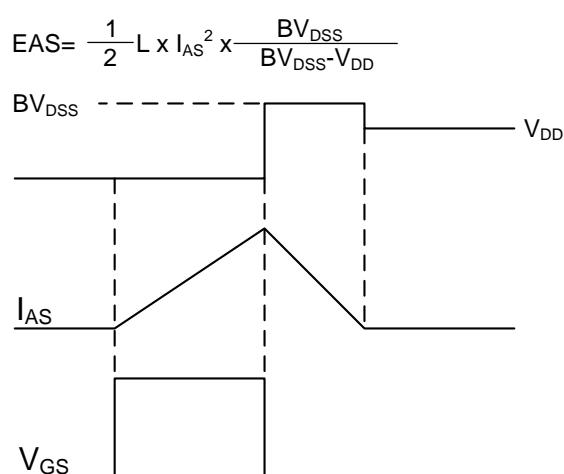
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

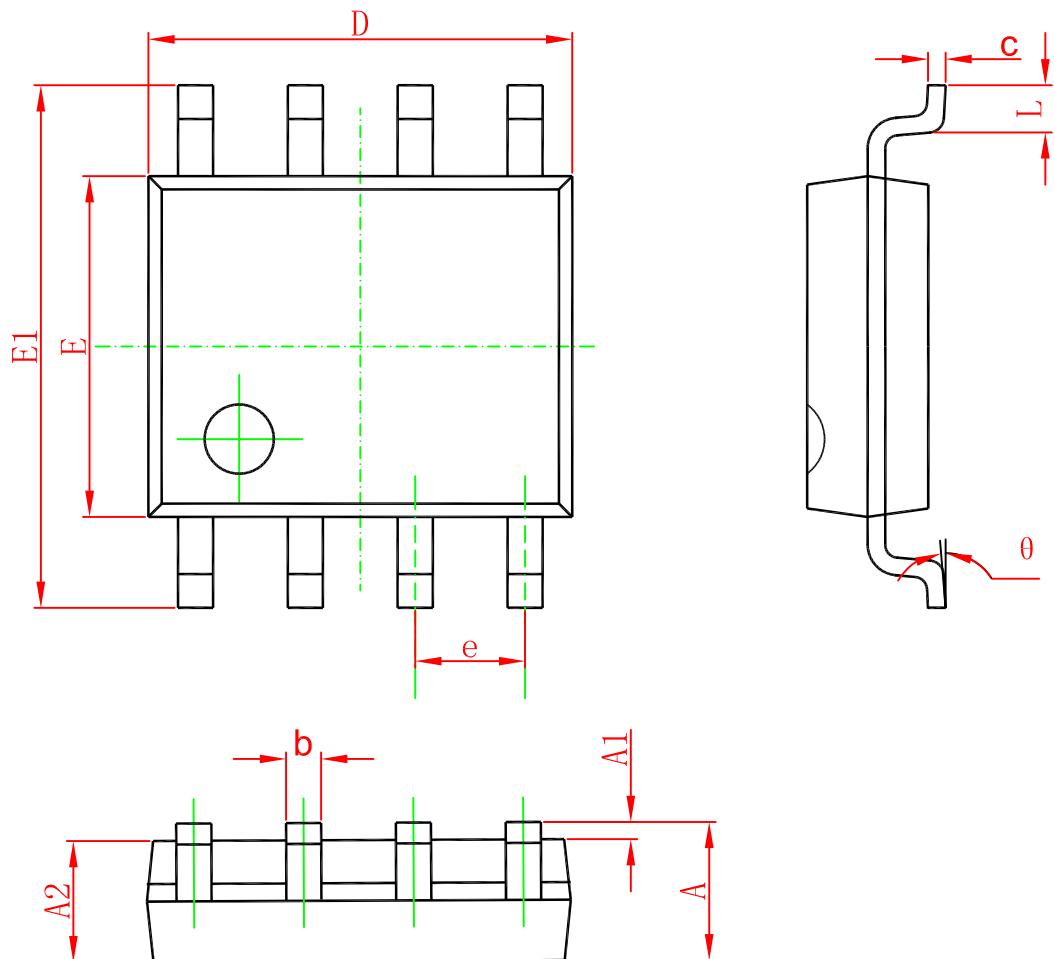


**Fig.10 Switching Time Waveform**



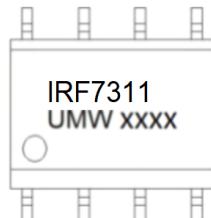
**Fig.11 Unclamped Inductive Waveform**

## SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

## Marking



## Ordering information

Order code	Package	Baseqty	Deliverymode
UMW IRF7311TR	SOP-8	3000	Tape and reel