Surface Mount PTC OZCF Series



RoHS 2 Compliant

Product Features

- 2920 Chip Size, Fast Trip Time, High Hold Currents
- AEC-Q Compliant
- Meets Bel automotive qualification*
 - * Largely based on internal AEC-Q test plan

Operating (Hold Current) Range

300mA - 5A

Maximum Voltage

6 - 60V (per table)

Temperature Range

-40°C to 85°C

Agency Approval

TUV (Std. EN/IEC 60738-1-1 and EN/IEC 60730-1, Cert. R50102117) UL Recognized Component (Std. UL1434, File E305051)

LEAD FREE =



HALOGEN FREE = HF



Electrical Characteristics (23°C)

AEC-Q Compliant

			Hold Tri	Trip	Rated	Maximum	Typical	Max Time to Trip		Resistance Tolerance		Agency Approvals	
		Part Number	Current	Current	Voltage	Current	Power	Current	Time	Rmin	R1max	c 71 2 us	Α̈́
			Ін, А	Іт, А	Vmax, Vdc	Imax, A	Pd, W	Α	Sec	Ohms	Ohms	C 744 US	ΤÜV
Ī	Α	0ZCF0030FF2C	0.30	0.60	60	100	1.5	1.5	3.0	1.000	4.800	Υ	Υ
	В	0ZCF0050FF2C	0.50	1.00	60	100	1.5	2.5	4.0	0.300	1.400	Υ	Υ
	С	0ZCF0075FF2C	0.75	1.50	33	100	1.5	8.0	0.3	0.180	1.000	Υ	Υ
	C	0ZCF0075AF2C	0.75	1.50	60	100	1.5	8.0	0.3	0.180	1.000	Υ	Υ
١	D	0ZCF0100AF2A	1.00	2.00	60	100	1.5	8.0	0.5	0.090	0.410	Υ	Υ
	ט	0ZCF0110FF2C	1.10	2.20	33	100	1.5	8.0	0.5	0.090	0.410	Υ	Υ
	Е	0ZCF0110AF2A	1.10	2.20	60	100	1.5	8.0	0.5	0.090	0.410	Υ	Υ
	H	0ZCF0125FF2C	1.25	2.50	33	100	1.5	8.0	2.0	0.050	0.250	Υ	Υ
	G	0ZCF0150FF2C	1.50	3.00	33	100	1.5	8.0	2.0	0.050	0.230	Υ	Υ
	I	0ZCF0185FF2C	1.85	3.70	33	100	1.5	8.0	2.5	0.040	0.150	Υ	Υ
Ī	_	0ZCF0200FF2C	2.00	4.00	16	100	1.5	8.0	4.5	0.035	0.120	Υ	Υ
	'	0ZCF0200AF2C	2.00	4.00	24	100	1.5	8.0	5.0	0.035	0.120	Υ	Υ
	J	0ZCF0250FF2C	2.50	5.00	16	100	1.5	8.0	16.0	0.025	0.085	Υ	Υ
	Κ	0ZCF0260FF2C	2.60	5.20	6	100	1.5	8.0	20.0	0.020	0.075	Υ	Υ
	r	0ZCF0260AF2C	2.60	5.20	24	100	1.5	8.0	20.0	0.020	0.075	Υ	Υ
		0ZCF0300FF2C	3.00	5.20	6	100	1.5	8.0	25.0	0.010	0.048	Υ	Υ
	L	0ZCF0300AF2C	3.00	5.20	15	100	1.5	8.0	20.0	0.010	0.048	Υ	Υ
		0ZCF0300BF2C	3.00	5.20	24	100	1.5	8.0	20.0	0.010	0.048	Υ	Υ
] ا	М	0ZCF0330FF2C	3.30	5.50	24	100	1.5	8.0	20.0	0.010	0.048	Υ	Υ
ı[Ν	0ZCF0400FF2A	4.00	8.00	16	100	1.5	20.0	4.0	0.010	0.040	Υ	Υ
ı	0	0ZCF0500FF2A	5.00	10.00	16	100	1.5	20.0	5.0	0.005	0.025	Υ	Υ

New Rating

New Rating New Rating **New Rating**

Hold Current- The maximum current at which the device will not trip in still air at 23°C. ΙH

ΙT Trip current- The minimum current at which the device will trip in still air at 23°C.

Vmax Maximum voltage device can withstand at its rated current without suffering damage.

Imax Maximum fault current device can withstand at rated voltage (Vmax) without damage.

Typical power dissipated by device when in tripped state in 23°C still air environment. Pd

Minimum device resistance at 23°C in initial un-soldered state. Rmin

R1max Maximum device resistance at 23°C, 1 hour after initial device trip, or after being soldered to PCB in end application.



Specifications subject to change without notice

Type 0ZCF Series

PTC's - Basic Theory of Operation / "Tripped" Resistance Explanation

A Bel PTC consists of a block of polymeric material containing conductive carbon granules which is sandwiched between two conductive metal plates. When this polymer block reaches approximately 125C, either due to current passing through it via conductive chains of carbon particles or due to an external heat source; it swells volumetrically. This expansion breaks apart a majority of the chains of carbon granules that run randomly between the two conductive plates. This behavior results in a sharp increase in resistance across the two plates which all but eliminates current flow through the device, allowing just enough residual current flow to maintain the block's internal temperature at 125C. Once this "tripped" state current is cut off, the polymer brick cools and shrinks to its original size, thereby allowing its broken carbon chains to reestablish themselves and permit the part to return to its low resistance state. Once cooled to room ambient, the PTC will once again exhibit a resistance less than its "R1max" rating.

At currents below the device IHOLD rating, AND at temperatures below 100C, the PTC maintains a resistance value below its R1 MAX rating.

The catalog data for each device specifies a "Typical Power" value. This is the power required to exactly match the heat lost by the tripped device to its ambient surroundings at 23C. By Ohm's Law, power can be stated as: $W = E^2/R$. Thus the approximate resistance of a "Tripped" PTC can be determined by: $R = E^2/W$, where "E" is the voltage appearing across the PTC (usually the supply's open circuit voltage), and "W" is the Typical Power value for the particular PTC.

Since the PPTC acts to maintain a constant internal temperature, its apparent resistance will change based upon applied voltage and, to a lesser degree, ambient conditions. Consider the following example....

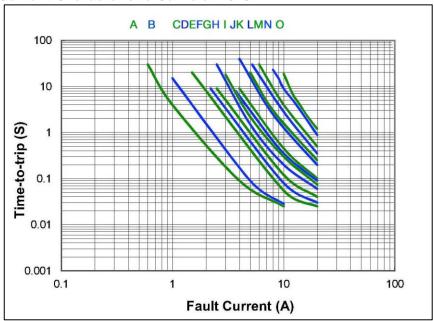
A PTC with a Typical Power of 1 watt protecting a circuit using a 60V supply will demonstrate an apparent, tripped resistance "R" of:

 $R = 60^2/1 = 3,600 \text{ ohms}$

This same tripped device when used to protect a 12V circuit would now present an apparent resistance of: $R = 12^2/1 = 144$ ohms

The value for Typical Power is "typical" because any physical factors that affect heat loss (such as ambient temperature or air convection) will somewhat alter the level of power that the PTC needs to maintain its internal temperature. In short, PTCs do not exhibit a constant, quantifiable tripped resistance value.

Average Time Current Characteristic Curve at 23°C



The Average Time Current Characteristic Curve and Temperature Rerating Curve are affected by a number of variables and these curves are provided for guidance only.



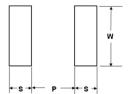
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Type 0ZCF Series

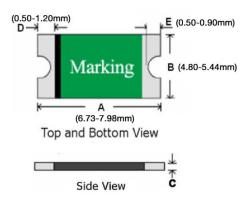
Pad Layout

The dimensions in the table below provide the Recommended pad layout.



	Р	•	S	W			
Nor	ninal	Nor	ninal	Nominal			
mm Inch		mm	Inch	mm	Inch		
5.10	0.201	2.30	0.091	5.60	0.221		

Mechanical Dimensions and Marking



Termination Pad Materials

Matte Tin - Plated Copper

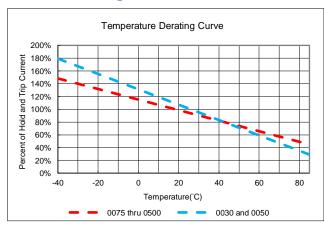
All dimensions in mm.

	Dimen	sions	Marking Code			
Part Number	C	;	^{нопзп} "b", Ін code			
	Min	Max	pxxxx	PXXX		
0ZCF0030FF2C	0.60	1.15	0030			
0ZCF0050FF2C	0.60	1.15	0050			
0ZCF0075FF2C	0.40	1.15	0075			
0ZCF0075AF2C	0.60	1.15		0075 60		
0ZCF0100AF2A	0.40	1.70		0100 60		
0ZCF0110FF2C	0.40	1.00	0110			
0ZCF0110AF2A	0.40	1.70		0110 60		
0ZCF0125FF2C	0.40	0.90	0125			
0ZCF0150FF2C	0.40	0.90	0150			
0ZCF0185FF2C	0.30	0.90	0185			
0ZCF0200FF2C	0.30	0.90	0200			
0ZCF0200AF2C	0.20	0.80		0200 24		
0ZCF0250FF2C	0.30	0.90	0250			
0ZCF0260FF2C	0.30	0.90	0260			
0ZCF0260AF2C	0.65	1.15		0260 24		
0ZCF0300FF2C	0.40	0.90	0300			
0ZCF0300AF2C	0.40	1.15		0300 15		
0ZCF0300BF2C	0.65	1.15		0300 24		
0ZCF0330FF2C	0.65	1.15	0330			
0ZCF0400FF2A	0.40	1.50	0400			
0ZCF0500FF2A	0.40	1.50	0500			

Temperature Derating Table

				Tempera	ture Derati	ing Table				
I Hold Value	-40	-20	0	23	30	40	50	60	70	85
0030 and 0050	182%	156%	130%	100%	93%	83%	72%	60%	48%	30%
0075 thru 0500	146%	131%	115%	100%	93%	83%	74%	64%	56%	43%

Thermal Derating Curve



Cautionary Notes

- Operation beyond the specified maximum ratings or improper use may result in damage and possible electrical arcing and/or flame.
- These Polymer PTC (PPTC) devices are intended for protection against occasional overcurrent/overtemperature fault conditions and may not be suitable for use in applications where repeated and/or prolonged fault conditions are anticipated.
- 3. Avoid contact of PTC device with chemical solvent. Prolonged contact may adversely impact the PTC performance.
- 4. These PTC devices may not be suitable for use in circuits with a large inductance, as the PTC trip can generate circuit voltage spikes above the PTC rated voltage.
- 5. These devices may be used in both DC and AC circuits provided that peak-to-peak line voltage when carrying AC does not exceed the PTC's Vmax rating. As PTCs are essentially thermal devices, the RMS value of AC current carried by a PTC will produce tripping parameters and times-to-trip similar to those of a DC voltage of the same magnitude.
- If potting is mandated, avoid rigid potting compounds as they will encase the PTC and prevent it from volumetrically expanding to properly respond to a trip event.
- 7. MSL: 2a (According to IPC J-Std-020).

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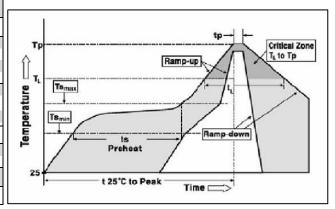
Type 0ZCF Series

Environmental Specifications

Temperature cycling	JESD22 Method JA-104
Biased humidity	MIL-STD-202 Method 103
Operational life	MIL-STD-202 Method 108
Resistance to solvents	MIL-STD-202 Method 215
Mechanical shock	MIL-STD-202 Method 213
Vibration	MIL-STD-202 Method 204
Resistance to soldering heat	MIL-STD-202 Method 210
Thermal shock	MIL-STD-202 Method 107
Solderability	ANSI/J-STD-002
Board flex(SMD)	AEC-Q200-005
Terminal strength	AEC-Q200-006

Solder Reflow and Rework Recommendations

Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate (Tsmax to Tp)	3°C°C/second max
Preheat :	
Temperature Min (Tsmin)	150°C
Temperature Max (Tsmax)	200°C
Time (tsmin to tsmax)	60-180 seconds
Time maintained above:	
Temperature(TL)	217°C
Time (tL)	60-150 seconds
Peak/Classification Temperature(Tp):	260°C
Time within 5°C of actual Peak:	
Temperature (tp)	20-40 seconds
Ramp-Down Rate :	6°C/second max.
Time 25°C to Peak Temperature :	8 minutes max



Solder Reflow

Due to "lead free / RoHS 2" construction of these PTC devices, the required Temperature and Dwell Time in the "Soldering" zone of the reflow profile are greater than those used for non-RoHS devices.

- 1. Recommended reflow methods; IR, vapor phase oven, hot air oven.
- 2. Not Recommended For Wave Solder / Direct Immersion.
- 3. Recommended paste thickness range 0.20 0.25mm.
- 4. Devices are compatible with standard industry cleaning solvents and methods.
- 5. MSL: 2a (According to IPC J-Std-020).

Caution

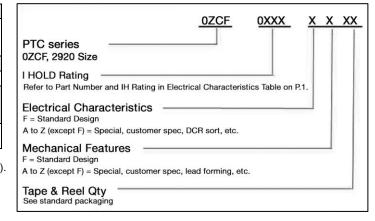
If reflow temperature / dwell times exceed the recommended profile, the electrical performance of the PTC may be affected. Rework: MIL-STD-202G Method 210F, Test Condition A.

Standard Packaging

Part Number	Tape/Reel Qty
0ZCF0030FF2C	
Thru	2,000
0ZCF0075AF2C	
0ZCF0100AF2A	1,000
0ZCF0110AF2A	1,000
0ZCF0110FF2C	
Thru	2,000
0ZCF0330FF2C	
0ZCF0400FF2A	1,000
0ZCF0500FF2A	1,500

2000 or 1000 fuses in 7 inches dia. Reel, 8mm wide tape, 4mm pitch, per EIA-481(equivalent IEC-286 part 3).

P/N Explanation and Ordering Information





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