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Vishay General Semiconductor

Surface-Mount TMBS[®] (Trench MOS Barrier Schottky) Rectifier



Cathode O Anode

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	3.0 A			
V _{RRM}	60 V			
I _{FSM}	60 A			
V_F at $I_F = 3.0$ A	0.48 V			
T _J max.	150 °C			
Package	SMP (DO-220AA)			
Circuit configuration	Single			

FEATURES

- Low profile package
- Trench MOS Schottky technology
- Low power losses, high efficiency
- · Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 gualified available - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMP (DO-220AA)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V3PL63	UNIT	
Device marking code		3LF		
Maximum repetitive peak reverse voltage	V _{RRM}	60	V	
Maximum DC forward current	I _{F(AV)} ⁽¹⁾	3	A	
	I _{F(AV)} ⁽²⁾	2.1	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	60	А	
Operating junction temperature range	T _J ⁽³⁾	-40 to +150	°C	
Storage temperature range	T _{STG}	-55 to +150	°C	

Notes

⁽¹⁾ Mounted on 10 mm x 10 mm PCB pad area

⁽²⁾ Free air, mounted on recommended copper pad area

⁽³⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_1 < 1/R_{B,IA}$





COMPLIANT

HALOGEN

FREE

V3PL63



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ELECTRICAL CHARACTERISTICS (T_J = 25 °C unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 1.5 A		V _F ⁽¹⁾	0.46	-	V
	I _F = 3 A			0.53	0.59	
	I _F = 1.5 A	- T _J = 125 °C		0.38	-	
	I _F = 3 A			0.48	0.55	
Reverse current	V _B = 60 V	T _J = 25 °C T _J = 125 °C	I _R ⁽²⁾	-	0.07	- mA
	v _R = 60 v	T _J = 125 °C		2.5	5	
Typical junction capacitance	4.0 V, 1 MF	4.0 V, 1 MHz		460	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise specified)				
PARAMETER	SYMBOL	V3PL63	UNIT	
Typical thermal resistance	R _{0JA} (1)(2)	125	°C/W	
	R _{θJM} ⁽³⁾	15		

Notes

⁽¹⁾ Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance $R_{\theta JA}$ - junction-to-ambient

⁽²⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

 $^{(3)}$ Mounted on 10 mm x 10 mm copper pad area PCB; thermal resistance $R_{\theta JM}$ - junction-to-mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V3PL63-M3/H	0.024	Н	3000	7" diameter plastic tape and reel		
V3PL63-M3/I	0.024	I	10 000	13" diameter plastic tape and reel		
V3PL63HM3/H ⁽¹⁾	0.024	Н	3000	7" diameter plastic tape and reel		
V3PL63HM3/I ⁽¹⁾	0.024	l	10 000	13" diameter plastic tape and reel		

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

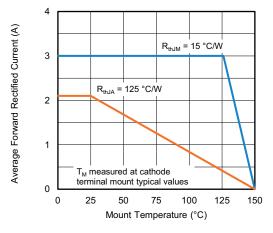


Fig. 1 - Maximum Forward Current Derating Curve

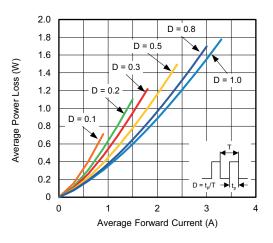


Fig. 2 - Forward Power Loss Characteristics

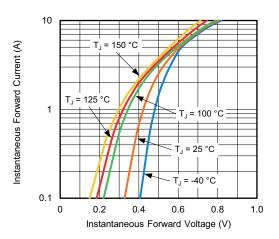


Fig. 3 - Typical Instantaneous Forward Characteristics

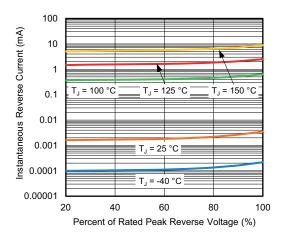
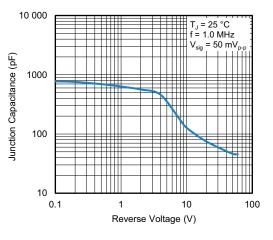
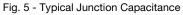


Fig. 4 - Typical Reverse Characteristics





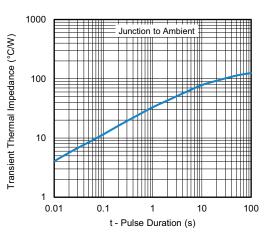
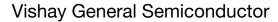


Fig. 6 - Typical Transient Thermal Impedance

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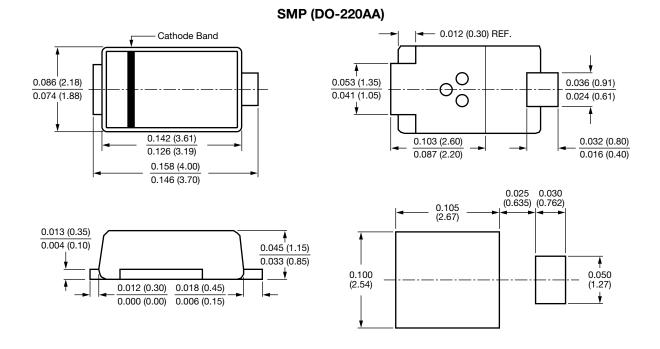
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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