

**N-Ch 150V Fast Switching MOSFETs**
**General Description**

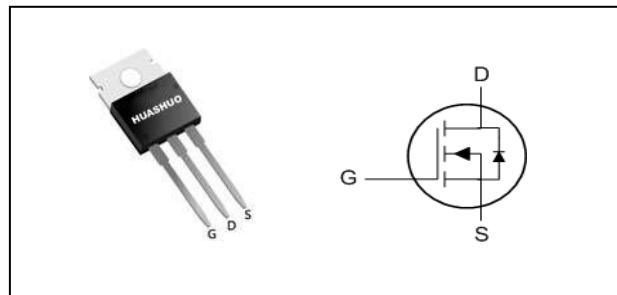
- 100% EAS Guaranteed
- Green Device Available
- Super Low R<sub>DSON</sub>
- Advanced high cell density Trench technology

**Product Summary**

V <sub>DS</sub>	150	V
R <sub>DSON,typ</sub>	7.3	mΩ
I <sub>D</sub>	100	A

**Applications**

- MOTOR Driver.
- BMS.
- High frequency switching and synchronous rectification.

**TO220 Pin Configuration**

**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	150	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1,6</sup>	100	A
I <sub>D</sub> @T <sub>c</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1,6</sup>	63	A
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	400	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	785	mJ
I <sub>AS</sub>	Avalanche Current	56	A
P <sub>D</sub> @T <sub>c</sub> =25°C	Total Power Dissipation <sup>4</sup>	178	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>	---	60	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	---	0.7	°C/W

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**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	150	---	---	V
R <sub>DSON</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =20A	---	7.3	9	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2.0	3.0	4.0	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =120V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =120V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C	---	---	5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V	---	---	±100	nA
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz	---	1.9	---	Ω
Q <sub>g</sub>	Total Gate Charge (10V)	V <sub>DS</sub> =75V , V <sub>GS</sub> =10V , I <sub>D</sub> =20A	---	100	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	25	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	31	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =30V , V <sub>GS</sub> =10V , R <sub>G</sub> =3.3Ω, I <sub>D</sub> =1A	---	32	---	ns
T <sub>r</sub>	Rise Time		---	25	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	98	---	
T <sub>f</sub>	Fall Time		---	89	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =75V , V <sub>GS</sub> =0V , f=1MHz	---	5880	---	pF
C <sub>oss</sub>	Output Capacitance		---	401	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	9.5	---	

**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>s</sub>	Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	---	100	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>s</sub> =1A , T <sub>J</sub> =25°C	---	---	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=50V,V<sub>GS</sub>=10V,L=0.5mH,I<sub>AS</sub>=56A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.
- 6.Package limitation current.



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Typical Characteristics

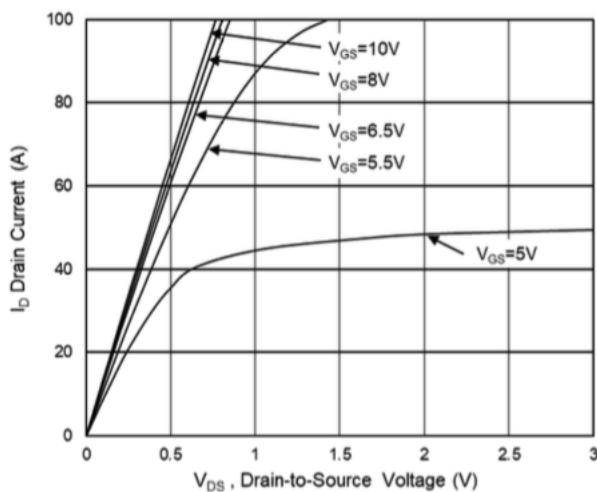


Fig.1 Typical Output Characteristics

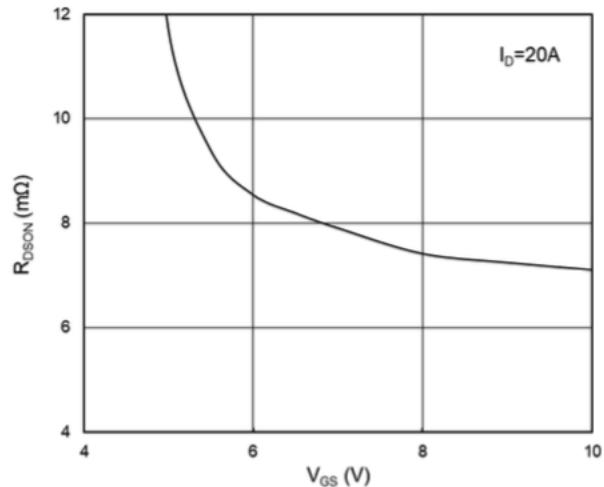


Fig.2 On-Resistance vs G-S Voltage

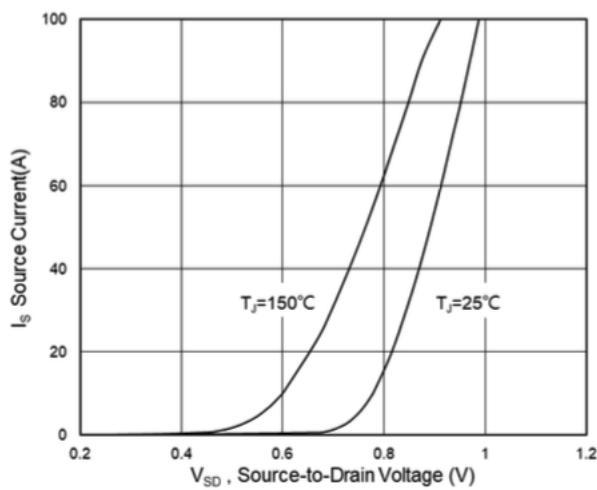


Fig.3 Source Drain Forward Characteristics

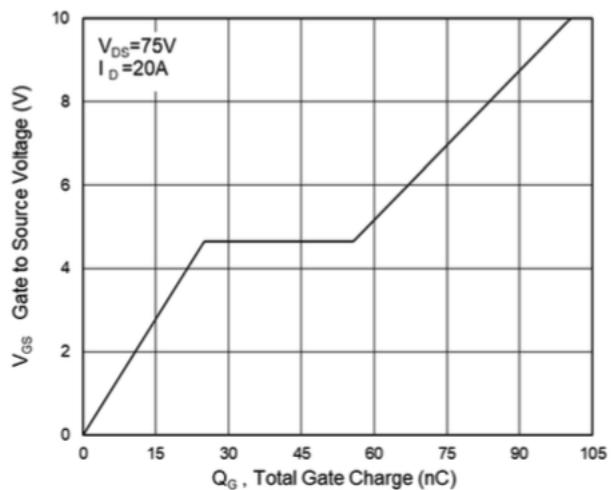


Fig.4 Gate-Charge Characteristics

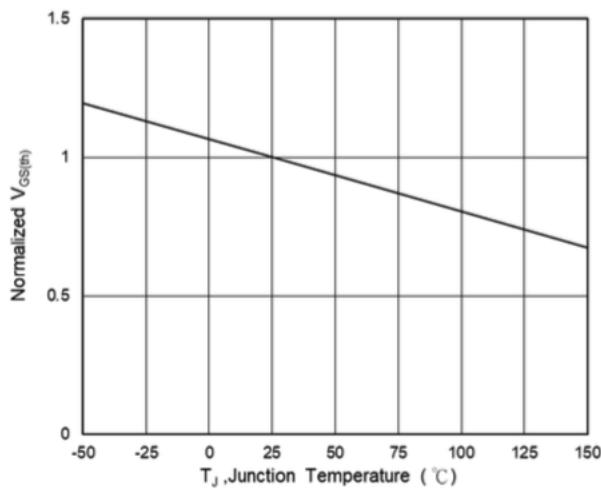


Fig.5 Normalized  $V_{TH}$  vs  $T_J$

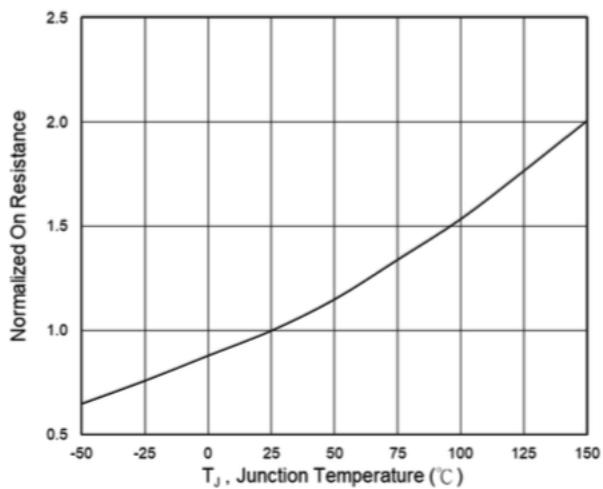


Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$



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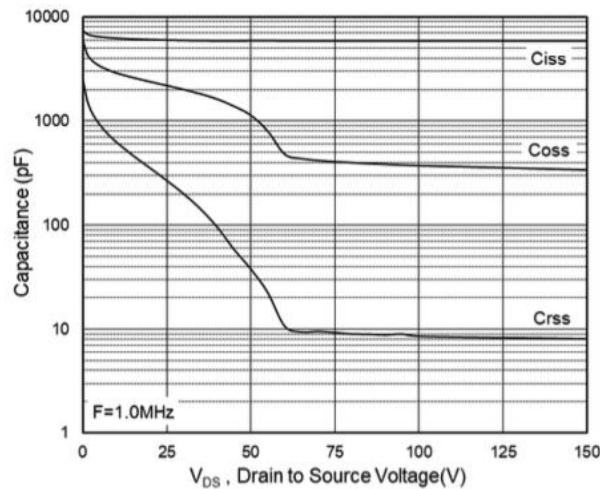


Fig.7 Capacitance

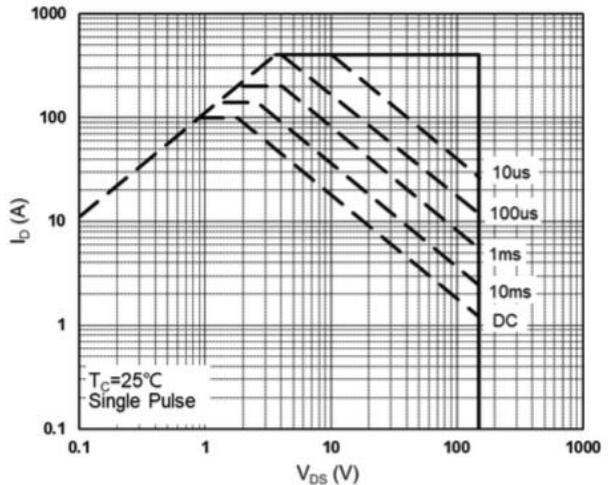


Fig.8 Safe Operating Area

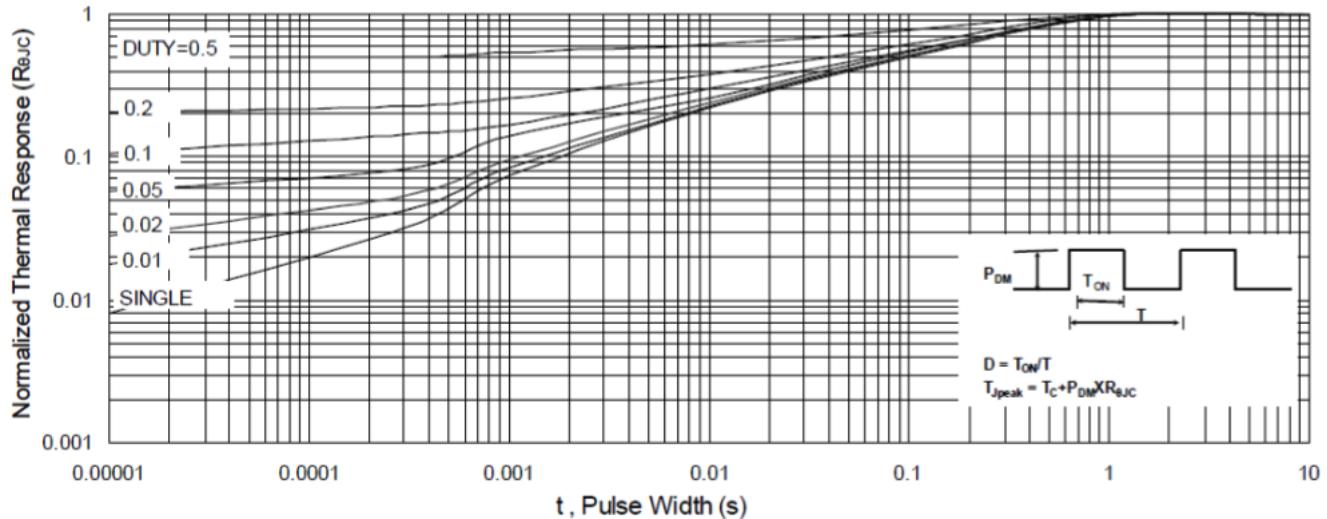


Fig.9 Normalized Maximum Transient Thermal Impedance

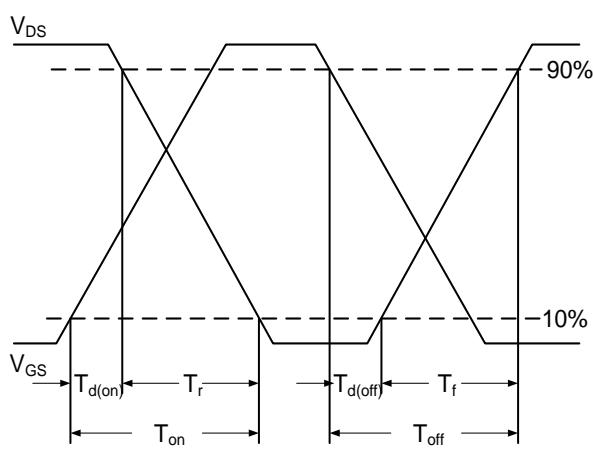


Fig.10 Switching Time Waveform

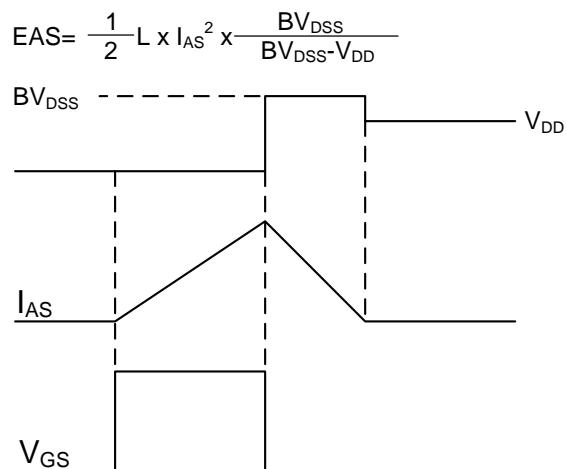
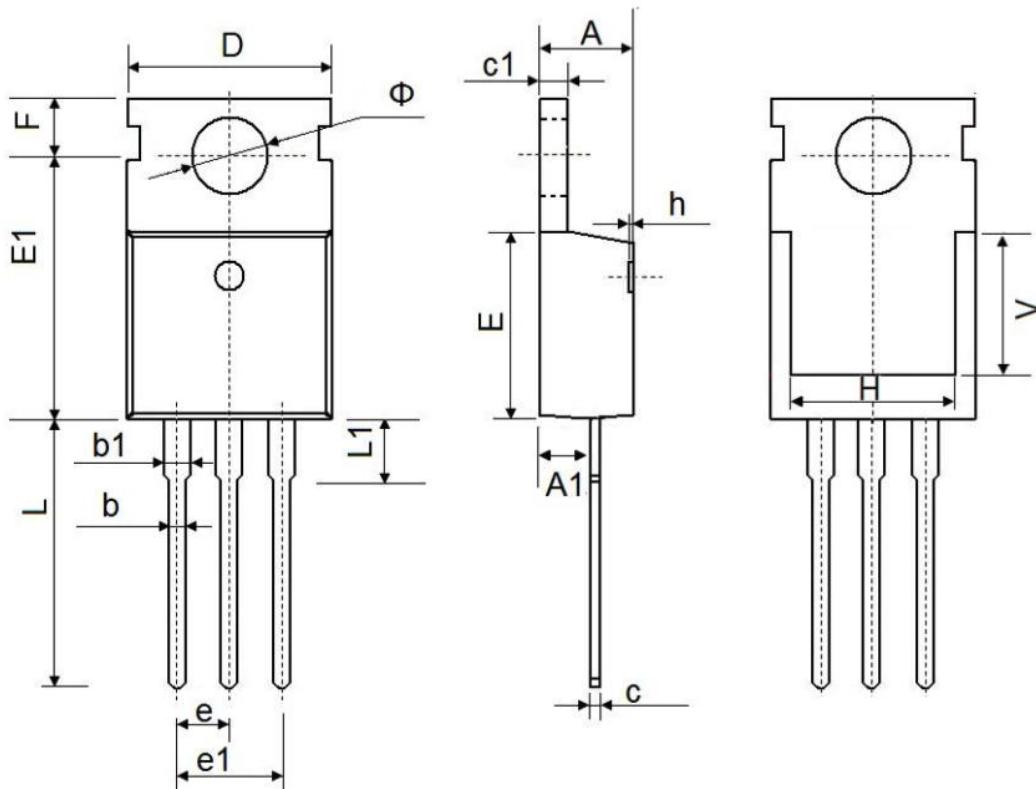


Fig.11 Unclamped Inductive Switching Waveform



## TO-220 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max
A	4.300	4.700	0.169	0.185
A1	2.200	2.600	0.087	0.102
b	0.700	0.950	0.028	0.037
b1	1.170	1.410	0.046	0.056
c	0.450	0.650	0.018	0.026
c1	1.200	1.400	0.047	0.055
D	9.600	10.400	0.378	0.409
E	8.8500	9.750	0.348	0.384
E1	12.650	12.950	0.498	0.510
e	2.540 TYP.		0.100TYP.	
e1	4.980	5.180	0.196	0.204
F	2.650	2.950	0.104	0.116
H	7.900	8.100	0.311	0.319
h	0.000	0.300	0.000	0.012
L	12.750	14.300	0.502	0.563
L1	2.850	3.950	0.112	0.156
V	7.500 REF.		0.295 REF.	
Φ	3.400	4.000	0.134	0.157