# APT60D120BG Datasheet Ultrafast Soft Recovery Rectifier Diode

June 2018





# **Contents**

1	Revis	Revision History				
		Revision G				
	1.2	Revision F				
	1.3	Revision E	1			
	1.4	Revision D	1			
	1.5	Revision C	. 1			
	1.6	Revision B	. 1			
	1.7	Revision A	. 1			
2	Prod	luct Overview	. 2			
_		Features				
		Benefits				
	2.3	Applications	. 2			
3	Flect	trical Specifications	. 3			
		Absolute Maximum Ratings				
	3.2	Typical Electrical Performance	3			
	3.3	Typical Performance Curves	5			
	3.4	Reverse Recovery Overview	. 8			
4	Pack	age Specification	. 9			
		Package Outline Drawing				



# 1 Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

#### 1.1 Revision G

Revision G was published in June 2018. The new Microsemi template and format was applied. The package outline drawing was updated.

For more information, see Package Outline Drawing (see page 9).

#### 1.2 Revision F

Revision F was published in June 2015. All APT references were updated to say Microsemi.

#### 1.3 Revision E

Revision E was published in May 2005. Lead-free format and 175 °C ratings implemented.

#### 1.4 Revision D

Revision D was published in January 2004. Changes include creating a new formatted datasheet.

#### 1.5 Revision C

Revision C was published in January 2003. S-package was added to the datasheet as offered originally.

#### 1.6 Revision B

Revision B was published in June 2002. IF(AV) test condition was changed to TC= 85DC. IF(RMS) was changed to 115 Amps.

#### 1.7 Revision A

Revision A was published in December 1998. It was the first publication of this document. The following is a summary of the changes in revision A of this document.

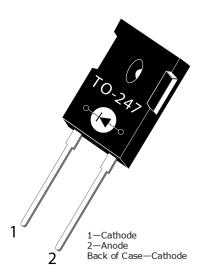
- APT website address was added.
- Shorting bar was added to TO-247 symbol.
- Minimum mounting hole diameter on outline drawing was corrected from 0.140 in. to 0.138 in.



# 2 Product Overview

This section outlines the product overview for the APT60D120BG device.

The following figure shows TO-247 package.



#### 2.1 Features

The following are key features of the APT60D120BG device.

- Ultrafast recovery times
- Soft recovery characteristics
- Low forward voltage
- Low leakage current
- RoHS compliant

#### 2.2 Benefits

The following are benefits of the APT60D120BG device.

- Low switching losses
- Low noise (EMI) switching
- Cooler operation
- Higher reliability systems
- Increased system power density

## 2.3 Applications

The APT60D120BG device is designed for the following applications.

- Power factor correction (PFC)
- Anti-parallel diode
  - Switchmode power supply
  - Inverters
- Freewheeling diode
  - Motor controllers
  - Converters
  - Inverters
- Snubber diode



# **3** Electrical Specifications

This section shows the electrical specifications for the APT60D120BG device.

# 3.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings for the APT60D120BG device.

All ratings: Tc = 25 °C unless otherwise specified.

**Table 1 • Absolute Maximum Ratings** 

Symbol	Parameter	Ratings	Unit
VR	Maximum DC reverse voltage	1200	V
VRRM	Maximum peak repetitive reverse voltage	1200	<del></del>
V <sub>RWM</sub>	Maximum working peak reverse voltage	1200	<del></del>
I <sub>F(AV)</sub>	Maximum average forward current (Tc = 126 °C, duty cycle = 0.5) 60		Α
I <sub>F(RMS)</sub>	RMS forward current	115	
lfsм	Non-repetitive forward surge current (T <sub>1</sub> = 45 °C, 8.3 ms)	540	
Tı , Tstg	Operating and storage temperature range	-55 to 175	°C
Tι	Lead temperature for 10 seconds	300	

# 3.2 Typical Electrical Performance

The following table shows the static characteristics of the APT60D120BG device.

**Table 2 • Static Electrical Characteristics** 

Symbol	Characteristic	Test Conditions	Minimum	Typical	Maximum	Unit
VF	Forward voltage	I <sub>F</sub> = 60 A		2.0	2.5	- V
VF	Forward voitage	I <sub>F</sub> = 120 A		2.3		- V
		IF = 120 A  IF = 60 A, T <sub>J</sub> = 125 °C  UM reverse leakage $V_R = V_R \text{ rated}$ $V_R = V_R \text{ rated}$ , T <sub>J</sub> = 125 °C	1.8		•	
Irm	Maximum reverse leakage	$V_R = V_R$ rated			250	μΑ
	current	V <sub>R</sub> = V <sub>R</sub> rated, T <sub>J</sub> = 125 °C			500	_
Ст	Junction capacitance	V <sub>R</sub> = 200 V		60		pF



The following table shows the dynamic characteristics of the APT60D120BG device.

**Table 3 • Dynamic Characteristics** 

Symbol	Characteristic	<b>Test Conditions</b>	Minimum	Typical	Maximum	Unit
trr	Reverse recovery time	I <sub>F</sub> = 1 A		38		ns
		$di_F/dt = -100 A/\mu s$				
		$V_R = 30 V$				
		T <sub>J</sub> = 25 °C				
trr	Reverse recovery time	I <sub>F</sub> = 60 A		400		=
Qrr	Reverse recovery charge	- di <sub>F</sub> /dt = -200 A/μs V <sub>R</sub> = 800 V	-	1200		nC
IRRM	Maximum reverse recovery current	Tc = 25 °C		6		Α
trr	Reverse recovery time	I <sub>F</sub> = 60 A - di <sub>F</sub> /dt = -200 A/μs V <sub>R</sub> = 800 V		470		ns
Qrr	Reverse recovery charge			4000		nC
IRRM	Maximum reverse recovery current	Tc = 125 °C		13		Α
trr	Reverse recovery time	I <sub>F</sub> = 60 A		200		ns
Qrr	Reverse recovery charge	<ul><li>di<sub>F</sub>/dt = -1000 A/μs</li><li>V<sub>R</sub> = 800 V</li></ul>	-	6200		nC
IRRM	Maximum reverse recovery current	Tc = 125 °C	-	47		Α

The following table shows the thermal and mechanical characteristics of the APT60D120BG device.

**Table 4 • Thermal and Mechanical Characteristics** 

Symbol	Characteristic/Test Conditions	Minimum	Typical	Maximum	Unit
Rejc	Junction-to-case thermal resistance			0.31	96/14
Rеја	Junction-to-ambient thermal resistance			40	– °C/W
WT	Package weight		0.22	40	OZ
			6.2		g
	Mounting torque			10	lbf-in
				1.1	N-m



# 3.3 Typical Performance Curves

This section shows the typical performance curves for the APT60D120BG device.

Figure 1 • Maximum Effective Transient Thermal Impedance, Junction-to-Case vs. Pulse Duration

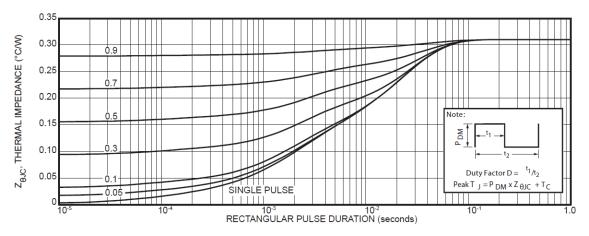


Figure 2 • Transient Thermal Impedance Model

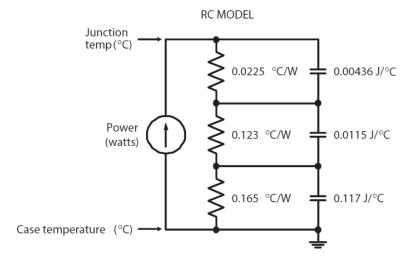




Figure 3 • Forward Current vs. Forward Voltage

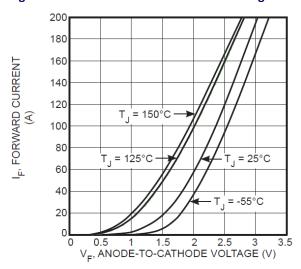


Figure 5 ● Reverse Recovery Charge vs. Current Rate of Change

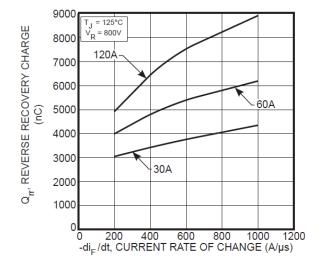


Figure 4 • RRT vs. Current Rate of Change

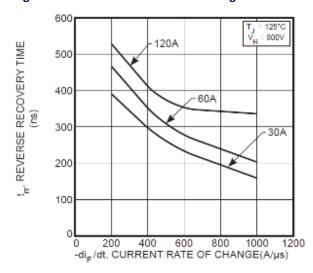


Figure 6 • Reverse Recovery Current vs. Current Rate of Change

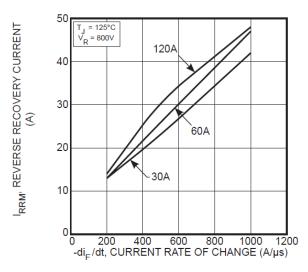




Figure 7 • Dynamic Parameters vs. Junction Temperature

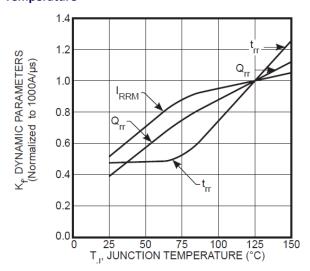


Figure 9 • Junction Capacitance vs. Reverse Voltage

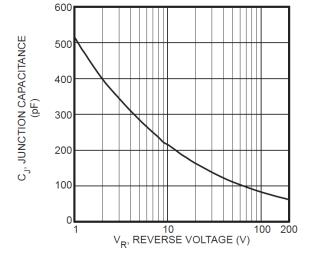
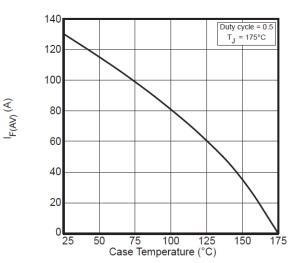


Figure 8 • Maximum Average Forward Current vs. Case Temperature

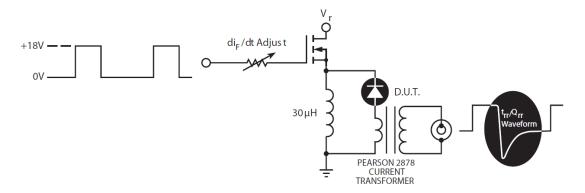




## 3.4 Reverse Recovery Overview

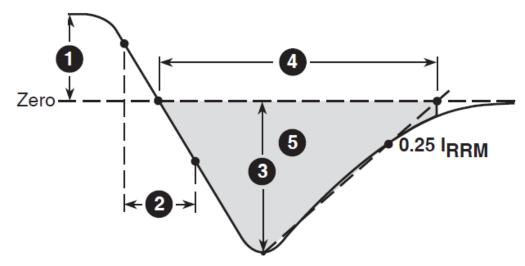
The following figure shows the diode test circuit.

Figure 10 • Diode Test Circuit



The following figure shows the diode reverse recovery waveform.

Figure 11 • Diode Reverse Recovery Waveform and Definitions



- 1. IF—Forward conduction current
- 2. di<sub>F</sub>/dt—Rate of diode current change through zero crossing
- 3. IRRM—Maximum reverse recovery current
- 4. trr—Reverse recovery time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through IRRM and 0.25•IRRM passes through zero
- 5.  $Q_{\text{rr}}\text{--}Area under the curve defined by <math display="inline">I_{\text{RRM}}$  and  $t_{\text{rr}}$



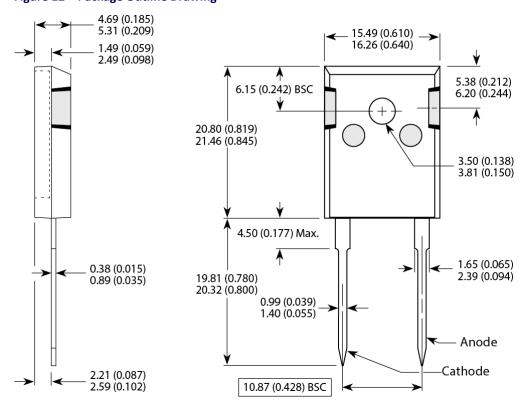
# 4 Package Specification

This section outlines the package specification for the APT60D120BG device.

# 4.1 Package Outline Drawing

The following figure shows the package outline drawing of the APT60D120BG device. Dimensions are in millimeters and (inches).

Figure 12 • Package Outline Drawing







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