



# BUK72150-55A

N-channel TrenchMOS standard level FET

Rev. 03 — 26 January 2011

Product data sheet

## 1. Product profile

### 1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product has been designed and qualified to the appropriate AEC standard for use in automotive critical applications.

### 1.2 Features and benefits

- AEC Q101 compliant
- Low conduction losses due to low on-state resistance
- Suitable for standard level gate drive sources
- Suitable for thermally demanding environments due to 175 °C rating

### 1.3 Applications

- 12 V and 24 V loads
- Automotive systems
- General purpose power switching
- Motors, lamps and solenoids

### 1.4 Quick reference data

Table 1. Quick reference data

| Symbol    | Parameter               | Conditions  | Min | Typ | Max | Unit |
|-----------|-------------------------|---|-----|-----|-----|------|
| $V_{DS}$  | drain-source voltage    | $T_j \geq 25\text{ °C}$ ; $T_j \leq 175\text{ °C}$  | -   | -   | 55  | V    |
| $I_D$     | drain current           | $V_{GS} = 10\text{ V}$ ; $T_{mb} = 25\text{ °C}$ ;<br>see <a href="#">Figure 3</a> ; see <a href="#">Figure 1</a> | -   | -   | 11  | A    |
| $P_{tot}$ | total power dissipation | $T_{mb} = 25\text{ °C}$ ; see <a href="#">Figure 2</a>  | -   | -   | 36  | W    |

#### Static characteristics

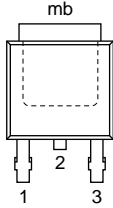
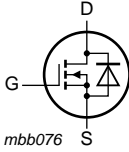
|              |                                  |  |   |     |     |            |
|--------------|----------------------------------|--|---|-----|-----|------------|
| $R_{DS(on)}$ | drain-source on-state resistance | $V_{GS} = 10\text{ V}$ ; $I_D = 5\text{ A}$ ;<br>$T_j = 25\text{ °C}$ ; see <a href="#">Figure 10</a> ;<br>see <a href="#">Figure 11</a> | - | 127 | 150 | m $\Omega$ |
|--------------|----------------------------------|--|---|-----|-----|------------|

Table 1. Quick reference data ...continued

| Symbol                         | Parameter                                    | Conditions  | Min | Typ | Max | Unit |
|--------------------------------|--|---|-----|-----|-----|------|
| <b>Avalanche ruggedness</b>    |  |   |     |     |     |      |
| $E_{DS(AL)S}$                  | non-repetitive drain-source avalanche energy | $I_D = 11\text{ A}$ ; $V_{sup} \leq 55\text{ V}$ ;<br>$R_{GS} = 50\ \Omega$ ; $V_{GS} = 10\text{ V}$ ;<br>$T_{j(\text{init})} = 25\text{ }^\circ\text{C}$ ; unclamped | -   | -   | 16  | mJ   |
| <b>Dynamic characteristics</b> |  |   |     |     |     |      |
| $Q_{GD}$                       | gate-drain charge                            | $V_{GS} = 10\text{ V}$ ; $I_D = 3\text{ A}$ ;<br>$V_{DS} = 44\text{ V}$ ; $T_j = 25\text{ }^\circ\text{C}$ ;<br>see <a href="#">Figure 12</a>                         | -   | 2.7 | -   | nC   |

## 2. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description                       | Simplified outline   | Graphic symbol   |
|-----|--------|-----------------------------------|--|--|
| 1   | G      | gate                              |  |  |
| 2   | D      | drain <sup>[1]</sup>              |  |  |
| 3   | S      | source                            |  |  |
| mb  | D      | mounting base; connected to drain |  |  |

**SOT428 (DPAK)**

[1] It is not possible to make a connection to pin 2.

## 3. Ordering information

Table 3. Ordering information

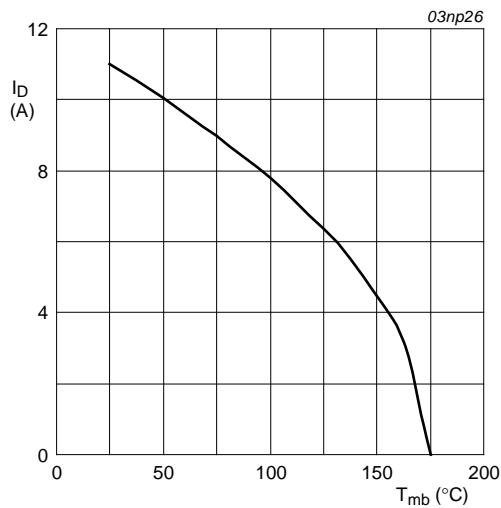
| Type number  | Package |   | Version |
|--------------|---------|---|---------|
|              | Name    | Description   |         |
| BUK72150-55A | DPAK    | plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped) | SOT428  |

## 4. Limiting values

**Table 4. Limiting values**

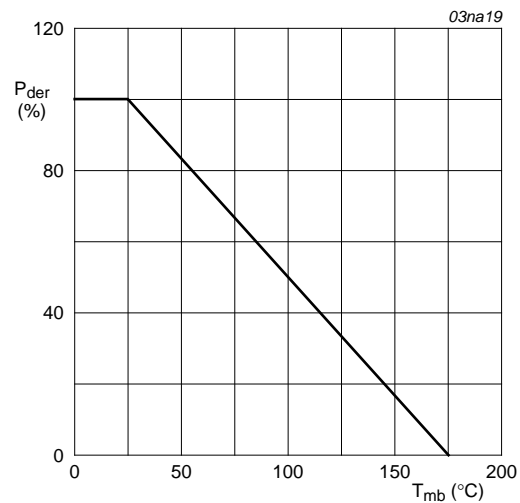
In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol                      | Parameter                                    | Conditions   | Min | Max | Unit |
|-----------------------------|--|--|-----|-----|------|
| $V_{DS}$                    | drain-source voltage                         | $T_j \geq 25\text{ °C}; T_j \leq 175\text{ °C}$  | -   | 55  | V    |
| $V_{DGR}$                   | drain-gate voltage                           | $R_{GS} = 20\text{ k}\Omega$   | -   | 55  | V    |
| $V_{GS}$                    | gate-source voltage                          |  | -20 | 20  | V    |
| $I_D$                       | drain current                                | $T_{mb} = 25\text{ °C}; V_{GS} = 10\text{ V};$ see <a href="#">Figure 3</a> ;<br>see <a href="#">Figure 1</a>                              | -   | 11  | A    |
|                             |  | $T_{mb} = 100\text{ °C}; V_{GS} = 10\text{ V};$ see <a href="#">Figure 1</a>   | -   | 7   | A    |
| $I_{DM}$                    | peak drain current                           | $T_{mb} = 25\text{ °C};$ pulsed; $t_p \leq 10\text{ }\mu\text{s};$<br>see <a href="#">Figure 3</a>   | -   | 44  | A    |
| $P_{tot}$                   | total power dissipation                      | $T_{mb} = 25\text{ °C};$ see <a href="#">Figure 2</a>  | -   | 36  | W    |
| $T_{stg}$                   | storage temperature                          |  | -55 | 175 | °C   |
| $T_j$                       | junction temperature                         |  | -55 | 175 | °C   |
| <b>Source-drain diode</b>   |  |  |     |     |      |
| $I_S$                       | source current                               | $T_{mb} = 25\text{ °C}$  | -   | 11  | A    |
| $I_{SM}$                    | peak source current                          | pulsed; $t_p \leq 10\text{ }\mu\text{s}; T_{mb} = 25\text{ °C}$  | -   | 44  | A    |
| <b>Avalanche ruggedness</b> |  |  |     |     |      |
| $E_{DS(AL)S}$               | non-repetitive drain-source avalanche energy | $I_D = 11\text{ A}; V_{sup} \leq 55\text{ V}; R_{GS} = 50\text{ }\Omega;$<br>$V_{GS} = 10\text{ V}; T_{j(init)} = 25\text{ °C};$ unclamped | -   | 16  | mJ   |



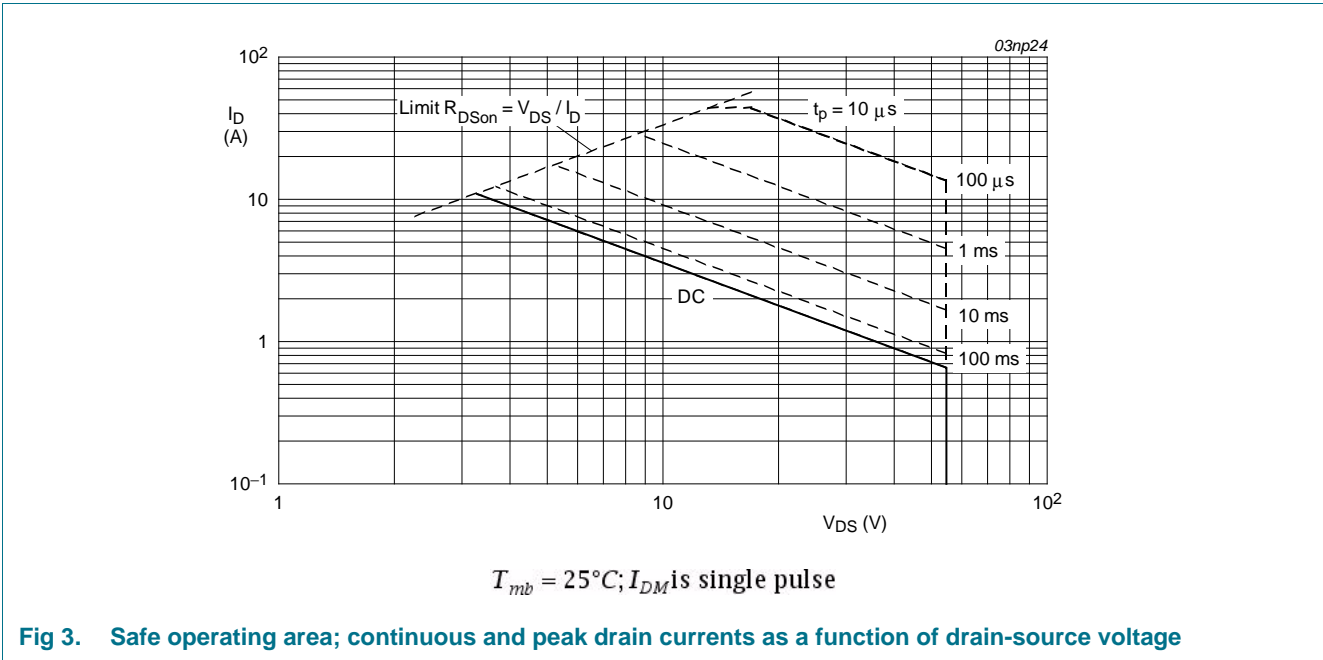
$$V_{GS} \geq 10V$$

**Fig 1. Normalized continuous drain current as a function of mounting base temperature**



$$P_{der} = \frac{P_{tot}}{P_{tot(25\text{ °C})}} \times 100\%$$

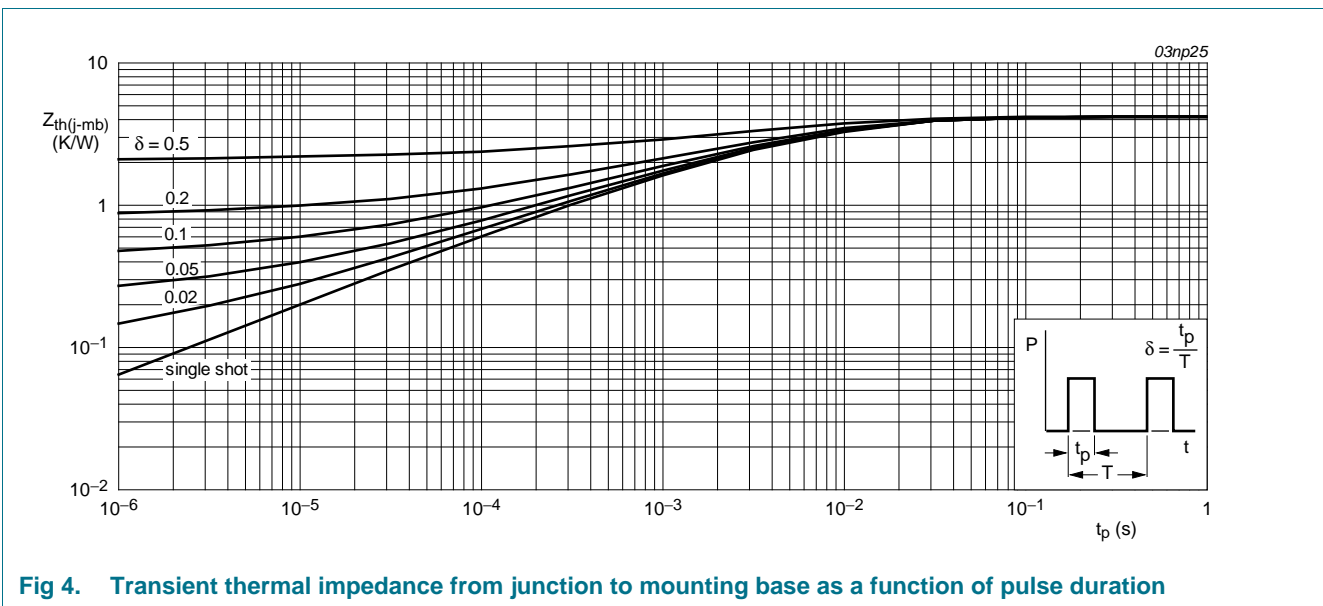
**Fig 2. Normalized total power dissipation as a function of mounting base temperature**



## 5. Thermal characteristics

**Table 5. Thermal characteristics**

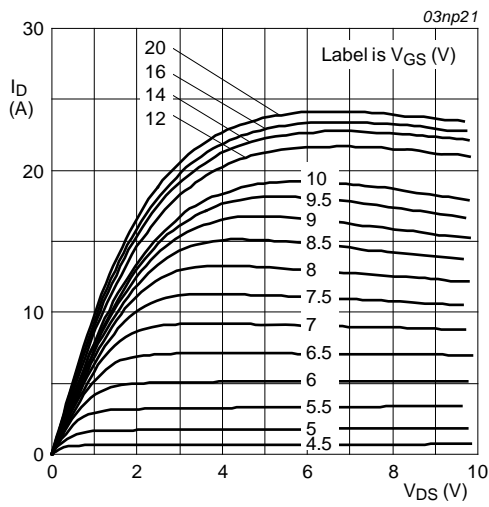
| Symbol         | Parameter   | Conditions                   | Min | Typ | Max | Unit |
|----------------|---|------------------------------|-----|-----|-----|------|
| $R_{th(j-mb)}$ | thermal resistance from junction to mounting base | see <a href="#">Figure 4</a> | -   | -   | 4.1 | K/W  |
| $R_{th(j-a)}$  | thermal resistance from junction to ambient       |                              | -   | 71  | -   | K/W  |



## 6. Characteristics

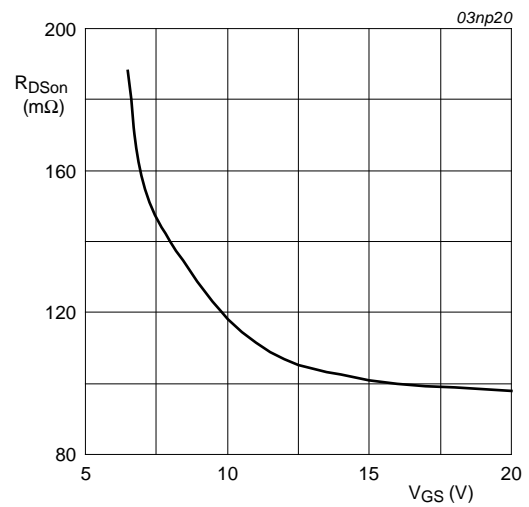
Table 6. Characteristics

| Symbol                         | Parameter                        | Conditions  | Min | Typ  | Max | Unit          |
|--------------------------------|----------------------------------|---|-----|------|-----|---------------|
| <b>Static characteristics</b>  |                                  |   |     |      |     |               |
| $V_{(BR)DSS}$                  | drain-source breakdown voltage   | $I_D = 0.25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = -55 \text{ }^\circ\text{C}$  | 50  | -    | -   | V             |
|                                |                                  | $I_D = 0.25 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$   | 55  | -    | -   | V             |
| $V_{GS(th)}$                   | gate-source threshold voltage    | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25 \text{ }^\circ\text{C};$<br>see <a href="#">Figure 9</a>   | 2   | 3    | 4   | V             |
|                                |                                  | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175 \text{ }^\circ\text{C};$<br>see <a href="#">Figure 9</a>  | 1   | -    | -   | V             |
|                                |                                  | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55 \text{ }^\circ\text{C};$<br>see <a href="#">Figure 9</a>  | -   | -    | 4.4 | V             |
| $I_{DSS}$                      | drain leakage current            | $V_{DS} = 55 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ }^\circ\text{C}$   | -   | -    | 500 | $\mu\text{A}$ |
|                                |                                  | $V_{DS} = 55 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$  | -   | 0.05 | 10  | $\mu\text{A}$ |
| $I_{GSS}$                      | gate leakage current             | $V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$  | -   | 2    | 100 | nA            |
|                                |                                  | $V_{GS} = -20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$   | -   | 2    | 100 | nA            |
| $R_{DS(on)}$                   | drain-source on-state resistance | $V_{GS} = 10 \text{ V}; I_D = 5 \text{ A}; T_j = 175 \text{ }^\circ\text{C};$<br>see <a href="#">Figure 10</a> ; see <a href="#">Figure 11</a>    | -   | -    | 300 | m $\Omega$    |
|                                |                                  | $V_{GS} = 10 \text{ V}; I_D = 5 \text{ A}; T_j = 25 \text{ }^\circ\text{C};$<br>see <a href="#">Figure 10</a> ; see <a href="#">Figure 11</a>     | -   | 127  | 150 | m $\Omega$    |
| <b>Dynamic characteristics</b> |                                  |   |     |      |     |               |
| $Q_{G(tot)}$                   | total gate charge                | $I_D = 3 \text{ A}; V_{DS} = 44 \text{ V}; V_{GS} = 10 \text{ V};$<br>$T_j = 25 \text{ }^\circ\text{C};$ see <a href="#">Figure 12</a>            | -   | 5.5  | -   | nC            |
| $Q_{GS}$                       | gate-source charge               |   | -   | 1    | -   | nC            |
| $Q_{GD}$                       | gate-drain charge                |   | -   | 2.7  | -   | nC            |
| $C_{iss}$                      | input capacitance                | $V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz};$<br>$T_j = 25 \text{ }^\circ\text{C};$ see <a href="#">Figure 13</a>             | -   | 242  | 322 | pF            |
| $C_{oss}$                      | output capacitance               |   | -   | 40   | 48  | pF            |
| $C_{rss}$                      | reverse transfer capacitance     |   | -   | 25   | 35  | pF            |
| $t_{d(on)}$                    | turn-on delay time               | $V_{DS} = 25 \text{ V}; R_L = 2.7 \text{ }^\Omega; V_{GS} = 10 \text{ V};$<br>$R_{G(ext)} = 5.6 \text{ }^\Omega; T_j = 25 \text{ }^\circ\text{C}$ | -   | 3    | -   | ns            |
| $t_r$                          | rise time                        |   | -   | 26   | -   | ns            |
| $t_{d(off)}$                   | turn-off delay time              |   | -   | 8    | -   | ns            |
| $t_f$                          | fall time                        | $V_{DS} = 25 \text{ V}; R_L = 2.7 \text{ }^\Omega; V_{GS} = 10 \text{ V};$<br>$R_{G(ext)} = 5.6 \text{ }^\Omega; T_j = 25 \text{ }^\circ\text{C}$ | -   | 10   | -   | ns            |
| $L_D$                          | internal drain inductance        | measured from drain to center of die ;<br>$T_j = 25 \text{ }^\circ\text{C}$   | -   | 2.5  | -   | nH            |
| $L_S$                          | internal source inductance       | measured from source lead to source bond pad ;<br>$T_j = 25 \text{ }^\circ\text{C}$   | -   | 7.5  | -   | nH            |
| <b>Source-drain diode</b>      |                                  |   |     |      |     |               |
| $V_{SD}$                       | source-drain voltage             | $I_S = 10 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ }^\circ\text{C};$<br>see <a href="#">Figure 14</a>                                     | -   | 1.25 | 1.5 | V             |
| $t_{rr}$                       | reverse recovery time            | $I_S = 10 \text{ A}; dI_S/dt = -100 \text{ A}/\mu\text{s};$<br>$V_{GS} = -10 \text{ V}; V_{DS} = 30 \text{ V}; T_j = 25 \text{ }^\circ\text{C}$   | -   | 32   | -   | ns            |
| $Q_r$                          | recovered charge                 |   | -   | 50   | -   | nC            |



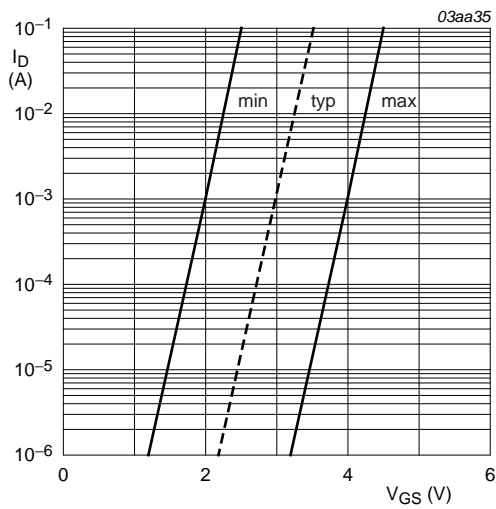
$T_j = 25^\circ\text{C}; t_p = 300\mu\text{s}$

**Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values**



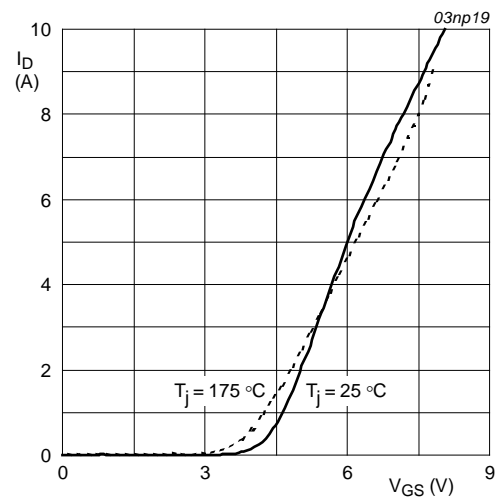
$T_j = 25^\circ\text{C}; I_D = 5\text{A}$

**Fig 6. Drain-source on-state resistance as a function of gate-source voltage; typical values**



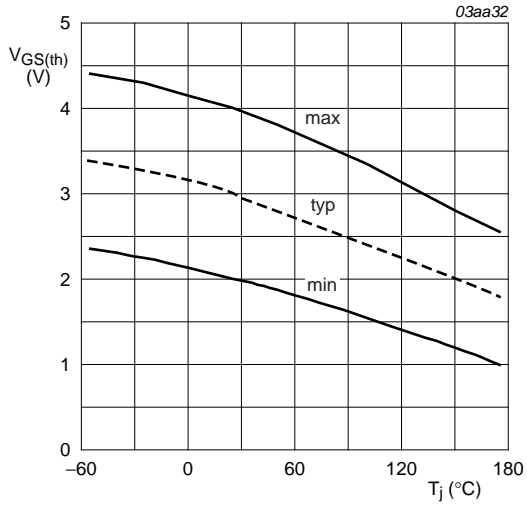
$T_j = 25^\circ\text{C}; V_{DS} = 5\text{V}$

**Fig 7. Sub-threshold drain current as a function of gate-source voltage**



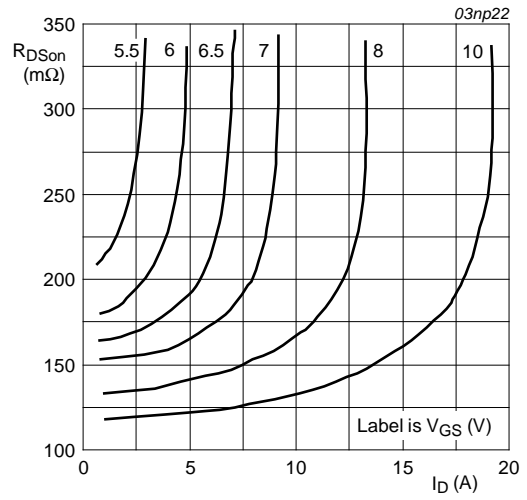
$V_{DS} = 25\text{V}$

**Fig 8. Transfer characteristics: drain current as a function of gate-source voltage; typical values**



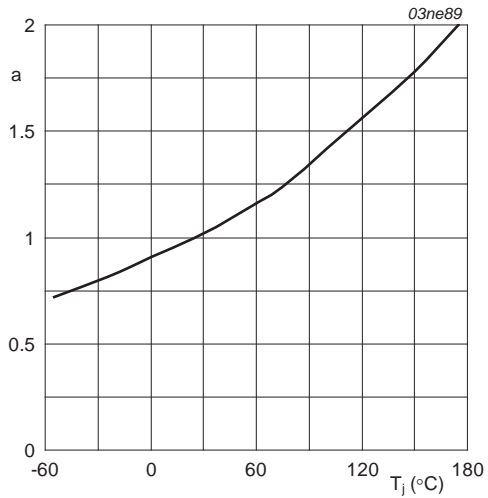
$$I_D = 1mA; V_{DS} = V_{GS}$$

**Fig 9. Gate-source threshold voltage as a function of junction temperature**



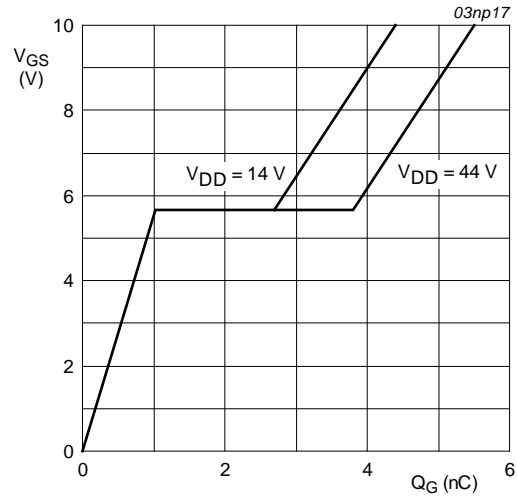
$$T_j = 25^\circ C; t_p = 300\mu s$$

**Fig 10. Drain-source on-state resistance as a function of drain current; typical values**



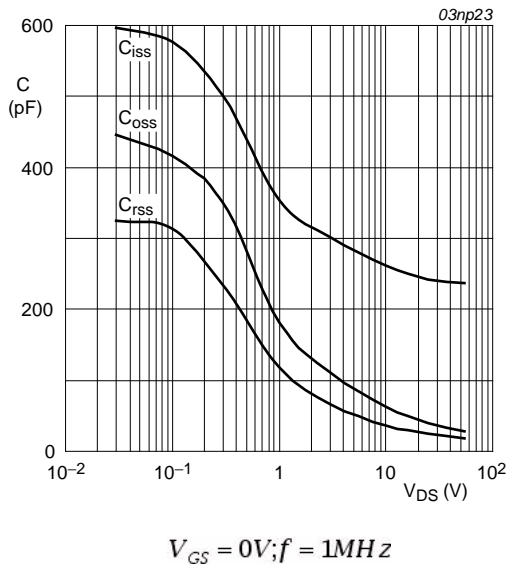
$$a = \frac{R_{DS(on)}}{R_{DS(on)25^\circ C}}$$

**Fig 11. Normalized drain-source on-state resistance factor as a function of junction temperature**

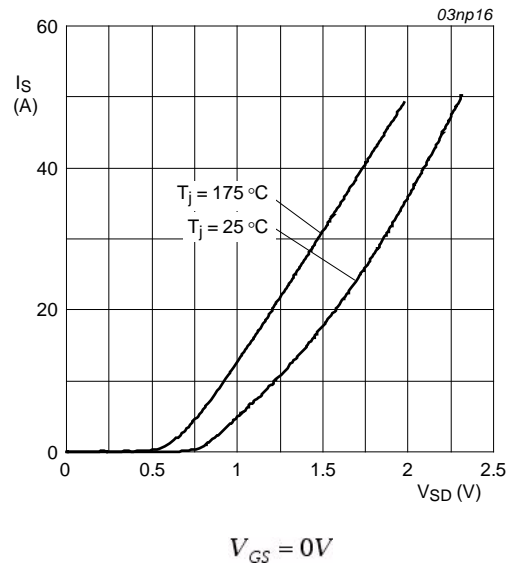


$$T_j = 25^\circ C; I_D = 3A$$

**Fig 12. Gate-source voltage as a function of gate charge; typical values**



**Fig 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values**



**Fig 14. Source current as a function of source-drain voltage; typical values**



**7. Package outline**

Plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)

SOT428

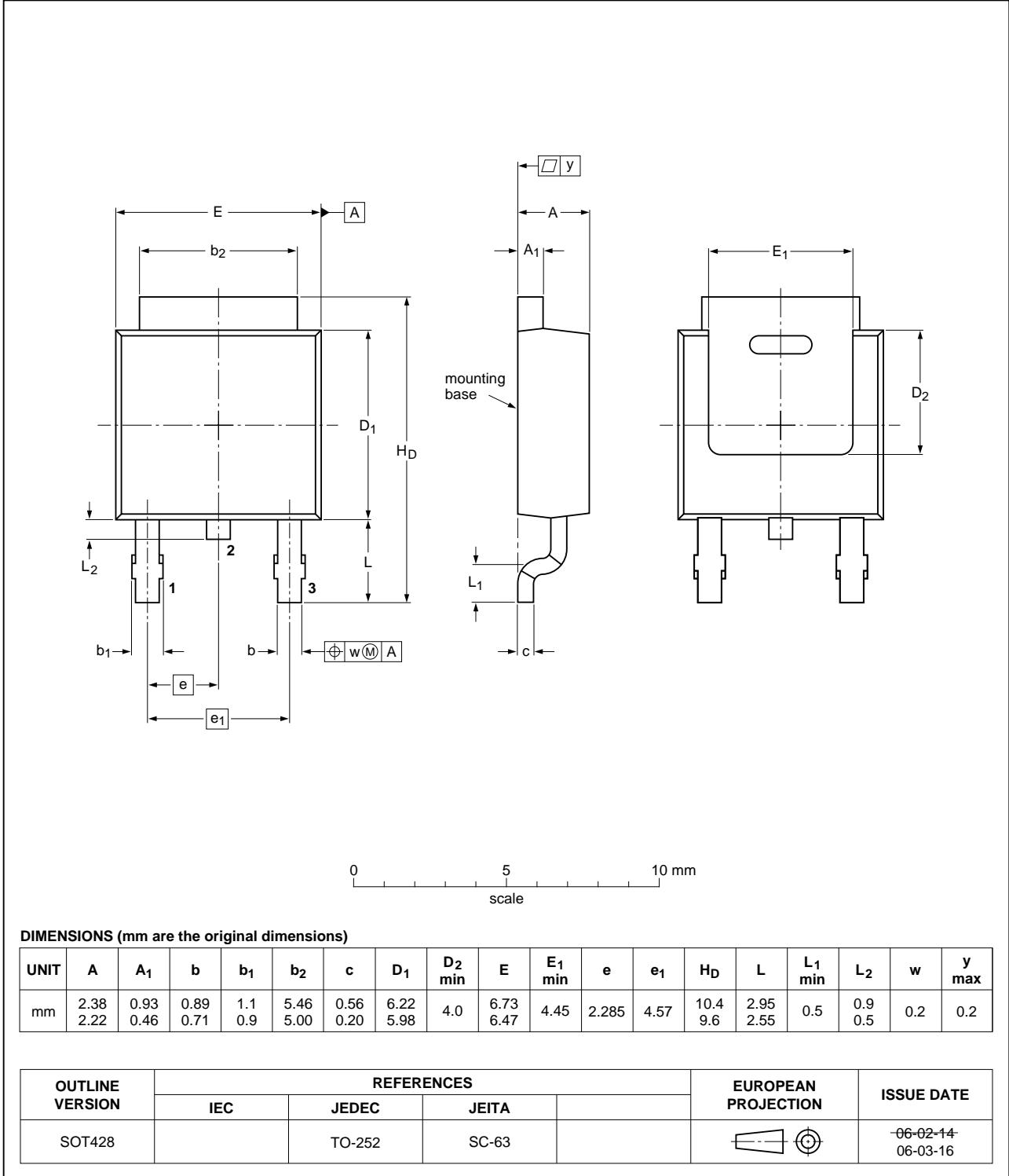


Fig 15. Package outline SOT428 (DPAK)

## 8. Revision history

Table 7. Revision history

| Document ID                          | Release date   | Data sheet status  | Change notice | Supersedes       |
|--------------------------------------|--|--------------------|---------------|------------------|
| BUK72150-55A v.3                     | 20110126   | Product data sheet | -             | BUK72150-55A v.2 |
| Modifications:                       | <ul style="list-style-type: none"><li>• The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors.</li><li>• Legal texts have been adapted to the new company name where appropriate.</li><li>• Various changes to content.</li></ul> |                    |               |                  |
| BUK72150-55A v.2<br>(9397 750 12335) | 20031120   | Product data sheet | -             | -                |

## 9. Legal information

### 9.1 Data sheet status

| Document status <sup>[1][2]</sup> | Product status <sup>[3]</sup> | Definition  |
|-----------------------------------|-------------------------------|---|
| Objective [short] data sheet      | Development                   | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet    | Qualification                 | This document contains data from the preliminary specification.                       |
| Product [short] data sheet        | Production                    | This document contains the product specification.                                     |

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nexperia.com>.

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