

60 V, 320 mA dual N-channel Trench MOSFET

23 November 2020

Product data sheet

1. General description

Dual N-channel enhancement mode Field-Effect Transistor (FET) in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Logic-level compatible
- · Very fast switching
- Trench MOSFET technology
- AEC-Q101 qualified

3. Applications

- Relay driver
- High-speed line driver
- Low-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Table 1. Quick	reference uala						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor							
V _{DS}	drain-source voltage	T _{amb} = 25 °C		-	-	60	V
V _{GS}	gate-source voltage			-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	-	320	mA
Static charact	eristics (per transistor)						
R _{DSon}	drain-source on-state resistance	V_{GS} = 10 V; I _D = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _j = 25 °C		-	1	1.6	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².



5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	S1	source1	□6 □5 □4	D ₁ D ₂
2	G1	gate1		
3	D2	drain2	0	
4	S2	source2		
5	G2	gate2	TSSOP6 (SOT363)	S_1 G_1 S_2 G_2
6	D1	drain1		S1 G1 S2 G2 msd901

6. Ordering information

Table 3. Ordering information							
Type number Package							
	Name	Description	Version				
2N7002PS	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363				

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
2N7002PS	M8%

[1] % = placeholder for manufacturing site code

2N7002PS

8. Limiting values

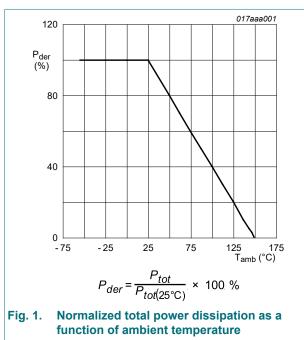
Table 5. Limiting values

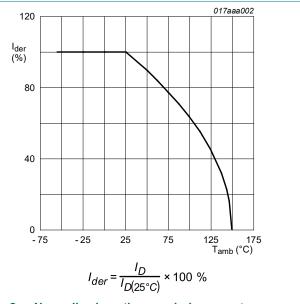
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transiste	or	-				
V _{DS}	drain-source voltage	T _{amb} = 25 °C		-	60	V
V _{GS}	gate-source voltage			-20	20	V
ID	drain current	V _{GS} = 10 V; T _{amb} = 25 °C	[1]	-	320	mA
		V _{GS} = 10 V; T _{amb} = 100 °C	[1]	-	240	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	1.2	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	280	mW
			[1]	-	320	mW
		T _{sp} = 25 °C		-	990	mW
Per device	I					
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	420	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drai	n diode					
I _S	source current	T _{amb} = 25 °C	[1]	-	320	mA

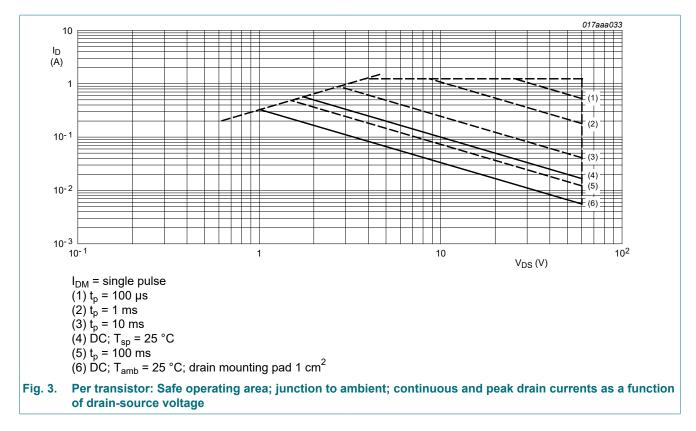
[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.









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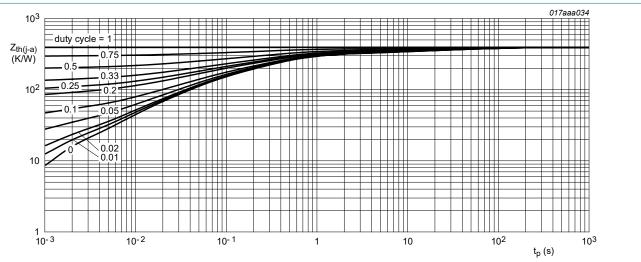
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per device							
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	300	K/W
Per transistor							
R _{th(j-a)}	R _{th(j-a)} thermal resistance from junction to ambient	in free air	[1]	-	390	445	K/W
		Ī	[2]	-	340	390	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	130	K/W

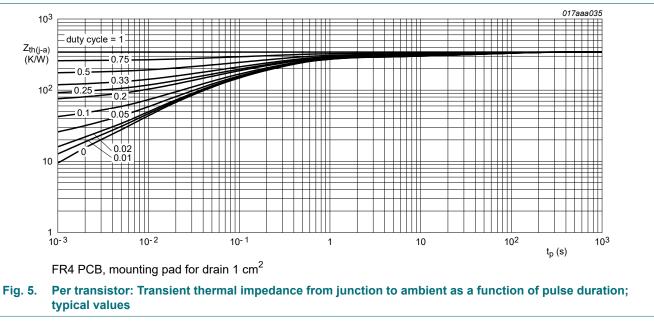
[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².



FR4 PCB, standard footprint





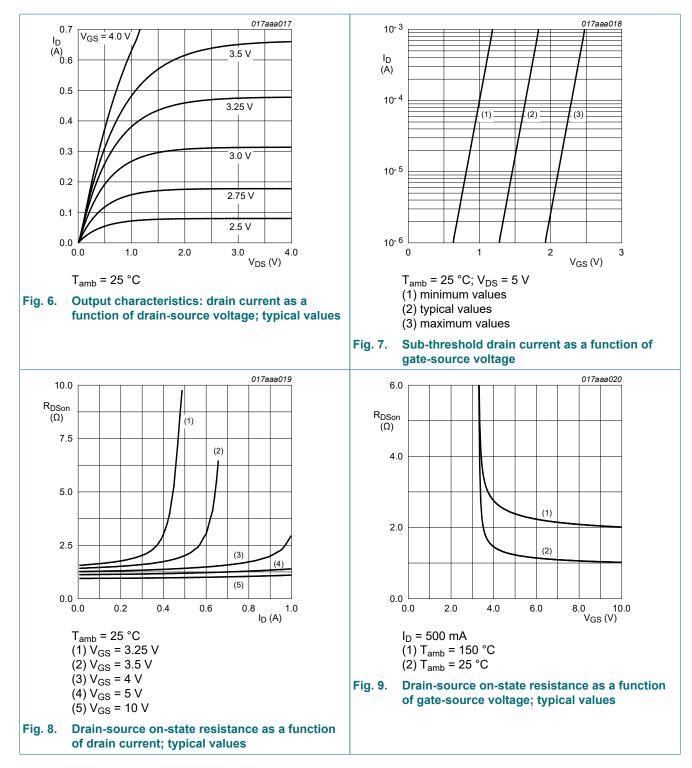
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10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics (per transistor)					
V _{(BR)DSS}	drain-source breakdown voltage	I _D = 10 μA; V _{GS} = 0 V; T _j = 25 °C	60	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = 250 μA; V _{DS} =V _{GS} ; T _j = 25 °C	1.1	1.75	2.4	V
I _{DSS}	drain leakage current	$V_{DS} = 60 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	1	μA
		V _{DS} = 60 V; V _{GS} = 0 V; T _j = 150 °C	-	-	10	μA
I _{GSS}	gate leakage current	V _{GS} = 20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	100	nA
		V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-100	nA
R _{DSon} drain-source on-state resistance	V_{GS} = 5 V; I _D = 50 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _j = 25 °C	-	1.3	2	Ω	
		V_{GS} = 10 V; I _D = 500 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _j = 25 °C	-	1	1.6	Ω
9 _{fs}	forward transconductance	V_{DS} = 10 V; I _D = 200 mA; pulsed; t _p ≤ 300 μs; δ ≤ 0.01; T _j = 25 °C	-	400	-	mS
Dynamic ch	aracteristics (per transist	or)				
Q _{G(tot)}	total gate charge	V _{DS} = 30 V; I _D = 300 mA; V _{GS} = 4.5 V;	-	0.6	0.8	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.2	-	nC
Q _{GD}	gate-drain charge		-	0.2	-	nC
C _{iss}	input capacitance	V _{DS} = 10 V; f = 1 MHz; V _{GS} = 0 V;	-	30	50	pF
C _{oss}	output capacitance	T _j = 25 °C	-	7	-	pF
C _{rss}	reverse transfer capacitance		-	4	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 50 V; R _L = 250 Ω; V _{GS} = 10 V;	-	3	6	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	4	-	ns
t _{d(off)}	turn-off delay time	1	-	10	20	ns
t _f	fall time		-	5	-	ns
Source-drai	n diode (per transistor)					
V _{SD}	source-drain voltage	I _S = 115 mA; V _{GS} = 0 V; T _i = 25 °C	0.47	0.75	1.1	V

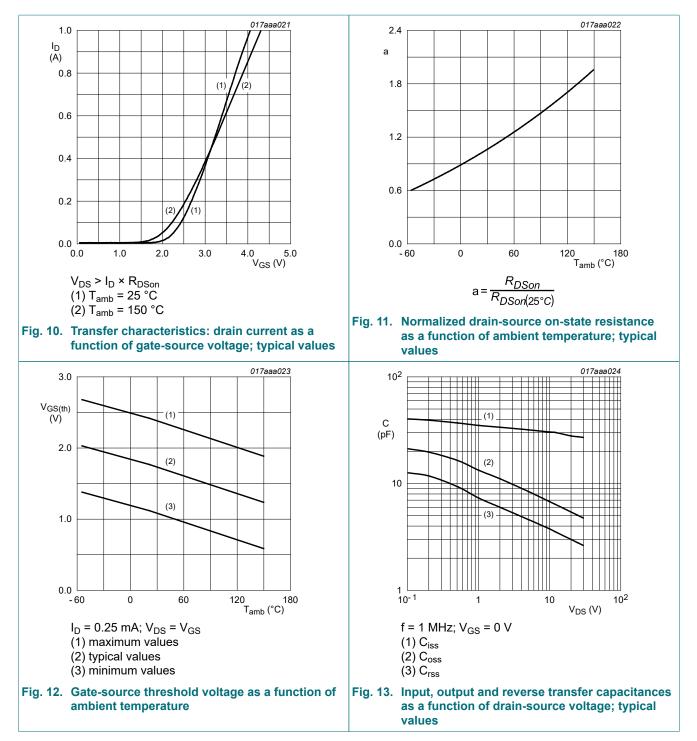
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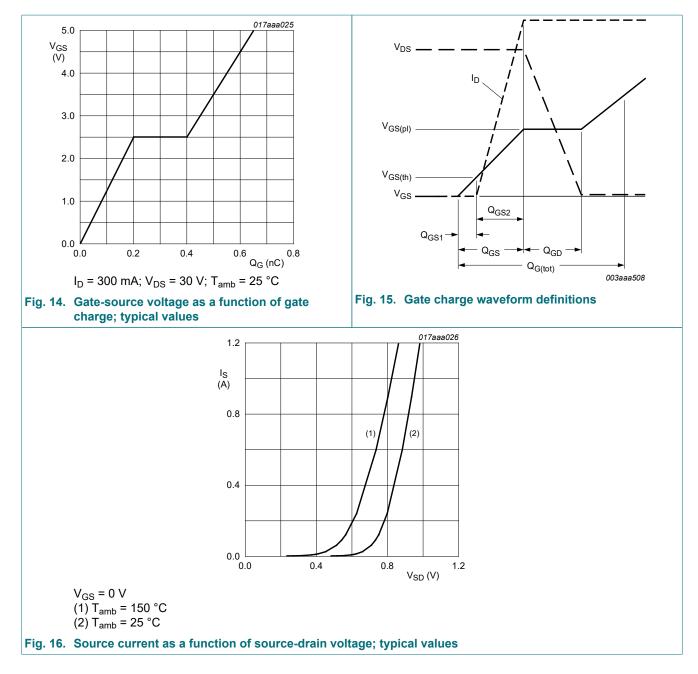


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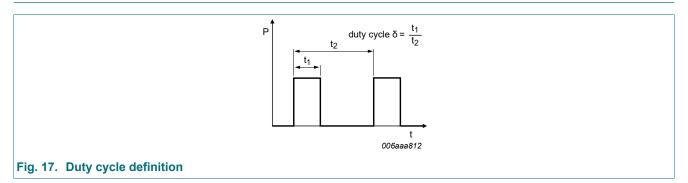


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11. Test information

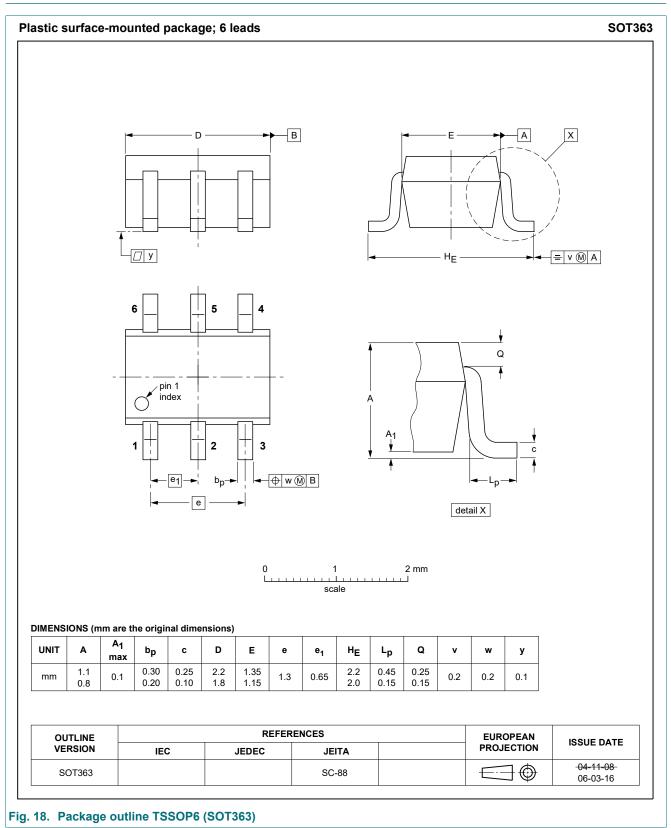


Quality information

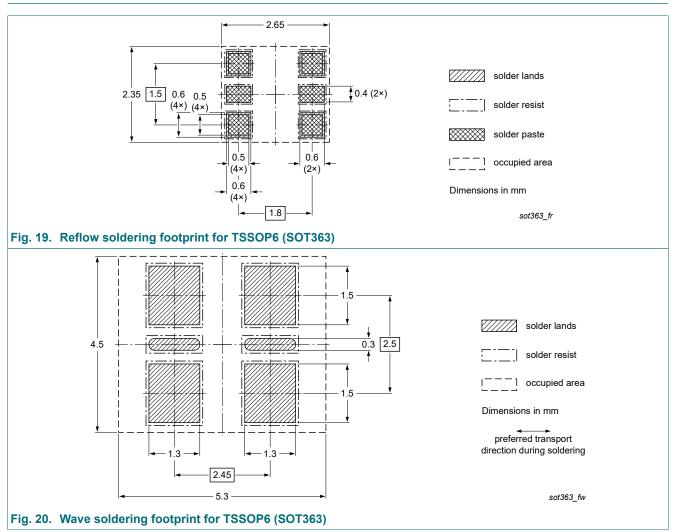
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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12. Package outline



13. Soldering



14. Revision history

Table 8. Revision h	nistory							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
2N7002PS v.2	20201123	Product data sheet	-	2N7002PS v.1				
Modifications:	Nexperia. • Legal texts ha	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Chapter "Characteristics": Typo correction for I_{GSS} and switching times. 						
2N7002PS v.1	20100701	Product data sheet	-	-				

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or [1] completing a design.

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