

Features

- Shielded construction low radiation
- Bifilar wound
- Impedance range: 90 to 2200 ohms @ 100 MHz
- AEC-Q200 compliant
- RoHS compliant* and halogen free**

SRF3216A Series - Common Mode Chip Inductors

Electrical Specifications @ 25 °C

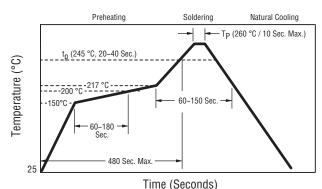
	Impedance @ 100 MHz / 1 V		DCR	DCR	
Bourns Part Number	Z (Ω)	Tolerance (%)	Typ.	Max. (Ω)	Irms (mA)
SRF3216A-900Y	90	± 25	0.15	0.3	400
SRF3216A-121Y	120	± 25	0.17	0.3	350
SRF3216A-161Y	160	± 25	0.18	0.4	350
SRF3216A-221Y	220	± 25	0.25	0.45	300
SRF3216A-261Y	260	± 25	0.35	0.5	300
SRF3216A-361Y	360	± 25	0.45	0.6	300
SRF3216A-601Y	600	± 25	0.60	0.8	300
SRF3216A-102Y	1000	± 25	0.65	1.0	230
SRF3216A-222Y	2200	± 25	1.10	1.2	200

Soldering Profile

Peak Temperature: 260 °C max.

Max. Peak Temperature 260 °C: 10 sec. max.

Max. Time Above 217 °C: 60~150 sec. max.



Reflow Times: 3 Times Max.

Schematic



Tolerance Code

WARNING Cancer and Reproductive Harm www.P65Warnings.ca.gov

- RoHS Directive 2015/863, Mar 31, 2015 and Annex.
- ** Bourns considers a product to be "halogen free" if (a) the Bromine (Br) content is 900 ppm or less; (b) the Chlorine (Cl) content is 900 ppm or less; and (c) the total Bromine (Br) and Chlorine (Cl) content is 1500 ppm or less. Specifications are subject to change without notice. Users should verify actual device performance in their specific applications. The products described herein and this document are subject to specific legal disclaimers as set forth on the last page of this document, and at www.bourns.com/docs/legal/disclaimer.pdf.

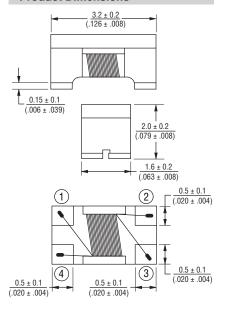
General Specifications

.....-55 °C to +125 °C Temperature Rise20 °C at rated Irms

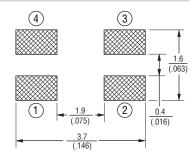
Materials

Core Ferrite
Wire Enameled copper
Terminal Finish Sn
Packaging 2000 pcs. per 7-inch reel

Product Dimensions

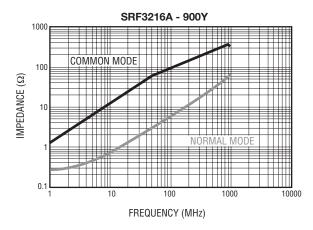


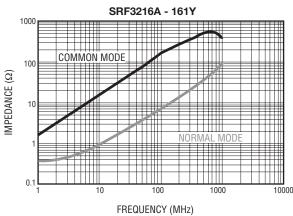
Recommended Layout

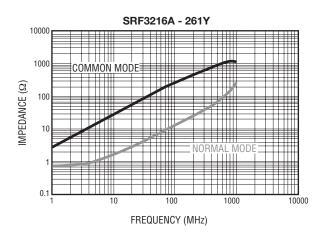


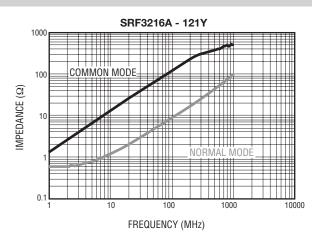
DIMENSIONS: $\frac{MM}{(INCHES)}$

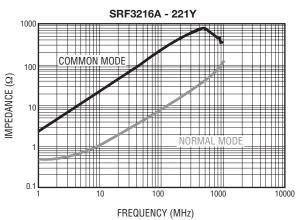
Typical Impedance vs. Frequency Curves

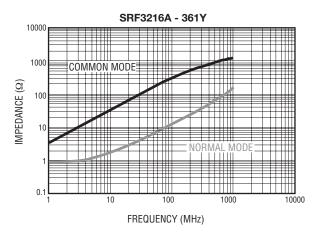








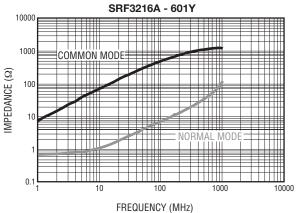


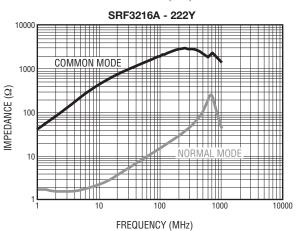


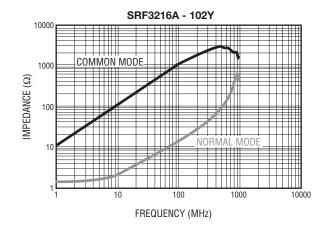
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Typical Impedance vs. Frequency Curves (Continued)



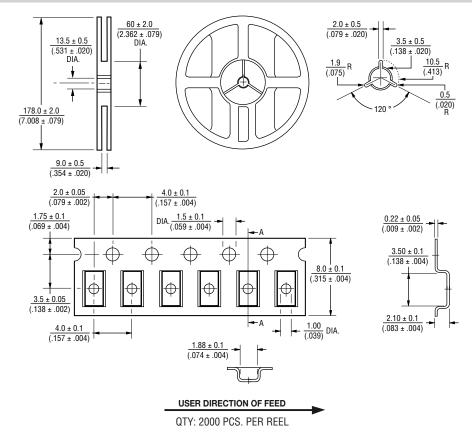




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Packaging Specifications



BOURNS®

Asia-Pacific: Tel: +886-2 2562-4117 • Email: asiacus@bourns.com

EMEA: Tel: +36 88 885 877 • Email: eurocus@bourns.com

The Americas: Tel: +1-951 781-5500 • Email: americus@bourns.com

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